FACULTY PROFILE

M. DEEPIKAKRISHNA

Department: Electronics and Communication Engineering
College: Gudlavalleru Engineering College
E-Mail: deepikakrishna5555@gmail.com

Address for Communication

College Address:

M. DeepikaKrishna
Assistant Professor,
ECE Department,
Gudlavalleru Engineering College,
Gudlavalleru, Krishna District,
Andhra Pradesh: 521356

Permanent Address:

M. DeepikaKrishna
 D.No: 7-164/3,
Near Ramalayam,Annavaram,
Mudinepalli mandal and post
 Krishna District
Andhra Pradesh: 521325

Academic Qualifications

S. No	Name of the Degree	University	Percentage of Marks/Grade	Specialization	Year of Pass
1	M.Tech	JNTUK	82.86	VLSI &EMBEDDED SYSTEMS	2019
2	B.Tech	JNTUK	79.97	E.C.E	2017
3	Intermediate	Board of Intermediate Education	93.7	M.P.C	2013
4	SSC	Board of Secondary Education	89	-	2011

Professional Experience

S. No.	Designation	Institution Name	Working Period	
			From	**To**
1	Assistant Professor	Gudlavalleru Engineering College	21-11-2019	Till date

Professional Body Membership

S. No.	Name of the Professional body	Membership Number
1	International Association of Engineers (IAENG)	391799

Papers Published in Journals No. : 03

1) **M.DeepikaKrishna**, "Efficient Implementation of FIR Filter Utilizing Dadda Multiplier and Brent Kung Adder", Journal of Controllers and Converters, MAT Journals,vol-9,Issue 2(May-August 2024) ,PP: (8-16)
2) **M.DeepikaKrishna**,"A High Speed Floating Point Multiply Accumulator Based on FPGAs", International Journal of Research,VolumeXII,Issue1V,April-2023, ISSN 2236-6124
3) **M.Deepikakrishna**, "Fault Tolerance Design Techniques For Parallel Matrix Vector Multiplications" International Journal of Modern Electronics and Communication Engineering , Volume 7, Issue 5, September-2019; ISSN 2321-2152

Papers Published in Conferences No. : 03

1) **DeepikaKrishna Muppidi**, Venkata Satyanarayana Bokka, Vasavi Sai Lakshmi Vyshnavi Bodinapalli, Lavanya Amballa "Duty Cycle Corrector For Wide Range Applications" National Conference on VLSI Design, Signal Processing, Image Processing ,Communications and Embedded Systems, NCVSPICE2K18 on 8th & 9th July 2018 at JNTUK, KAKINADA.

2) Venkata Satyanarayana Bokka, **DeepikaKrishna Muppidi,** Vasavi Sai Lakshmi Vyshnavi Bodinapalli, Sai Dinesh Ponnada "Design of 8X3 Encoder with Low Quantum Cost and Delay" National Conference on VLSI Design, Signal Processing, Image Processing ,Communications and Embedded Systems, NCVSPICE2K18 on 8th & 9th July 2018 at JNTUK, KAKINADA.

3) DeepikaKrishna Muppidi, Sai Dinesh Ponnada, Venkata Satyanarayana Bokka, VS L Vyshnavi Bodinapalli,"Design of 5 X32 Decoder Using Reversible Logic." National Conference on VLSI Design, Signal Processing, Image Processing, Communications and Embedded Systems, NCVSPICE2K18 on 8th & 9th July 2018 at JNTUK, KAKINADA.

Workshops Organized No. : NIL

Workshops /Conferences /Seminars Attended No. : 01

1) Attended the two day workshop on "PCB DESIGNING" at Dhanekula Institute of Engineering and Technology , Vijayawada, organized by QUE Technologies during 17-18 November 2014.

Certifications/Training Programs Attended No. : 06

1) Participated in One Week Refresher Course on "Transmission Lines and Waveguides" organized by Department of Electronics and Communication Engineering, Seshadri Rao Gudlavalleru Engineering College during 29th January – 2 nd February 2024.
2) Participated in One Week Refresher Course on "Microcontrollers and Embedded Systems" organized by Department of Electronics and Communication Engineering, , Seshadri Rao Gudlavalleru Engineering College during 9 th January – 13th January 2024.
3) One Week Faculty Development Program on Data Acquisition and RT using LabVIEW organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru in association with National Instruments, Bangalore from 25th to 30th November 2019.
4) Attended a one week FDP on "Improving Teaching Skills In Electronic Devices and Recent Trends in Communication Engineering (Lab VIEW, FEKO, XILINX)" during 04 to 09th February 2019 at University College of Engineering Kakinada, JNTUK.
5) Attended one week Training Program of "Software Project Development through Python" during 6 to 11th July 2018 at University College of Engineering Kakinada, JNTUK.
6) Attended a one week FDP on "Improving Teaching Skills In Electronic Devices and Circuits" during 27 to 31st March 2018 at University College of Engineering Kakinada, JNTUK.

Online FDP Attended No.:18

1. Participated in One Week Faculty Development Program on "Applications of Computer Vision using Deep Learning" conducted by the department of Electronics and Communication Engineering, Seshadri Rao Gudlavalleru Engineering College during 20 - 25 February 2023.

2. Attended a Faculty Development Programme on "Insights of NEP 2020 and Strategies for its Implementation" organized by Academic Strengthening and Advancement Cell, Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru on 12 June 2022.

3. Successfully participated in one week national level online faculty development program on "Signals, Systems and Transform Techniques" organized by the Department of Electrical & Electronics Engineering Maturi Venkata Subba Rao (MVSR) Engineering College (Autonomous), Hyderabad during 22nd - 27th November 2021.

4. Faculty Development Program on Intellectual Property Rights for Academicians organized by Department of Business Administration , Jhulelal Institute of technology in association with Rajiv Gandhi Institute of Intellectual Property Management on 5th June 2021.

5. AICTE Training and Learning (ATAL) Academy Online FDP on Internet of Things organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru from 7th to 11th December 2020.

6. AICTE - ISTE Sponsored One Week Online Induction Program on Millimeter Wave Frequencies organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru on 16th to 21st November 2020.

7. Online Faculty Development Program on Universal Human Values in the context of Student Induction Program organized by National Institute of Technology Warangal from 2nd to 7th November 2020.

8. AICTE Sponsored Online Short term Training Program on Block Chain Architecture Design and Use Cases, (Smart Contracts with Ethereum Solidity) Phase-III organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru from 24th to 29th August 2020.

9. AICTE Sponsored Online Short term Training Program on Block Chain Architecture Design and Use Cases, (Decentralized Network Synchronization) Phase-II organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru from 17th to 22nd August 2020

10. One Week Refresher Course on Embedded System Design organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru from 27th July to 1st August 2020

11. A National Level Online Faculty Development Program on Cyber Security organized by Departments of Computer Science and Engineering & Information Technology in association with CyberPsy, Gudlavalleru Engineering College, Gudlavalleru from 22nd to 26th July 2020.

12. One Week International Online Knowledge Development Program on Challenges and Advancements in the Design of IoT, Embedded and VLSI Systems: A Researchers View organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru from 8th to 13th June 2020.

13. Faculty Development Program on Recent Trends and Future Applications in Electronics and Communication Technologies organized by MLR Institute of Technology, Hyderabad from 29th May to 2nd June 2020.

14. Online Faculty Development Program on Embedded Systems organized by APSSDC from 25th May to 6th June 2020.

15. Five Day Online Faculty Development Program on Advanced Python Programming using Django organized by Department of Computer Science and Engineering in

association with Data Point Solutions by Malineni Lakshmaiah Women's Engineering college, Guntur from 21st to 25th May 2020.

16. Two Day Online FDP on Microstrip Antennas For Advanced Wireless Communications organized by Department of Electronics and Communication Engineering,in association with IETE Vijayawada Division by Kallam Haranadhareddy Institute of Technology, Guntur from 15th to 16th May 2020.

17. One Week Online Faculty Development Program on Advanced Antenna Design using HFSS organized by Department of Electronics and Communication Engineering, Santhiram Engineering College, Nandyal in collaboration with Vardhaman College of Engineering, Hyderabad from 12th to 17th May 2020

18. Attended faculty development programme on Digital Image Processing using MATLAB from 24th to 27th April 2020 hosted on skill to hire.

Online Webinars Attended No.:10

1. Xilinx Webinar on Embedded System Design Flow on Zynq using Xilinx Vivado and Vitis organized by CoreEL Technologies and Xilinx on 10th June 2021.

2. Successfully completed 20 days webinar series on AI master class organized by the Andhra Pradesh State Skill Development Corporation and Pantech Prolabs India Pvt Ltd during 05th - 24th October 2020.

3. Three Days National Level Webinar Series on VLSI organized by Department of Electronics and Communication Engineering, Andhra Loyala Institute of Engineering and Technology in collaboration with Apply Volt from 10th to 12th June 2020.

4. Webinar on Impact of Covid in Engineering Education and way Forward organized by ISTE chapter, Gudlavalleru Engineering College, Gudlavalleru on 9th June 2020.

5. Webinar on Writing an Effective Research Paper: Means and Methods organized by Department of Computer Science and Engineering , CMR Engineering College on 29th May 2020.

6. Webinar on Radio Frequency Measurements For Cellular and Wireless Communication Systems organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru on 25th May 2020.

7. Technical Webinar on Design Approaches for Frontend VLSI Design Organized by Department of Electronics and Communication Engineering, Vaagdevi College of Engineering on 23rd May 2020.

8. Xilinx Webinar on Custom IP Design and Validation using Vivado organized by CoreEL Technologies and Xilinx on 22nd May 2020.

9. IT Live Webinar on How to Prepare a Good Research Grant Proposal organized by Department of Information Technology, PSNA College of Engineering and Technology on 20th May 2020.

10. Mentor Webinar on Analog IC Design using Mentor EDA Tools organized by CoreEL Technologies and Mentor on 10th April 2020.

OnlineWorkshops Attended No.:01

1) Attended workshop on "VLSI circuits Analog and Digital Design perspectives", organized by department of ECE, Jntua College of anantapuramu in association with AMS Semiconductors and HCL Technologies during 16th May to 17th May 2020.

NPTEL Certifications: 05
1. Digital System Design certification with (Elite + Gold) during April 2023

2. System Design Through Verilog Certification with (Elite+ Gold) during September 2021

3. Digital Circuits Certification with (Elite + Silver) during December 2020

4. Digital VLSI Testing with a Score of 62% during December 2020

5. Fundamentals of Semiconductor Devices with a Score of 65% during April 2020

Other Certifications: 02
1. Completed and received a passing grade in EMS001x: Semiconductors for Beginners a course of study offered by NUS, an online learning initiative of The National University of Singapore.
2. Completed and received a passing grade in RPFP101 : Programming 101 –An Introduction to python for educators a course study offered by Raspberry foundation, an online learning initiative of raspberry pi foundation.

Guest Lectures Delivered No. : 01
Acted as a Resource person for Final year placement training on Digital Circuit Design Subject during 2nd January 2024 - 10th January 2024 .

Subjects Handled:
Under Graduation:
- Elements of Electronics Engineering.
- Digital Circuit Design
- Electronic Devices and Circuits
- Data Structures
- Analog and Digital IC Applications
- Cellular Mobile Communication
- Digital Logic and Computer Organization

Labs Handled: (UG) :
- Elements of Electronics Engineering.
- VLSI Design
- Digital Circuit Design
- Electronic Devices and Circuits
- Data Structures

- Data Structures using Python
- Digital System Design using Verilog
- Microprocessors and Micro Controllers
- Internet of Things

Post Graduation: NIL
Labs Handled: (PG): NIL
Other Responsibilities:
Department Level:
1) Placement Section Coordinator from Academic year 2021-2022.
2) First Year Feedback Coordinator
3) Class In charge
4) Various Duties in NAAC and NBA

Projects Guided

Under Graduation: 04

1) Design and Implementation of Different Types of Efficient Parallel Prefix Adders.-
2) 64 Bit ALU Design using Vedic Sutras for Multiplication
3) A Low Power High Speed Accuracy controllable Approximate Multiplier Design
4) A High Speed Floating-Point Multiply Accumulator Based on FPGAs
5) Efficient Implementation of FIR Filter Utilizing Dadda Multiplier and Brent Kung Adder

Post Graduation: NIL

Guiding Ph. D Students: NIL

Books Published No. : NIL
R&D and Consultancy: NIL
Curriculum Design and Development: NIL
Academic Rewards / Achievements:
1. Secured State Rank 91 in A.P.R.J.C- 2011.
2. Secured State Rank 31 in PGECET -2017.
3. Qualified as Assistant Professor in UGC NET held in Dec-2018.
4. Recognized as NPTEL Discipline Star during JAN-APR 2023

Extra Curricular Activities
1. Attended ANNUAL TRAINING CAMP –X held at Agricultural Market Yard , Nuzvid from 24 Nov 2011 to 03 Dec 2011
2. Attended COMBINED ANNUAL TRAINING CAMP – I I held at Agricultural Market Yard ,Nuzvid from 08 July 2012 to 17 July 2012
3. Qualified N.C.C 'B' and 'C' Certificate Examinations With B.Grade

Declaration

I hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date: (M.Deepikakrishna)
Place: Signature