FACULTY PROFILE

V.PRIYA DARSHINI

Department: Electronics and Communication Engineering
College: Seshadri Rao Gudlavalleru Engineering College
Mobile: +91-8074235190
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Address for Communication

Teacher's street, Bethavolu. Gudivada, Krishna(DT)

Academic Qualifications

S. No.	Name of the Degree	University	Percentage of Marks/Grade	Specialization	Year of Pass
1.	Ph.D	J.N.T.U.K Kakinada	Pursuing	Low Power VLSI Design	Pursuing
2	M.Tech	J.N.T.U.K Kakinada	84	VLSI DESIGN	2012
3	B.Tech	J.N.T.U.H Hyderabad	72	Electronics and Communication Engineering	2008
4	Intermediate	S.B.T.E.T. Hyderabad	86.7	MPC	2004
5	S.S.C	B.SE Hyderabad	89	---	2002

Professional Experience

S. No.	Designation	Institution Name	Working Period From	To
1	Assistant Professor	Gudlavalleru Engineering College, Gudlavalleru	09-06-2014	Till date
2	Assistant Professor	Sri Vishnu Engineering College, Bhimavaram	05-06-2009	31-05-2014

Professional Body Membership:

S. No	Name of the Professional body	Membership Number
1	The Institution of Engineers India (IE)	AM1632895

Papers Published in Journals No. : -- 15

1. **PriyaDarshini V, Pavana Lakshmi Murala, Meghana Pendurthi ,Sai Rishwanth Mathi, Mahesh Nallamothue,"5**-Read 2-Write Multiport Register File Design Using Novel D-Latches" Journal of Systems Engineering and Electronics, ISSN NO: 1671-1793, Volume 34 ISSUE 4 2024,DOI: 20.14118.jsee.2024.V34I4.1571.

2. **PriyaDarshini V, AsleshaSanjanaK, PavanKumarB, RoshithaRajB, AbdulAbdulla,"** Design of Low power D Flipflop using Clock Gating Techniques"Journal of Systems Engineering and Electronics, ISSN NO: 1671-1793, Volume 34 ISSUE 4 2024,DOI: 20.14118.jsee.2024.V34I4.1579.

3. **Priyadarshini v,Dr.M.Kamaraju,Dr.U.V.Ratna Kumari ,"**Low Power MIPS-RISC Processor: A Survey"CVR Journal of Science and Technology, Volume 25, December 2023 DOI: 10.32377/cvrjst2506.

4. **priydarshini v,K.Sai Bhragav a paper on" Design of 32 bit ALU using Hybrid Techniques"**International Journal of All Research Education & Scientific Methods, (IJA RESM), **April22,ISSN No: 2455-6211", Impact Factor : 7.429**

5. **priydarshini v,** V. Naga Lakshmi, P. Sandhya Rani, P. Sushma Lavanya, S. Manoj Murthy a paper on**" A Design Of Recursive Based Approximate Multipliers"JETIR**(UGC Approved Journal),**April 2019, Volume 6, Issue 4,Impact factor:5.87.**

6. **priyadarshini v** ,v.sreelakshmi and S.Bhavani,a paper on **"Design and Analysis of Approximate Multipliers"**,International Journal of Research in Electronics and Communication Engineering(IJRECE)**(UGC Approved Journal),**Vol.6,Issue4,Oct-Dec 2018,**Impact factor:4.305.**

7. K.Lakshmi Durga, **Priyadarshini v**,a paper on **"VLSI Architecture for Montgomery Modular Multiplication Algorithm by using PASTA Adder",**International Journal of Professional Engineerring Studies,vol 8,Issue 4,May 2017.

8. **Priyadarshini V** and Vasavi v, a paper on " **Low Power Counter design by using DTCPSFF"** ,International Journal of VLSI System Design and Communication Systems(IJVSDC)**,**Vol 4,Issue 2,Feb -2016, **Impact factor:0.18.**

9. Ramya.P, Bhavani S, **Priyadarshini.V**, a paper on "**An Improved Energy Efficient and Certificate Revocation Technique for Mobile Ad Hoc Networks**", IEEE sponsored 4th International Conference on Communications, Signal Processing, Computing and Information Technologies (ICCSPCIT-2015)**,** MRCET, December 18-19, 2015.

10. **PriyadarshiniV** and prof.G.R.L.V.N.Srinivasa Raju, published a paper on "**Power-Area Tradeoff for different CMOS Design Technologies**" as International Journal of Technology and Applications(IJCTA),volume 3(4),1388-1394,July-August-2012, **(UGC Approved Journal), Impact facto**r**:4.080.**

11. **Priyadarshini V** and Ramya p published a paper on" **Design of Low Power Energy Efficient Full Adder Circuits"** International Journal of Innovative science Engineering and Technology(IJISET) ,volume 1 ,Issue 6,august 2014, **Impact factor:5.264.**
12. Ramya.P,**Priyadarshini.V**,Sai Ram Vamsi.T, "**Customizing AOSP on ARM,MIPS,x86 Platforms"**,International Journal of Engineering Associates, Vol.3, Issue 7,pp 20-23,2014.
13. Ramya.P,**Priyadarshini.V**, "**Design of 32-bit ALU using Low Power Energy Efficient Full Adder Circuits"**,IJCTA, Vol.5,pp1754-1762,sep-oct 2014. **(UGC Approved Journal), Impact factor:4.080.**
14. vyshnavi.M,**priyadarshini V** Published a paper on **"Recital Analysis of 64 Bit Unsigned Data Using Various adders"** in International Journal of applied Engineering Research" IJAER,November2014.SJR Journal,**H index 35.**
15. Ramya.P,**Priyadarshini.V**,Chandu.P **"Internet of Everything and Secure Cloud for Real World Data Management",**International Journal of Computer Applications, Vol 104,Issue 13,pp 9-11,October 2014. **Impact factor:3.12.**

Papers Published in Conferences No. : 03

1. priydarshini v,K.Sai Bhragav a paper on" **Design of 32 bit ALU using Hybrid Techniques** presented a paper "International Conference on Computational and Intelligent Techniques for Automation of Engineering Systems(CITASS-2022)" on 29th-30th July 2022 organized by department of ECE ,Seshadri Rao Gudlavalleru Engineering College.

2. **Priyadarshini V,**Dr.M.Kamaraju,Dr.U.V.Ratna Kumari,**"A survey on MIPS RISC Processors",Presented a paper "**International Conference on Digital Transformation: A strategic approach towards Sustainable Development (ICDTSD-2022)" On 11th – 12th February 2022 Organized by Institute of Management Education Research and Training (IMERT), Pune,Maharashtra.

3. Ramya.P, Bhavani S, Priyadarshini.V, "An Improved Energy Efficient and Certificate Revocation Technique for Mobile Ad Hoc Networks", IEEE sponsored 4th International Conference on Communications, Signal Processing, Computing and Information Technologies (ICCSPCIT-2015), MRCET, December 18-19, 2015.

Workshops /Conferences /Seminars Attended No: 50

1. Mrs.V.Priyadarshini : A Three day online Faculty Development Program on "Generative AI" held at Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru from 26-28 June 2024.

2. A One Week Faculty Development Program on "Emerging Trends and Developments in simulation and Fabrication of Semiconductor Devices(online)" held at Schools of Electronics Engineering at VIT-AP University,Amaravathi,during 2-8 May 2024.

3. A One Week online Faculty Development Program on "Impact of AI :Robotics,VLSI and Medical Domains" held at VVIT,during 22-27 April 2024.

4. Participated in one week International Faculty Development Program on "IOT ,Industry 4.0 and stress Management" organized by Department of IOT Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru,in association with IETE Vijayawada center from 17-07-2023 to 22-07-2023.

5. Participated in one week online Faculty Development Program on "Applications on Computer Vision using Deep learning" organized by Department of ECE Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru, during 20-25 February, 2023.

6. Participated in one week online Faculty Development Program on"VLSI Exposure and it's Challenges in Engineering "organized by Department of ECE ,Krishna University College of Engineering and Technology from 11th july 2022-15th July 2022.

7. participated in A two day workshop on MACHINE LEARNING : CONCEPTS AND APPLICATIONS organized by R&D Cell, Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru, during 30th June, 2022 - 01st July, 2022.

8. participated in One - week Online Joint Faculty Development Programme on "RISC-V Processor Design : Circuits and Architecture" jointly organised by Electronics and ICT Academies held from 04 - 09 April, 2022 under the "Scheme of financial assistance for setting up of Electronics and ICT Academies" of the Ministry of Electronics and Information Technology (MeitY), Government of India.

9. Participated in one week online Faculty Development Program on"Trends and challenges in Advanced VLSI system Design"Organized by Dept of ECE,S.R.K.R Engineering College(A) during 21st-26th February 2022.

10. Participated in a 40 -hour Online Faculty Development Programme on ASIC Design FLOW:LOW POWER PERSPECTIVE"Sponsered by Ministry of Electronics and Information Technology(MeitY),GoI organized by NITW and E&ICT Academy,NIT Warangal 1st-10th July 2021.

11. Attended an online FDP on "Internet of Things(IOT)" conducted by Gudlavalleru Engineering College and ATAL Academy during 7th to 11th December 2020.

12. Participated in Xilinx webinar on "Next Generation VLSI Design using Lower Technology Nodes"held on 4th December 2020.

13. Participated in one week online STTP on "Mixed Signal Design"organized by VVIT,from 26th october to 31st october 2020.

14. Partiipated in online Three Day Higher Education Conclave,Organized by IEEE Guntur Sub Section in collaboration with IEEE Education Society Chapter,Hyderabad section during october 15th-17th,2020.

15. Attended a AICTE Sponsered STTP conducted by Gudlavalleru Engineering College on Block Chain Architecture design and use cases"pahse-III held during 24th-29th August 2020.
16. Attended a AICTE Sponsered STTP conducted by Gudlavalleru Engineering College on Block Chain Architecture design and use cases"pahse-II held during 17th-22nd August 2020.
17. Attended a AICTE Sponsered STTP conducted by Gudlavalleru Engineering College on Block Chain Architecture design and use cases"pahse-I held during 10th-15th August 2020.
18. Attended a webinar conducted by Gudlavalleru Engineering college on "Intellectual Property Rights &Patents -a View"held during 10th to 12th August 2020.
19. paricipated in one week Referesher course on "Embedded System Design " organized by the Department of Electronics and Communication Engineering, Gudlavalleru Engineering College,Gudlavalleru from 27th July to 1st August 2020.
20. One week online workshop on Blended E-Learning organized by the department of Computer Science and Engineering, JNTUK university college of Engineering vizianagaram, from 6th to 10th July 2020.
21. successfully completed National Level Quiz on Industrial Engineering And Management Quiz, Organized by Department of Mechanical Engineering, Rise Krishna Sai Prakasam Group Of Institutions, Ongole on 7/16/2020 with a score of 100%.
22. Three Day National Level WEBINAR SERIES on "VLSI", organized by the Department of Electronics and Communication Engineering, from 10th to 12th June 2020 in collaboration with APPLY VOLT.
23. one week International online knowledge development program on " challenges and advancements in the design of IOT ,Embedded and VLSI systems: A researcher View" Organized by department of Electronics and Communication Engineering, Gudlavalleru,from 8th-13th june ,2020.
24. participated in the webinar Writing a Great Research Paper (Picking an A+ Topic) organized by Matterhere on 31-May-2020.
25. Participated in the onw week National Level Online Faculty Development Program on "Research challenges and Emerging trends in Electronics and Communication Engineering(RCEECE-2020) organized by Department of ECE,from 27 may 2020 to 01 June 2020.
26. participated in the webinar on "Radio Frequency Measurements for Cellular and Wireless Communication Systems" organized by Department of Electronics and Communication Engineering,Gudlavalleru Engineering College on 25th May, 2020.
27. Participated 5 days online certification program on Industrial IOT held from 19th to 23rd May 2020.

28. participated in a Five Day National Level Online FDP on "Modern Research Trends in Communication, Signal Processing and VLSI/MEMS", organized by the Department of Electronics and Communication Engineering, from 19th to 23rd May 2020.

29. Paricipated in one week FDP on Data Acquisition and RT using LAbVIEW organized by National Instruments,Banglore,during 25th-30th November 2019.

30. Paricipated in Three days Workshop on Advanced Development Tools Based on IOT using TI Boards from 11th to 13th November 2019 under Texas Instruments University Program in association with EdGate Technologies Pvt Ltd Banglore.

31. One week Faculty Development Programme on "Communications and Signal Processing "organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru 29th oct to 3rd Nov 2018.

32. Two day faculty training Programme on "Advances in VLSI Design and Embedded Systems" Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College,Gudlavalleru 6th -7th November 2017.

33. Two Week ISTE STTP on CMOS,"Mixed Signal and Radio frequency VLSI Design "Conducted by Indian Institute of Technology Kharagpur from 30th January 2017 to 4th February 2017.

34. Five day FSP on "Custom IC Design and ASIC Implementation using Cadence EDA Tools"during 28th November to 2nd December organized by the Department of Electronics and Communication Engineering,V R Siddhartha Engineering College,Vijayawada.

35. One week Faculty Development Programme on "Hands on Experience in Signal Processing Applications "held during 7th-12thNovember, 2016 at Gudlavalleru Engineering College.

36. Two day staff training program on "Simulation of Linear & Digital Integrated Circuits and Microcontroller based Applications" using NI MultiSim organized by Department ofElectronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru   on 4-6 November 2016.

37. Attended A three day workshop on "VLSI Design Techniques using Xilinx& Mentor Graphics EDA Tools" conducted on 23rd -25th June 2016 at Andhra Loyola College of Engineering and Technology, at Vijayawada

38. Participated in the Faculty Development Programmed(FDP) on "Development of Wireless Sensor Networks using Network Simulator(NS2)-Hands on Experience "organized by Communication Research Group in association with Vertex Soft,Hyderabad,1st -5th Dec 2015.

39. Attended a one week Refresher course on " Microprocessors and Micro Controllers"held during 17th -21st November 2015,at Gudlavalleru Engineering College organized by Research and Development Centre, Department of ECE.

40. One day workshop on "Ultra Low Power Communication Using CC430 Microcontroller – Hands on Experience", organized by the Department of Electronics and communication Engineering, Gudlavalleru Engineering College, Gudlavalleru on 09th February 2015.

41. One day workshop on "Research in Energy Management Wireless Netwrks & Smart Transportation System"organized by The Institution of Engneers,Vijayawada Local Centre,Vijayawada on 29th March 2015.
42. One day workshop on "Speech Processing: Current Challenges and Hands-on Experience", organized by Signal Processing and Communication Research Group, Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru on 23rd August 2014.
43. National workshop on Formulation on "Research &Development Initiatives for Women Scientists and Technologies" 05-07 February 2014,Hyderabad.
44. Two day Workshop on "Real time Audio and Video Processing on Texas Instruments development Tools" organized by Department of Electronics and Communication Emgimeerimg ,SVECW,Bhimavaram.
45. Two day Workshop on"Issues in Integrated Circuits(ICs) Design" organized by Department of Electronics and Communication Emgimeerimg ,GEC Gudlavalleru, on 13th-14th December 2013.
46. Participated in IEEE R10 Women Engineering congress2012 held at SVECW Bhimavaram.
47. Participated in the AICTE sponsored staff development program on :challenges in FPGA based Digital System Desigm: held from 6-18th June,2011.
48. Two days Workshop on Digital Design Simplified Taking Design from Simulation to Layout Extaction" organized by Department of Electronics and Communication Emgimeerimg ,SVECW,Bhimavaram ,30th April – 1st May 2010.
49. A two day workshop on "VLSI design" organized by Department of Electronics and Communication Emgineerimg, Gudlavalleru Engineering College, Gudlavalleru.
50. Two day Workshop on VERILOG PROGRAMMING & IMPLEMENTATION during 27-28 October 2007 Organiged by the Department of Electronics and Communication Emgimeerimg Gudlavalleru Engineering College, Gudlavalleru.

Training Programs Attended No. : 05

1. Ms.Priyadarshini v: A Five day Refresher course on "Microcontrollers and Embedded systems" held at Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru .
2. Participated in two day Faculty Training program on "Design of Integrated Circuits using Microwind Tools"organized by department of ECE ,Seshadri Rao Gudlavalleru Engineering College during 30th-31th August 2023.
3. Participated in two day faculty training program on "Full custom IC Design using CADENCE software Tools"organized by department of ECE ,Seshadri Rao Gudlavalleru Engineering College during 4th-5th August 2022.
4. Participated in two day Faculty Training programe on "Design and Development of VLSI circuits using Mentor Graphics Tools"organized by department of ECE ,Seshadri Rao Gudlavalleru Engineering College during 14th-15th August 2020.

5. Six day training program on "Training Teachers for Excellence", organized by Gudlavalleru Engineering College, Gudlavalleru during 19th- 21st August 2014 and 29th - 31st December 2014.

NPTEL online Certification courses completed: 06

NPTEL Course	Percentage	Topper
Computer Organization And Architecture: A Pedagogical Aspect	Elite(75%)	Topper5%
Digital Circuits	Elite(83%)	Topper5%
Digital Electronics	Elite(73%)	Topper5%
Embedded System Design with ARM	Elite+Silver(86%)	
Hardware Modelling using Verilog	Elite+Silver(76%)	
Microprocessorand Interfacing	96%	

EdX Online certification courses Completed:

1. **IoT Sensors and Devices**
2. **Introduction to the Internet of Things (IoT)**
3. **IoT Networks and Protocols**
4. **Cybersecurity and Privacy in the IoT**
5. **IoT Programming and Big Data**
6. **Build Your First Internet of Things (IoT) Application on Arm**

Subjects Handled
Under Graduation

- Digital Logic Design
- Data Communication Systems
- Computer Networks
- Telecommunications and Switching Systems
- VLSI Design
- Control Systems

- Computer Architecture & Organization
- Analog Communications
- Microprocessors and Interfacing
- Electronic Measurement and Instrumentation

Post Graduation
- Principles of Digital system Design
- Advances Computer Networks
- Testing and Testabilty of VLSI circuits

Labs Handled

Under Graduation
- Ecad Lab
- Digital Logic Design Lab
- Analog Communications Lab
- VLSI & Embedded Systems Lab
- Microprocessors and Interfacing Lab

Other Responsibilities

College Level (in GEC)   --   **NIL**

Department Level (in GEC)

- III Year Exam section Coordinator
- MOOCS Coordinator
- Faculty NPTEL Coordinator
- Department Magazine (e-pulse) Coordinator
- Student Counselor

Projects Guided:

Under Graduation   --   **10**

Post Graduation   --   **03**

Books Published No. : 04

- Co-author for chapter 5.1 titled "VLSI Laboratory" of Electronics and Communication Engineering laboratory Manual series 2 with ISBN – 978-1-4276-5564-6 in the year 2016

- Co-author for chapter 4.2 titled "Electronic Computer Aided Design Lab" of Electronics and Communication Engineering laboratory manual series with ISBN – 978-1-4276-5563-9 in the year 2016
- Co-author for chapter 4 titled "Switching Theory and Logic Design" of Electronics and Communication Engineering Instructional manual with ISBN – 978-1-4276-5539-4 in the year 2015.
- Co-author for chapter 2.2 titled "Analog Communications Laboratory" of Electronics and Communication Engineering laboratory Manual with ISBN – 978-1-4276-5536-3 in the year 2015

Curriculum Design and Development
- Ecad Lab Manual
- VLSI Lab Manual
- Digital Logic Design Lab Manual
- Advanced Digital Design syllabus

Academic Rewards / Achievements
- Ratified as an Assistant Professor by JNTU Kakinada
- Got **top 5%** in NPTEL On line Certification for the Course "**Computer Organization andArchitecture: A pedagogical Aspect".**
- Got **top 5%** in NPTEL On line Certification for the Course**" Digital Circuits"**
- Got **top 5%** in NPTEL On line Certification for the Course**" Digital Electronic circuits"**

Extra Curricular Activities
- Enacted as BoS member for First Bos Meeting of Electronics and Communication Engineering for R20 Regulations-scheduled on Ma7 2022 at Gudlavalleru Engineering College, Gudlavalleru
- Enacted as BoS member for First Bos Meeting of Electronics and Communication Engineering for R17 Regulations-scheduled on 31st March 2017 at Gudlavalleru Engineering College, Gudlavalleru.
- Enacted as BoS member for the Autonomous curriculum design of Electronics and Communication Engineering at Gudlavalleru Engineering College, Gudlavalleru on 2nd August 2014 and 25th May 2015.

Declaration

I, hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date: 31-1-2025   Signature

Place: Gudlavalleru   (PriyaDarshini.v)