

FACULTY PROFILE

N.MONIKA

Department: Electronics and Communication Engineering
College: Gudlavalleru Engineering College
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Address for Communication:

College Address:

N.Monika
Assistant Professor,
ECE Department,
Gudlavalleru Engineering College,
Gudlavalleru, Krishna District,
Andhra Pradesh: 521356

Permanent Address:

N.Monika
D/o N.Sankar Babu, H.No: 2-38,
Mopidevi Post, 3rd ward,
Mopidevi (V&M),
Krishna District
Andhra Pradesh: 521125

Academic Qualifications:

| S. No | Name of the Degree | University | Percentage of Marks/Grade | Specialization | Year of Pass |
|-------|--------------------|--------------------------------------|---------------------------|---|--------------|
| 1 | M.Tech | JNTUK University | 8.9(cgpa) | Digital Electronics and Communication Systems | 2018 |
| 2 | B.Tech | JNTUK University | 75 | E.C.E | 2015 |
| 3 | Intermediate | Board of Intermediate Education | 86 | M.P.C | 2011 |
| 4 | 10 th | Central Board of Secondary Education | 70 | - | 2009 |

Professional Experience:

| S. No. | Designation | Institution Name | Working Period | |
|--------|---------------------|----------------------------------|----------------|-----------|
| | | | From | To |
| 1 | Assistant Professor | Gudlavalleru Engineering College | 21-11-2019 | Till date |

Papers Published in Journals No. : 01

1. N.Monika, B.K.N.Srinivasarao, M.Vijayakumar, "VLSI Architecture For Contrast Enhancement Using Pixel Based Image Fusion", International Journal Of Creative Research Thoughts (Ijert), Issn:2320-2882, Volume.6, Issue 2, Page No Pp.233-237, April 2018, Available At : [Http://Www.Ijert.Org/Ijert1893038.Pdf](http://Www.Ijert.Org/Ijert1893038.Pdf) .

Workshops Organized No. : NIL

Papers Published in Conferences: 01

2. Presented a paper entitled " "Graphical Designing of FPGA VLSI Architecture for Pixel Based Image Fusion using LABVIEW" in the Online National conference on "Communications and Computing (NC3-2020)", organized by Department of Electronics and Communication Engineering, Bapatla Engineering College, Bapatla, Guntur (Dt), Andhra Pradesh, INDIA, During 28th- 29th July 2020.

NPTEL Certification Courses: 02

1. Passed a NPTEL course on "Architectural Design of Digital Integrated Circuits".
2. Passed a NPTEL course on "Microprocessors and Microcontrollers".

Workshops Attended No. : 03

1. Attended a Workshop on " VLSI Circuits Analog and Digital Perspectives", organized by the Department of Electronics and Communication Engineering, JNTUA College of Engineering, in association with AMS Semiconductors and HCL Technologies from 16th to 17th May 2020.
2. Attended the two day workshop on "PCB Design of Electronic Circuits" at Nova College of Engineering and technology, Ibrahimpatnam, during 1st and 2nd October 2012.
3. Attended the two day workshop on "IROBOT" at Nova College of Engineering and technology, Ibrahimpatnam, during 4th and 5th January 2013.

Webinars Attended No. : 09

1. Attended a Webinar on “ **Radio Frequency Measurements for Cellular and Wireless Communication Systems**”, organized by the Department of Electronics and Communication Engineering, Gudlavalleru Engineering College on 25th May 2020.
2. Attended aInternational Webinar on “ **Nano-materials and Detection Technique of COVID-19**”, organized by the Department of Physics, Babu Shivnath Agarwal(P.G) College, U.P on 7th June 2020.
3. Attended a Webinar on “ **Kick Start your Research Work**”, organized by the Department of Computer Science and Engineering, JAIN(deemed to be university) on 30th May 2020.
4. Attended a Xilinx Webinar on “ **Custom IP Design and Validation using Vivado**”,organized in association with CoreEL Technologies and Xilinx on 22th May 2020.
5. Attended a Technical Webinar on “ **Design Approaches For Front End VLSI Design**”, organized by the Department of Electronics and Communication Engineering, Vaagdevi College of Engineering on 23rd May 2020.
6. Attended a Webinar on “ **Digital Transformation**”, organized by the Department of Electronics and Communication Engineering,Poornima Institute of Engineering and Technology on 29th May 2020.
7. Attended a Webinar on “ **Labview-Programming for Real Time Applications**”, organized by the Department of Electronics and Communication Engineering, lakkireddy Balireddy College of Engineering on 1st July 2020.
8. Attended a Webinar on “ **Successful Steps towards high quality Scientific Publications**”, organized by Chalapathi Institute of Technology on 18th May 2020.
9. Attended a Webinar on “ **Impact of COVID-19 in Engineering Education and Way Forward**”, organized by ISTE Chapter, Gudlavalleru Engineering College on 9th June2020.

Courses completed No. : 05

1. Completed a course on “Introduction to Electronics” authorised by Georgia institute of technology and offered through Coursera.
2. Completed an AICTE self-paced course on “MATLAB onramp” offered through Mathworks Training Services.
3. Completed an AICTE self-paced course on “DEEP learning onramp” offered through Mathworks Training Services.
4. Completed an AICTE self-paced course on “Advanced architectures in LABVIEW” offered through NI Customer Education.
5. Completed a E-Learning program on “PLCs” authorised by TATA Steel and offered through Capability Development.

Training Programs /FDPs Attended No. : 22

1. Participated in an online FDP Program on "**Cyber Security**" organized by the department of Computer Science Engineering and Information Technology, in association with CyberSpy, Gudlavalleru Engineering College, Gudlavalleru from 22th July to 26th July.
2. Participated in one week refresher course on "**Embedded System Design**" organized by the department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru from 27th July to 1st Aug.
3. Attended an online short-term training programme on "**Block Chain Architecture Design and Use Cases**" Phase-I organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College from 10-08-2020 to 15-08-2020.
4. Attended a two day training programme on "**Mentor Graphics Tools**" at Gudlavalleru Engineering College, Gudlavalleru from 31-08-17 to 01-09-17.
5. Attended a two day FDP on "**Advances in VLSI Design and Embedded Systems**" at Gudlavalleru Engineering College, Gudlavalleru during 6th to 7th November 2017.
6. Attended a one week National level FDP on "**Signal and Image Processing using LabVIEW**" at Gudlavalleru Engineering College, Gudlavalleru, during 13th to 18th November 2017 at Gudlavalleru Engineering College, Gudlavalleru.
7. Attended a 13-day online Faculty Development Program on "**Embedded Systems**", organized by APSSDC from 25th May 2020 to 6th June 2020.
8. Attended a online Faculty Development Program on "**Applied Machine Learning and Deep Learning**", organized by APSSDC from 15th to 20th June 2020.
9. Attended a online Faculty Development Program on "**Matlab and Simulink in Engineering Education**", organized by APSSDC from 10th to 11th June 2020.
10. Attended a 3-day Faculty Development Program on "**VLSI Technology and Hardware for AI & ML**", organized by the Department of Electronics and Communication Engineering, GMR Institute of Technology from 25th to 27th May 2020.
11. Attended a Faculty Development Program on "**Artificial Intelligence and its applications**", organized by the Department of Computer and Science Engineering, Ramachandra College of Engineering from 25th to 30th May 2020.
12. Attended a 3-day International Faculty Development Program on "**Technologies For Healthcare**", organized by the Department of Electronics and Communication Engineering, GMR Institute of Technology from 8th to 10th June 2020.
13. Attended a one-week online International online Knowledge Development Program on "**Challenges and Advancements in the Design of IoT, Embedded and VLSI Systems: A Researches View**", organized by the Department of Electronics and Communication Engineering, Gudlavalleru Engineering College from 8th to 13th June 2020.

14. Attended a 3 day Faculty Development Program on “ **Smart Applications Development using -ARDUINO**”, organized by the Department of Electronics and Communication Engineering, in association with Pantech E-Learning from 17th to 19th June 2020.
15. Attended a one week Faculty Development Program on “ **ICT Tools**”, in association with STP- IIT Bombay organized by the Department of Information Technology, Sree Vidyanikethan Engineering College from 11th to 16th May 2020.
16. Attended a National level 5-Day Faculty Development Program on “ **Modern Research Trends in Communication, Signal processing, VLSI MEMS**”, organized by the Department of Electronics and Communication Engineering, Andhra Layola Institute of Engineering and Technology from 19th to 23rd May 2020.
17. Attended a Faculty Development Program on “ **Artificial Intelligence and its applications**”, organized by the Department of Computer and Science Engineering, Ramachandra College of Engineering from 25th to 30th May 2020.
18. Attended a 2-Day online Faculty Development Program on “**Microstrip Antennas for Advanced Wireless Communications**”, organized by the Department of Electronics and Communications Engineering, Kallam Harinadhreddy Institute of Technology from 15th to 16th May 2020.
19. Attended a 5-Day Faculty Development Program on “ **IPR Awareness and Patent Prosecution**”, organized by the Department Information Technology, CMR Engineering College from 6th to 10th May 2020.
20. Attended a one-week online Faculty Development Program on “**Advanced Antenna Design using HFSS**”, organized by the Department of Electronics and Communication Engineering, Santhiram Engineering College from 12th to 17th May 2020.
21. Attended a 5-Day online Faculty Development Program on “**SCILAB**”, organized by the Department of Electronics and Communication Engineering, VKR, VNB & AGK College of Engineering in association with Spoken Tutorials, IIT Bombay , Santhiram Engineering College from 12th to 17th May 2020.
22. Attended a online Faculty Awareness program on “ **Research Methodology**”, organized by the Department of Mechanical Engineering, Sri Chatrapathi Shivajiraje College, Pune from 20th to 25th May 2020.

Subjects Handled:

Under Graduation: NIL

Labs Handled: (UG) :

1. Microprocessor and Microcontrollers (MPMC)
2. Digital Signal Processing(DSP)

Academic Rewards / Achievements: 02

- Got academic excellence (Merit) certificate in M-tech course.
- Got "Certificate of Merit" at the seminar conducted by 'Yandamoori Veerendranath' in 2011.
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Extra Curricular Activities: 06

- Got prize in District level "Shuttle Badminton" competition held in Z.P.H School at Nandigama.
- Won prizes in Table Tennis, long jump, high jump, Running in All India D.A.V sports Meet.
- Got prize in "Art competition" held by Andhrajyothi in Hyderabad.
- And actively participated in many school level competitions and won prizes.
- Got 2nd prize in "logo contest" held by NOVA College of Engineering and Technology.
- Got 1st prize in "Art Expo" held at Paladugu Parvatidevi college of Engg & Technology.

Declaration

I hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date: 21-08-2020

Place: Gudlavalleru

Signature

(N.Monika)