FACULTY PROFILE

Name of the faculty: CHAPARALA SURESH

      Department: ECE

Department: ECE

College: Seshadri Rao Gudlavalleru Engineering College

Mobile: 7842778918

E-Mail:suresh.gdvl@gmail.com

RESIDENTIAL ADDRESS: Gudlavalleru,521356

ACADEMIC QUALIFICATIONS: M.Tech

ACADEMIC REWARDS / ACHIEVEMENTS:

- ➢ I got 122 rank in ECET.
- ➢ Certified on Diploma in Computer Applications (**DCA**)
- ➢ Ratified by JNTUK, Kakinada

PROFESSIONAL EXPERIENCE:

Designation	Institution	Period
Assistant Professor	SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE	August 25th 2022 to till date
Assistant Professor	AMRITA SAI INSTITUTE OF SCIENCE AND TECHNOLOGY	5.6 years

PROJECTS GUIDED:

PhD	Post Graduation	Under Graduation
-	1	7

SPECIALIZATION: Communication Engineering and Signal Processing

RESEARCH PAPERS / BOOKS:NIL

PAPERS PUBLISHED IN JOURNALS:

1. A Paper titled "Digital video watermarking by PAC and discrete cosine transform" was publish in International Journal for Innovative Engineering and management research in Volume 7 Issue 02, February 2018

PAPERS PUBLISHED IN CONFERENCES/WORKSHOPS:NIL

LIST OF WORKSHOPS ORGANIZED:

- Coordinated workshop on "Mobile Robotics".

LIST OF WORKSHOPS /CONFERENCES /SEMINARS ATTENDED:

- Two day Workshop on "Digital Signal Processors Architectures and Optimization Methodologies" organized by the Department of Electronics and Communication Engineering,D.V.R & Dr.H.S MIC college of Technology on 23rd and 25th September 2017

CERTIFICATIONS/Training Programs Attended No:NIL

GUEST LECTURES DELIVERED:NIL

MEMBERSHIP IN PROFESSIONAL BODIES: ----

S. No.	Name of the Professional body	Membership Number
1	ISTE	**LM104812**

CONSULTANCY ACTIVITIES:NIL

GRANTS FETCHED: NIL

Subjects Handled

Under Graduation

- Microprocessors and interfacing
- Analog communications
- Radar Systems
- Electronic Measurements and Instrumentation
- Switching theory and logic design
- Electronic devices and circuits
- Electronic switching systems
- Digital IC applications

Post-Graduation

- Advanced Embedded Systems
- Digital system design

Labs Handled

Under Graduation
- Electronic Devices and Circuits Lab
- Digital signal processing Lab
- Microprocessors and Interfacing Lab
- Communications Lab

Post-Graduation
- HDL Lab
- Advanced Digital Design Lab

Books Published No. :NIL
R&D and Consultancy:NIL
Curriculum Design and Development
1. Communication systems
2. Advanced wireless Sensor networks

Extra Curricular Activities: Department level activities

Declaration

I hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date: 26-10-22   CH SURESH

Place Gudlavalleru   Signature