# **FACULTY PROFILE**

Name of the faculty: MANNE GALEEB

Department: ECE

College: Gudlavalleru Engineering College

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**RESIDENTIAL ADDRESS:** Door no:7-3, Near Srilanka colony,

vuyyuru(v),vuyyuru(M).pin- 521165

ACADEMIC QUALIFICATIONS: M.Tech, pursuing Ph.D. at KLU Gutur ,AP.

## **ACADEMIC REWARDS / ACHIEVEMENTS:**

> GATE 2010 qualified.

## PROFESSIONAL EXPERIENCE:

Designation	Institution	Period
Assistant Professor	Gudlavalleru Engineering College	July 29 <sup>th</sup> 2022 to till date
Assistant Professor	RAMACHANDRA College of Engineering- Eluru	10 months Oct 1 <sup>st</sup> 2021- July 29 <sup>th</sup> 2022
Assistant Professor	Usha Rama College of Engg& Technology - Telaprolu	5 years 3 months June 6 <sup>th</sup> 2016- Sep 30 <sup>th</sup> 2021
Assistant Professor	Nimra Group of Colleges, Vijayawada	2 years July 1 <sup>st</sup> 2014- May 30 <sup>th</sup> 2016
Assistant Professor	Kaushik College of Engineering, Visakhapatnam	2 years 2 months June 6 <sup>th</sup> 2009- Aug 30 <sup>th</sup> 2011
Assistant Professor	Sana Engineering College, Kodada	11 months June 4 <sup>th</sup> 2008- May 25 <sup>th</sup> 2009

## **PROJECTS GUIDED:**

PhD	Post Graduation	<b>Under Graduation</b>
-	3	9

**SPECIALIZATION:** Microwave and Communication.

## **RESEARCH PAPERS / BOOKS:**

### PAPERS PUBLISHED IN JOURNALS:

- 1. **M Galeeb** "DESIGN OF AES USING VERILOG" Juni Khyat ISSN: 2278-4632 (UGC Care Group I Listed Journal) Vol-12 Issue-01 No.01: 2022.
- 2. Shaik.Bajivali, MGaleeb, "DESIGN A HIGH SPEED AND HIGH SECURE 16\*16 SRAM ARRAY USING 22mm" Journal of Critical Reviews (JCR), Vol–7, Issue-15.2020.
- 3. G Srikanya, P kanaka raju, **MGaleeb**, "ANALYSIS OF VARIOUS 8TSRAM DESIGNS USING DTCMOS TECHNIQUE" International Journal of Research in Electronics and Computer Engineering (IJRECE), Vol–5, Issue-4. July Aug, 2017. (Available online: http://ijrece.com/Login.aspx?ArticleId=IJRECE/V5/52609&MenuName=Volume-5,%20Issue-5PW: ijrece#@\$2609)
- 4. **MGaleeb**, TThirupathirao, "ESTABLISHING AN EFFICIENT COMMUNICATION FOR LOW POWER APPLICATIONS", International Journal of Innovative Research In Advanced TechnologyISSN: 2319-1467, Vol 1, issue:01 page:7910-19, January 2016(Available online: https://ijirat/publications.org/journals).
- 5. **M Galeeb** "A Novel Approach of Rayleigh Fading Channel and MIMO Multiple Relay Assignment in OFDM-based System using amplify-and-forward(AF) Relay Strategies" (IJERT) ISSN: 2278-0181Vol. 2 Issue 11, November 2013. (Available online: https://www.ijert.org/research/a-novel-approach-of-rayleigh-fading-channel-and-mimo-multiple-relay-assignment-in-ofdm-based-system-using-amplify-and-forward-af-relay-strategies-IJERTV2IS111051.pdf)

### PAPERS PUBLISHED IN CONFERENCES/WORKSHOPS:

### LIST OF WORKSHOPS ORGANIZED:

- Coordinator for" ROBOTICS" workshop in URCET.
- Coordinator for "PCB Design" workshop in URCET.
- Coordinator for TECHNICAL QUIZ for URFEST in URCET.

## LIST OF WORKSHOPS /CONFERENCES /SEMINARS ATTENDED:

- One week (April 18th to 23rd 2022) **FDP** On "Low Power VLSI & its Industry trends" Organized by Department of Electronics and Communication Engineering at sanskrithi school of Engineering puttaparthi.
- 7 Days' (27 March-02 April, 2017) Workshop on "QIP Short Term Course cum Workshop on Industrial Applications of Terahertz Radiation" At Organized By, IIT Kharagpur-721 302, Venue: S N Bose Auditorium, IIT Kharagpur.
- 3-Days (29TH Nov to 1st Dec 2016) FDP On "DESIGN AND ANALYSIS OF RF ANTENNAS USING HFSS" Organized by Department of Electronics and Communication Engineering at LAKIREDDY BALIREDDY COLLEGE OF ENGINEERING, MYLAVARAM
- One Week (16th 20th December 2019) FDP On "VLSI Chip Design Hands-on using Open-Source EDA" Jointly Organized by Electronics and ICT Academy Supported by Ministry of Electronics and Information Technology (Meity), Govt. Of India, At Malaviya NIT Jaipur.
- One Week (8th 12th July 2019) FDP On "VLSI Chip Design Hands-on using Open-Source EDA" Jointly Organized by Electronics and ICT Academy Supported by Ministry of Electronics and Information Technology (Meity), Govt. Of India, At Malaviya NIT Jaipur.
- One Week (1st 5th July 2019) FDP On "Antenna trends" Jointly Organized by Electronics and ICT Academy Supported by Ministry of Electronics and Information Technology (Meity), Govt. Of India, At Malaviya NIT Jaipur.
- One Week (3rd 7th December 2018) FDP On "Wireless and Mobile Communications" Jointly Organized by Electronics and ICT Academy Supported by Ministry of Electronics and Information Technology (Meity), Govt. Of India, At Malaviya NIT Jaipur.
- 24th 29th August 2020.AICTE Sponsored One Week Online Short-Term
   Training Program (STTP-II) On "Electromagnetics, Microwave, RF & Antenna
   Design Using Ansys HFSS" Organized by Department of Electronics & Communication Engineering, SHRI VISHNU ENGINEERING COLLEGE FOR WOMEN(Autonomous) Vishnupur, Bhimavaram.

- 5 Day's (01-06-2020 to 05-06-2020) **FDP** On "Technological Advances in Power Switching Converters for Renewable Energy Sources and Fuel Cell Technology for E-vehicles" At Organized by Department of Electrical & Electronics Engineering, Bapatla Engineering College, Bapatla.
- 5 Day's (01-06-2020 to 05-06-2020) **FDP** On "National level Faculty developments program on multidisciplinary Approach towards Emerging Trends in science and humanities" At Organized by RAMACHANDRA COLLEGE OF ENGINEERING, ELURU.
- Attended in 3-DAYS (4th Jan to 6th Jan 2018) Workshop On "RESEARCH METHODOLOGY" organized by Koneru Lakshmaiah Education Foundation (KL University) at Vaddeswaram
- Participated in One day National Level Workshop on "ANTENNA DESIGN USING HFSS" in UshaRama College of Engineering.

## **CERTIFICATIONS/Training Programs Attended No: 2**

- 7 Days' (27 March-02 April, 2017) Workshop on "QIP Short Term Course cum Workshop on Industrial Applications of Terahertz Radiation" 'At Organized By, IIT Kharagpur-721 302, Venue: S N Bose Auditorium, IIT Kharagpur.
- 24th 29th August 2020.AICTE Sponsored One Week Online Short-Term
   Training Program (STTP-II) On "Electromagnetics, Microwave, RF & Antenna Design Using Ansys HFSS" Organized by Department of Electronics & Communication Engineering, SHRI VISHNU ENGINEERING COLLEGE FOR WOMEN(Autonomous) Vishnupur, Bhimavaram.

### **GUEST LECTURES DELIVERED: ----**

## MEMBERSHIP IN PROFESSIONAL BODIES: SDIWC, IAENG, ISRD

- The Society of Digital Information and WirelessCommunications (SDIWC)
- INTERNATIONAL ASSOCIATION OF ENGINEERS (IAENG)
- INTERNATIONAL SOCIETY FOR RESEARCH AND DEVELOPMENT(ISRD)

## **CONSULTANCY ACTIVITIES: ----**

## **GRANTS FETCHED: ----**

## **Subjects Handled**

### **Under Graduation**

- Electromagnetic and Transmission line (EMTL)
- Microwave Engineering (MWE)
- Antennas and Wave Propagation (AWP)
- Control systems (CS)
- Digital communication (DC)
- STLD
- Radar system (RS)
- Electronic circuit analysis (ECA)
- Signals and Systems (SS)
- Electronic devices and circuits (EDC)

#### **Post-Graduation**

### **Labs Handled**

### **Under Graduation**

- MWE&OC Lab
- Digital Signal Processing lab (Matlab)
- AC, DC, EDC, STLD Labs

## **Post-Graduation**

## **Other Responsibilities**

## **College Level**

### **Department Level**

- Department Exam-Cell In-charge
- NBA Criteria-6 In-charge
- Students Counselling In-charge
- Microwave Lab In-charge

# **Projects Guided**

# **Under Graduation**

S. No	Name of the project	Year
1	Solar based SMART Lighting depends on vehicles movement	2015
2	Automatic Train speed control by using singals	2016
3	Channal Estimation for OFDM Systems	2018
4	Design of U-Shaped microstrip patch antenna for WIMAX Application	2019
5	Efficient Direction of ARRIVAL Estimation using the MIMO Technology	2020
6	Implementation of Advanced Multiplier for various multimedia applications using approximate compressers	2021
7	Density and latency optimised two speed Radix4 modified booth multiplier	2021
8	Automatic Health Monitoring System	2022
9	Automatic Railway Gate control system	2022

## **Post-Graduation**

S. No	Name of the project	Year
1	ESTABLISHING AN EFFICIENT COMMUNICATION FOR LOW POWER APPLICATIONS	2016
2	ANALYSIS OF VARIOUS 8TSRAM DESIGNS USING DTCMOS TECHNIQUE	2017
3	DESIGN A HIGH SPEED AND HIGH SECURE 16*16 SRAM ARRAY USING 22mm	2020

Guiding Ph. D Students – NIL

**Books Published No.:** 

# **R&D** and Consultancy

# **Curriculum Design and Development**

## **Extra Curricular Activities:**

## **Declaration**

I hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date: 29-07-2022 MANNE GALEEB

Place Gudlavalleru Signature