



## ABOUT THE COLLEGE

Seshadri Rao Gudlavalleru Engineering college is promoted by AANM & VVRSSR Educational Society, Gudlavalleru and was established in 1998. The college is situated in Gudlavalleru, surrounded by lush green fields, providing serene atmosphere to learners. The college offers B.Tech. programmes in eight disciplines of Engineering viz, Civil, Electrical & Electronics, Mechanical, Electronics & Communication, Computer Science, Information Technology, Artificial Intelligence & Data Science (AI&DS), Internet of Things, and Artificial Intelligence and Machine Learning (AI&ML) with a total intake of 1320. It also offers five PG programmes in Engineering (M.Tech.) and Business Management (MBA). The college is permanently affiliated to JNTUK, Kakinada and obtained the Autonomous status from UGC in the year 2014 under TIER – I and the status extended till 2025. All the eligible branches of engineering are accredited by NBA, New Delhi. The Institution is continuously striving for excellence by adopting changes to improve the systems, practices and performance levels.

## ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering started functioning from the academic year 1998 for B.Tech Course. The department is also offering M.Tech course in VLSI & Embedded Systems from the academic year 2019-2020. Faculty members are Doctorates / Post graduates in their respective fields and are actively involved in research work and regularly publish their research in reputed journals and conferences. Faculty, as a team, aims to remain focused, promote innovative learning and a network of like minded persons for success. The Department is recognized as a Research centre by JNTUK, Kakinada. The department has well qualified staff specialized in Microelectronics, Instrumentation and Control, Digital Systems & Computer Electronics, Digital Communication and Networks, Systems & Signal Processing, Microwave Engineering, VLSI Design. Department Labs are equipped with specialized equipment like spectrum analyzer, Digital Storage Oscilloscope, Universal Programmer, Curve Tracer, DSP, VLSI & Embedded System Development Kits supported by software like CCS (Code Composer Studio) for DSP, MATLAB, Aldec VHDL, Xilinx vivado, FPGA Express, Altera Quartus-II, Mentor Graphics (Front End, Back End Tools), Xilinx 14.1i (Project Navigator), Modelsim, Multisim, LabVIEW, TI based embedded systems, Robotics, and IoT, etc.

## SCOPE OF THE CONFERENCE

International conference on Computational and Intelligent Techniques for Automation of Engineering Systems (CITAES'22) will be held at SRGEC, Gudlavalleru, AP India, during 29-30, July 2022. This conference is to provide an international forum for industry professionals and researchers to deliberate and state their research findings, discuss the latest advancements and explore the future directions in the emerging areas of Engineering and Technology. The conference brings both novice and experienced scientists and developers together to meet new challenges, collect new ideas and establish cooperation among research groups and to provide a platform for researchers from academia and industry to present their original works and exchange ideas, information, techniques and applications in the fields of Electronics and Communication Engineering.

We invite all leading researchers, engineers and scientists in the domain of interest from around the world. We warmly welcome all authors to submit your research papers to CITAES-22, and share the valuable experiences with the scientist and scholars around the world.

## Call for papers

Authors are invited to submit their original research papers /findings for presentation in the following areas and not limited to the topics mentioned below:

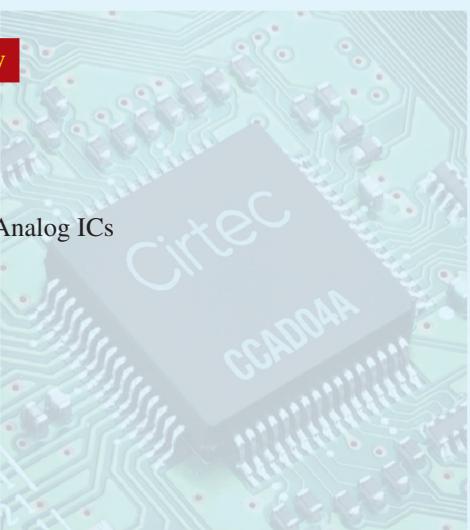
### Track 1: IOT and Embedded Computing

- Adhoc wireless sensor Networks
- IOT security
- Edge and cloud computing
- Software Defined Network
- Multi core/Multi Computer Architectures
- AI with assistive Technologies
- Real time operating systems
- Robotics / Mechatronics
- Assistive Technologies
- Embedded networking
- Human-Machine Interface, IIOT



### Track 2: Signal and Image Processing

- Audio and Acoustic Signal Processing
- Multimedia Signal Processing
- Design and Implementation of Signal Processing Systems
- Image and Video Processing
- Signal Processing for Communications and Networking
- Adaptive Signal Processing
- Biomedical Signal Analysis
- Biomedical Modelling and Informatics
- Biomedical imaging, VLSI Signal Processing



### Track 3 : VLSI Design and Nano Technology

- Reconfigurable Computing
- Testing of VLSI and Low power VLSI
- Network on Chip and System on Chip.
- Reconfigurable Architectures
- Design Modelling and Simulation of Digital/Analog ICs
- VLSI Circuits and Systems
- CAD for VLSI
- Interconnect Modelling and Algorithms
- Micro-Electro-Mechanical Systems
- Mixed Analog / Digital Systems
- Quantum computing
- ASIC Design

### Track 4 : Networks and Communications

- Ad-hoc and wireless Sensor Networks
- Wireless Communications and Networks
- Information and Coding Theory
- Cognitive Radio and Networks
- Optical Communication and Networking
- Communications and Information Security
- Communication for Intelligent transportation



### Track 5 : Antennas and Microwave Engineering

- Microstrip Antenna Design
- Antenna Configuration for 5G MIMO Communications
- Microwave Theory and Techniques
- Antenna Theory and Design
- Terahertz Communication Technologies
- Smart Antenna Design
- Cell Phone/Tower Radiation Hazards and Solutions
- Microwave in Medical Applications : Antenna Design and Characterization
- Antenna Applications for Defence Industry

### Track 6: Software Engineering and Emerging Technologies

- Artificial Intelligence Algorithms
- Fuzzy and Neural Systems
- Artificial Intelligence Tools and Applications
- Pattern Recognition
- Parallel Processing
- Soft Computing Techniques
- Machine Learning for Signal Processing
- Software Inspection, Verification & Validation
- Software Sizing and estimation techniques
- Application of AI and Soft Computing techniques to power systems
- Electronic Measurements & Instrumentation
- Bio Medical Applications
- Electrical Vehicles
- Solar based Applications
- Automotive Applications

### Important Dates

Paper Submission	2 <sup>nd</sup> May, 2022
Notification of acceptance	27 <sup>th</sup> May, 2022
Submission of Camera Ready	
Paper and Author Registration	10 <sup>th</sup> June, 2022
Conference Dates	July 29-30, 2022

Authors intended to submit their research findings, must follow the guidelines given below:

1. Each paper must contain Background, Motivation, Objective, Statement of Contribution, Methodology, Results, Discussion and Conclusion.
2. Papers should be strictly confined to the IEEE format and limited to eight pages in length.
3. All Accepted, registered, and presented papers will be communicated to Scopus Indexed / UGC Approved / Peer Reviewed Journal, as per journal policies for publication.
4. All contributed papers will be peer reviewed and verified for Plagiarism, which must not be above 10-15% (without references).
5. The publication charges are to be borne by the author as per guidelines of the journal.
6. Few papers would be allowed as poster presentations.

### Submission of Full length papers through

- a) Conference Website : <http://www.citaes.com>
- b) College Website : [www.gecgudlavalleru.ac.in](http://www.gecgudlavalleru.ac.in)
- c) Paper submission E-mail : [citaes2k22@gmail.com](mailto:citaes2k22@gmail.com)

### Best paper award

Based on the evaluation of the track chair and evaluation panel members, one of the papers will be awarded as Best Paper for each track.

### Best poster award

The authors can present their posters on the same tracks mentioned for paper presentation, in line with the theme of the conference. Best posters will be awarded as per the evaluation by the track chair and panel members.

### Registration

- Registration fee is payable through online only, followed by given registration link in the conference website [www.citaes.com](http://www.citaes.com)
- Registration fee includes fee for Proceedings PDF, and Certificate of participation.

### Registration fee of an individual delegate is as follows:

Conference Registration Fee		
	Author	Listener
<b>Research Scholar</b>	<b>Rs. 1000/-</b>	<b>Rs. 500/-</b>
<b>Academician</b>	<b>Rs. 1000/-</b>	<b>Rs. 750/-</b>
<b>Industrial Person</b>	<b>Rs. 2000/-</b>	<b>Rs. 1000/-</b>
<b>Foreigner</b>	<b>USD \$40</b>	<b>USD \$20</b>

Name of the Journal/Proceedings	International Journal of Electrical and Electronic Engineering & Telecommunications (IJEETC)-Scopus Indexed	\$250 (USD)
GECS International Journal of Innovation Technology and Management (Print Journal with ISSN)		Rs.3000/-
Journal of Information and Computational Science (UGC Care Group - II)		2000 (INR)
International Journal of Computers and Information (IJCI)	Peer Reviewed & Free	

\*As per the choice of the author, selected papers will be recommended to the journal for possible publication as per journal policies.

### Bank Details :

Beneficiary Name	Principal, Gudlavalleru Engineering College
Bank Name	State Bank of India
Account Number	35339035882
IFSC Code	SBIN0001461
Branch Location	Gudlavalleru
Country	India