FACULTY PROFILE

M. DEEPIKAKRISHNA

Department: Electronics and Communication Engineering
College: Gudlavalleru Engineering College
Mobile: 9391248330
E-Mail: deepikakrishna5555@gmail.com

Address for Communication

College Address:

M. DeepikaKrishna
Assistant Professor,
ECE Department,
Gudlavalleru Engineering College,
Gudlavalleru, Krishna District,
Andhra Pradesh: 521356

Permanent Address:

M. DeepikaKrishna
 D.No: 7-164/3,
Near Ramalayam,Annavaram,
Mudinepalli mandal and post
 Krishna District
Andhra Pradesh: 521325

Academic Qualifications

S. No	Name of the Degree	University	Percentage of Marks/Grade	Specialization	Year of Pass
1	M.Tech	JNTUK	82.86	VLSI &EMBEDDED SYSTEMS	2019
2	B.Tech	JNTUK	79.97	E.C.E	2017
3	Intermediate	Board of IntermediateEducation	93.7	M.P.C	2013
4	SSC	Board of Secondary Education	89	-	2011

Professional Experience

S. No.	Designation	Institution Name	Working Period	
			From	**To**
1	Assistant Professor	Gudlavalleru Engineering College	21-11-2019	Till date

Professional Body Membership

S. No.	Name of the Professional body	Membership Number
1	nil	nil

Papers Published in Journals No. : 01

1) **M.Deepikakrishna**, K.Rama Devi "Fault Tolerance Design Techniques For Parallel Matrix Vector Multiplications" International Journal of Modern Electronics and Communication Engineering , Volume 7, Issue 5, September-2019; ISSN 2321-2152

Papers Published in Conferences No. : 03

1) **DeepikaKrishna Muppidi**, Venkata Satyanarayana Bokka, Vasavi Sai Lakshmi Vyshnavi Bodinapalli, Lavanya Amballa "Duty Cycle Corrector For Wide Range Applications" National Conference on VLSI Design, Signal Processing, Image Processing ,Communications and Embedded Systems, NCVSPICE2K18 on 8th & 9th July 2018 at JNTUK, KAKINADA.

2) Venkata Satyanarayana Bokka, **DeepikaKrishna Muppidi,** Vasavi Sai Lakshmi Vyshnavi Bodinapalli, Sai Dinesh Ponnada "Design of 8X3 Encoder with Low Quantum Cost and Delay" National Conference on VLSI Design, Signal Processing, Image Processing ,Communications and Embedded Systems, NCVSPICE2K18 on 8th & 9th July 2018 at JNTUK, KAKINADA.

3) **DeepikaKrishna Muppidi,** Sai Dinesh Ponnada, Venkata Satyanarayana Bokka, VS L Vyshnavi Bodinapalli,"Design of 5 X32 Decoder Using Reversible Logic." National Conference on VLSI Design, Signal Processing, Image Processing, Communications and Embedded Systems, NCVSPICE2K18 on 8th & 9th July 2018 at JNTUK, KAKINADA.

Workshops Organized No. : NIL

Workshops /Conferences /Seminars Attended No. : 02

1) Attended the two day workshop on "PCB DESIGNING" at Dhanekula Institute of Engineering and Technology , Vijayawada, organized by QUE Technologies during 17-18 November 2014.
2) Attended a two day workshop on "Robotics" at Dhanekula Institute of Engineering, and Technology Vijayawada, organized by Spark Lab Engineering System during 5th and 6th March 2015.

Certifications/Training Programs Attended No. : 03

1) Attended a one week FDP on "Improving Teaching Skills In Electronic Devices and Circuits" during 27 to 31st March 2018 at University College of Engineering Kakinada, JNTUK.
2) Attended one week Training Program of "Software Project Development through Python" during 6 to 11th July 2018 at University College of Engineering Kakinada, JNTUK.
3) Attended a one week FDP on "Improving Teaching Skills In Electronic Devices and Recent Trends in Communication Engineering (Lab VIEW, FEKO, XILINX)" during 04 to 09th February 2019 at University College of Engineering Kakinada, JNTUK.

Online FDP Attended No.:07

1) Attended faculty development programme on "Digital Image Processing using MATLAB" from 24th to 27th April 2020 hosted on skill to hire.
2) Attended two day online fdp on "Microstrip Antennas for Advanced wireless Communications" organized by Department of Electronics and Communication engineering,in association with items by Kallam Haranadhareddy Institute of Technology from 15th to 16th May 2020.
3) Attended one week online FDP on "Advanced Antenna Design using HFSS" organized by Department of Electronics and Communication Engineering, Santhiram Engineering College from 12th may to 17th May 2020.
4) Attended five day online faculty development programme on "Advanced Python Programming using Django" organized by department of computer science and engineering in association with data point solutions by Malineni Lakshmaiah Women's Engineering college from 21st to 25th May 2020.
5) Attended online fdp on "Embedded Systems" organized by APSSDC from 25th may to 6th June 2020.
6) Attended Faculty Development Programme on "Recent Trends and Future Applications in Electronics and Communication Technologies" organized by MLR institute of technology from 29th may to 2nd June 2020.
7) Attended one week international Online Knowledge development program on "challenges and advancements in the design of IOT, VLSI and Embedded Systems: A

Researcher View", organized by Department of Electronics and Communication Engineering of Gudlavalleru Engineering College during 8th to 13th June 2020.

Online Webinars Attended No.:07

1) Attended a webinar on "Analog IC Design using Mentor EDA Tools" organized by coreEL Technologies and Mentor on 10th April 2020.
2) Attended the IT live webinar on "How to Prepare a Good Research Grant Proposal" organized by Department of Information Technology, PSNA College of Engineering and Technology on 20th May 2020.
3) Attended webinar on "Design Approaches for Frontend Design" organized by Department of Electronics and Communication Engineering by Vaagdevi College of Engineering on 23rd May 2020.
4) Attended a webinar on "Radio frequency Measurements for cellular and wireless Communication Systems" organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College on 25th may 2020.
5) Attended webinar on "Writing an Effective Research Paper – Means and Methods" organized by Department of Computer Science and Engineering by CMR Engineering College on 29th May 2020.
6) Attended a webinar on" Impact of Covid in Engineering Education and Way Forward" organized by ISTE chapter,Gudlavalleru Engineering College on 9th june 2020.
7) Attended "Three day National Level Webinar series on VLSI" organized by Department of Electronics and Communication Engineering of Andhra Loyola Institute of Engineering and Technology from 10th to 12th June 2020.

OnlineWorkshops Attended No.:01

1) Attended workshop on "VLSI circuits Analog and Digital Design perspectives", organized by department of ECE, Jntua College of anantapuramu in association with AMS Semiconductors and HCL Technologies during 16th May to 17th May 2020.

NPTEL Certifications: NIL
Guest Lectures Delivered No. : NIL

Subjects Handled:
Under Graduation:
- Elements of Electronics Engineering.

Labs Handled: (UG) :
- Elements of Electronics Engineering.
- VLSI
- Digital Circuit Design

Post Graduation: NIL
Labs Handled: (PG): NIL

Other Responsibilities: NIL
Projects Guided

Under Graduation: 01

 1) Design and Implementation of Different Types of Efficient Parallel Prefix Adders.- 2019
Post Graduation: NIL

Guiding Ph. D Students: NIL

Books Published No. : NIL
R&D and Consultancy: NIL

Curriculum Design and Development: NIL

Academic Rewards / Achievements:
Secured State Rank 91 in A.P.R.J.C- 2011
Secured State Rank 31 in PGECET -2017
Qualified as Assistant Professor in UGC NET held in Dec-2018.
Extra Curricular Activities
1. Attended ANNUAL TRAINING CAMP –X held at Agricultural Market Yard , Nuzvid from 24 Nov 2011 to 03 Dec 2011
2. Attended COMBINED ANNUAL TRAINING CAMP – I I held at Agricultural Market Yard ,Nuzvid from 08 July 2012 to 17 July 2012
3. Qualified N.C.C 'B' and 'C' Certificate Examinations With B.Grade
Declaration
    I hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date: 21-08-2020   Signature

Place: Gudlavalleru   (M.Deepikakrishna)