

BHUPATHI AJAY KUMAR

Department: Electronics and Communication Engineering
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RESIDENTIAL ADDRESS:

B.Ajay kumar,
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ACADEMIC QUALIFICATIONS:

Pursuing PhD at JNTU Kakinada.

Master of Engineering in Electronics and instrumentation.

Bachelor of Engineering in Electronics and Communication Engineering

ACADEMIC REWARDS / ACHIEVEMENTS:

- Stood **College First (state 8th rank)** in Inter First Year.
- I got the price money of **1000 Rs** for 2nd and 4th year **TOPPER**.
- Stood **College first** in B.Tech final academic year
- Stood class **first in 1st** semester of M.E.

PROFESSIONAL EXPERIENCE:

| Designation | Institution | Period |
|---------------------|----------------------------------|--------------------------|
| Assistant Professor | Gudlavalleru Engineering College | 16-06-2014 to till date |
| Assistant Professor | VIET Engineering college | 01-10-2013 to 13-06-2014 |

PROJECTS GUIDED:

| PhD | Post Graduation | Under Graduation |
|------------|------------------------|-------------------------|
| --- | 03 | 07 |

SPECIALIZATION: Electromagnetic fields and Antenna communications.

RESEARCH BOOKS:NIL

BOOKS PUBLISHED IN PUBLICATIONS: NIL

PAPERS PUBLISHED IN JOURNALS: 04

1. **Bhupathi Ajay Kumar, S Aruna kumari**, published a paper on “**A Novel Scheduling Model for Fast Aggregation in Wireless Sensor Networks**”. In Global Journal of Advanced Engineering Technologies Volume 6, Issue 2-2017.ISSN (Online):2277-6370 & ISSN (Print): 2394-0921.
2. **B. Ajay Kumar, V. Gopi**, Published a paper on “**Channel hopping Algorithm in Cognitive Radio Networks**” in International Journal of Advanced Research in Computer and Communication Engineering ISO 3297:2007 Certified Vol. 5, Issue 8, August 2016.
3. K. Rushendra babu, **B. Ajay Kumar, R. Chandrasekhar**, Published a paper on “**Convergence Rate Improvement and Interference Reduction in Smart Antenna System.**” in **IJAREEIE**, Volume 5, Issue 2, February 2016.
4. P. Vidyullatha, R. Chandrasekhar, B. Ajay Kumar, Published a paper on “**Performance Analysis of PAPR in STBC MIMO-OFDM System under Different Modulation Schemes**” in International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol. 5, Issue 1, January 2016

PAPERS PUBLISHED IN CONFERENCES:02

1. **B. Ajay Kumar** “**Cooperative Spectrum Sensing in Cognitive Radio Networks using Fuzzy logic**” in the track of Communication of the “**6th International Conference on Innovations in Electronics and Communication Engineering (ICIECE-2017)**” held during 21st & 22nd July 2017.
2. R. Chandrasekhar, **B. Ajay Kumar, K. Rushendra Babu, M. Kamaraju**, “**Optimization of Peak to Average Power Ratio Reduction Using Novel Code for OFDM Systems**” Microelectronics, Electromagnetic and Telecommunications, Lecture Notes in Electrical Engineering 372(Springer), DOI 10.1007/978-81-322-2728-1_24, pp:267-275,2016.

LIST OF WORKSHOPS /CONFERENCES /SEMINARS ATTENDED:

1. Attended a seminar on “**Advances in Communication, Navigation & Computer Networks (ACNCN-2012).**” held at Andhra University college of engineering on March 17th and 18th, 2012.
2. Attended a one day work shop on “**Research in Energy Management Wireless Networks & Smart Transportation system.**” organized by The Institution of Engineers (India), Vijayawada Local Centre, Vijayawada- 520 002 on 29th March 2014.

3. Attended a one day workshop on “***Speech Processing: Current Challenges And Hands-On Experience***”. On 23rd August, 2014 Organized by Signal Processing and communications Research Group, Department of Electronics and Communication Engineering, Gudlavalleru Engineering College.
4. Attended a one day work shop on “***Research in Signal and Image Processing on FPGA’s and embedded flat forms.***” Organized by The Institution of Engineers (India), Vijayawada Local Centre, Vijayawada- 520 002 on 22nd November 2014.
5. Attended a one day workshop on “***Ultra Low Power Communication using CC430 Microcontroller: Hands on Experience***”. On 7th February, 2015 Organized by Signal Processing and communications Research Group, Department of Electronics and Communication Engineering, Gudlavalleru Engineering College.
6. Participated in a two week ISTE STTP on “***Introduction to Design of Algorithms.***” Conducted by Indian Institute of Technology Kharagpur from 27th April, 2015 to 30th May, 2015.
7. Attended a one week refresher course on “***Microprocessors & Microcontrollers***” held during 17-21 November, 2015 at Gudlavalleru Engineering College organized by Research and Development Center, Department of ECE.
8. Participated in the faculty development programme (FDP) on “***Development of Wireless sensor networks using Network simulator (NS2) – Hands on experience***”. organized by Communication Research Group in Association with Vertex soft, Hyderabad on 1st -5th December 2015.
9. Participated in the Workshop on “***Design and simulation of Antennas & Microwave Devices using ANSYS HFSS***”. Under TEQIP-II, organized by Department of Electronics and Communication Engineering, SVECW in collaboration with ENTUPLE Technologies held from 3rd to 7th October, 2016.
10. Participated in one week faculty development programme (FDP) on Hands on Experience In “***signal processing applications***” organized by signal processing Research Group, Department of ECE, Gudlavalleru engineering college on 7th -12th November 2016.
11. Participated in a two week ISTE STTP on “***Cmos, Mixed signal and Radio frequency VLSI Design.***” Conducted by Indian Institute of Technology Kharagpur in association with Gudlavalleru Engineering college from 30th January to 4th February, 2017.
12. Attended a five day short term course on “***Trends in VLSI, Communications and Signal processing (TVCoS-2019)***” during 5th -9th march 2019 in the department of Electronics and Communication Engineering at National Institute of Technology Andhra Pradesh (NIT Andhra), Tadepalligudem.
13. Attended a one day training program on Faculty Orientation Program conducted by ***JNTUK centre of Excellence for e-Resource Development & Deployment (CoEeRD)*** and Globarena Technologies Pvt.Ltd. held at LBCE on 22 August 2014.

14. Two day training program on “*Training Teachers for Excellence*”, organized by Gudlavalleru Engineering College, Gudlavalleru during 19th- 21st August 2014 and 29th - 31st December 2014.
15. Participated in a Two-Day Training program on “*RF circuit Design using ADS software: Hands on Experience.*” Under TEQIP-II, organized by Department of Electronics and Communication Engineering, SVECW in collaboration with Agilent Technologies held during 17-18 June 2015.
16. Two day training program on “*Digital Image Processing using MATLAB*” organized by Computer Centre, MIT Campus, Anna University, Chennai during 20th-21st June 2015.
17. Attended a Two day staff training program on “*Simulation of Linear & Digital Integrated Circuits, Microcontroller Based Applications using NI Multisim*”, held during 4th -5th November 2016 at Gudlavalleru Engineering College, Gudlavalleru.
18. Attended a Two day staff training program on “*Advances in VLSI Design and Embedded Systems*”, Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 6th-7th November 2017.
19. Participated in a one week faculty development program on “*Communications and signal processing*”, organized by department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 29th October to 3rd November 2018.
20. Attended a Two day training program on “*Design and Development of Digital Circuits Using VLSI Tools*”, Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 6th-7th December 2018.
21. Participated in online faculty development program on “*Artificial Intelligence and its Applications*” conducted by Department of Computer Science and Engineering, in collaboration with 360Digi TMG from 25th may 2020 to 30th may 2020.
22. Awarded certificate of Appreciation to successfully completed the **National Level E-Quiz contest organized by IEEE AITAM Student’s branch** Electromagnetic waves and Transmission Lines on July 20, 2020.
23. Participated in online faculty development program on “*Research Methodology*” conducted by Department of Mechanical Engineering, Rajgd dnyanpeeth’s Shri chatrpati shivajirje college of Engineering, Pune-412206 during the period of 20-5-2020 to 25-5-2020 with a score of 89%.
24. Participated in one week online workshop on **Blended E- Learning** organized by the Department of Computer Science and Engineering, JNTUK University college of Engineering Vizianagaram in association with Computer Society of India (Region-V) from 6th to 10th July 2020.

