

FACULTY PROFILE

Name of the faculty: Ramya P

Department: ECE

College: Gudlavalleru Engineering College

Mobile: 9704874848

E-Mail: ramya.sikindhar@gecgudlavallerumic.in



RESIDENTIAL ADDRESS: Gudivada. 521301

ACADEMIC QUALIFICATIONS: M.Tech, pursuing Ph.D. at NIT,AP.

ACADEMIC REWARDS / ACHIEVEMENTS:

- GATE 2011 qualified.
- UGC NET JUNE 2014 JRF qualified.
- Ratified by JNTUK, Kakinada
- 4 Excellence awards (Infosys)
- Received letter of appreciation for the subject - Real Time Operating Systems.

PROFESSIONAL EXPERIENCE:

Designation	Institution	Period
Assistant Professor	Gudlavalleru Engineering College	March 1 st 2014 to till date
Senior Software Engineer	Infosys Technologies	4 years Dec 17 th 2007-Nov 30 th 2011
Embedded Engineer	Network Informatics	1.4 years Aug 7 th 2006- Nov 30 th 2007

PROJECTS GUIDED:

PhD	Post Graduation	Under Graduation
-	3	7

SPECIALIZATION: Embedded Systems

RESEARCH PAPERS / BOOKS:

PAPERS PUBLISHED IN JOURNALS:

[1]. Ramya.P, Radha.K, “Design of Efficient Transport Protocol for Multimedia through Wireless Networks”, *International Journal of Recent Technology and Engineering*, Vol.2, Issue 3, pp 55-58, July 2013.

[2]. P.Ramya , K.Rambabu, Y.Sudhakar, “ Design and analysis of 128 bit Area Efficient Carry Select Adder”, *International Journal of Applied Engineering Research*, Vol.9, Number 21 , pp 11185-11194,2014.

[3]. Ramya.P, Priyadarshini.V, Sai Ram Vamsi.T, “Customizing AOSP on ARM,MIPS,x86 Platforms,*International Journal of Engineering Associates*, Vol.3, Issue 7,pp 20-23,2014.

[4]. Ramya.P, Priyadarshini.V, “Design of Low Power Energy Efficient Full Adder Circuits”, *IJISET*, Vol.1, Issue 6, pp. 563-569, August 2014.

[5]. Ramya.P, Priyadarshini.V, “Design of 32-bit ALU using Low Power Energy Efficient Full Adder Circuits”, *IJCTA*, Vol.5, pp 1754-1762, sep-oct 2014.

[6]. Ramya.P, Priyadarshini.V, Chandu.P, “Internet of Everything and Secure Cloud for Real World Data Management”, *International Journal of Computer Applications*, Vol. 104, Issue 13, pp 9-11, October 2014.

[7]. Ramya.P, Bhavani S, Priyadarshini.V, “An Improved Energy Efficient and Certificate Revocation Technique for Mobile Ad Hoc Networks”, *International Journal on Computing, Communications and Systems (IJCCS)*, Vol. 4, Issue 2, pp 30-33, December 2015.

- [8]. P.Ramya, G.Sri kanya, “ESDIT:An Approach for Improving Performance in MANET by Detecting and Isolating Selfish Nodes”, *International Journal of Advanced Research in Computer and Communication Engineering (IJARCCE)*, Vol. 5, Issue 7, pp 685-689, July 2016.
- [9] Ramya.P, Sairam Vamsi T , G.R.L.V.N.S. Raju , “Design of Intelligent Accident Avoiding System using GSM & GPS”, *International Journal for Modern Trends in Science and Technology* , Vol. 03, Issue 07, pp 310-314,July 2017,
- [10] Ramya.P, Sairam Vamsi T , “Impact analysis of blackhole, flooding and grayhole attacks and enhancing security in Mobile Ad hoc Networks using SHA3 algorithm” , Springer LNEE. pp 639-647
- [11] Ramya.P, Sairam Vamsi T, Dr.G.R.L.V.N.S Raju “A Survey on Underground Distributed Wireless Sensor Networks:Design & Research Challenges” IRACST – International Journal of Computer Networks and Wireless Communications (IJCNWC), ISSN: 2250-3501 Vol.7, No 6, Nov-Dec 2017.
- [12] Ramya.P, Sairam Vamsi T , “ Securing MANETs Using SHA3 Keccak Algorithm” , *ICICCT 2019 – System Reliability, Quality Control, Safety, Maintenance and Management*, ICICCT 2019, **Springer**, Singapore, pp. 328-335, June 2019.

PAPERS PUBLISHED IN CONFERENCES/WORKSHOPS:

- [1]. P.Ramya, K.Radha, “An Efficient Approach for Multimedia Streaming through Wireless Networks”, 2nd *International Conference on Innovation in Electronics and Communication Engineering (ICIECE-2013)*, GNIT, Aug 9th -10th, 2013.
- [2]. Ramya.P, Bhavani S, Priyadarshini.V, “An Improved Energy Efficient and Certificate Revocation Technique for Mobile Ad Hoc Networks”, *IEEE sponsored 4th International Conference on Communications, Signal Processing, Computing and Information Technologies (ICCSPCIT-2015)*, MRCET, December 18th -19th, 2015.
- [3] Ramya.P, Sairam Vamsi T , G.R.L.V.N.S. Raju , “Design of Intelligent Accident Avoiding System using GSM & GPS”, *International Conference on Recent Advances in Electronics and Communications*, ICECE 2017, CIET, 7th – 8th July 2017.

- [4] Ramya.P, Sairam Vamsi T, Manideepthi.Kolli, “ Cryptographic approach for defending vulnerabilities in MANETS”, Springer, 6th International Conference on innovations in Electronics & Communication Engineering, ICIECE-2017, GNIT, July 21st -22nd, 2017.
- [5] Ramya.P, Sairam Vamsi T , “Impact analysis of blackhole, flooding and grayhole attacks and enhancing security in Mobile Ad hoc Networks using SHA3 algorithm” , Springer, 3rd International Conference on Micro-Electronics, Electromagnetics and Telecommunications, ICMEET 2017, BVRIT, 9th – 10th September 2017 .
- [6] Ramya.P, Sairam Vamsi T, “Securing MANETs using SHA3 Keccak Algorithm”, Springer, International Conference on Intelligent Computing and Communication Technologies (ICICCT – 2019), BVRIT, 9th -11th January 2019.
- [7] Ramya.P, Padmalatha L, “IoT BASED HEALTH MONITORING SYSTEM (IHMS)”, 4th International and 19th National Conference on Machines and Mechanisms (iNaCoMM 2019), Springer, IIT Mandi, 5th – 7th December 2019.

LIST OF WORKSHOPS ORGANIZED:

- Presented almost 30 knowledge Transfer Sessions (workshops) for Microsoft Team members. (Industry).
- Coordinated workshop on “Embedded Systems Design & Programming”.
- Conducted practical sessions for M.Tech students on Linux programming.
- Coordinated “Job oriented training” for M.Tech students.

LIST OF WORKSHOPS /CONFERENCES /SEMINARS ATTENDED:

- One day workshop on “Speech processing: current challenges and hands-on experience” on 23rd Aug 2014 at Gudlavalleru Engineering College.
- One day workshop on “Ultra Low Power Communication using CC430 Microcontroller: Hands-on experience” on 7th Feb 2015 at Gudlavalleru Engineering College.
- One day workshop on “Open Hardware” on 6th Feb 2015 at Koneru Lakshmiah University, Vijayawada.
- Two day FDP on “Research Challenges and Issues in Big Data using Hadoop Framework” on 12th -13th Mar 2015 at Gudlavalleru Engineering College.
- One day Workshop on “Research in Energy Management Wireless Networks and Smart Transportation Systems” organized by the Institute of Engineers (IE), INDIA, Vijayawada local Center, Vijayawada on 29th March 2015.
- One day workshop on “Embedded Technologies” on 30th October 2015 at V.R.Siddhartha Engineering College, Vijayawada.

- One week FDP on “Development of Wireless Sensor Networks using Network Simulator(NS2) – Hands on Experience on 1st-5th December 2015 at GEC, Gudlavalleru.
- 2nd *International Conference on Innovation in Electronics and Communication Engineering (ICIECE-2013)*, GNIT, Aug 9-10, 2013.
- *IEEE sponsored 4th International Conference on Communications, Signal Processing, Computing and Information Technologies (ICCSPCIT-2015)*, MRCET, December 18-19, 2015.
- One week refresher course on “Microprocessors and microcontrollers” on 17th – 21st November at GEC, Gudlavalleru.
- Three day FDP on “VLSI design techniques using Xilinx & Mentor Graphics EDA tools” on June 23rd – 25th, 2016 at Andhra Loyola Institute of Engineering and Technology, Vijayawada.
- Two day staff training program on " Simulation of Linear & Digital Integrated Circuits and Microcontroller based Applications using NI Multisim” on 4th -5th November 2016 at Gudlavalleru Engineering College, Gudlavalleru.
- One week Faculty Development Programme on “Hands on Experience in Signal Processing Applications” on 7th-12th November, 2016 at Gudlavalleru Engineering College, Gudlavalleru.
- Two day national level workshop on “Research Methodologies” on 3rd – 4th February 2017 at Ramachandra College of Engineering, Eluru.
- Two day training programme on “Mentor Graphics Tools” on 31st August-1st September 2017 at Gudlavalleru Engineering College,Gudlavalleru.
- Two day faculty training programme on “Advances in VLSI Design and Embedded Systems” on 6th -7th November 2017 at Gudlavalleru Engineering College,Gudlavalleru.
- One week national level Faculty Development Programme on “Signal and Image Processing using LabVIEW” on 13th – 18th November 2017 at Gudlavalleru Engineering College,Gudlavalleru.
- Two day Hands-on workshop on “IPv4 & IPv6” on 24th – 25th November 2017 at Gudlavalleru Engineering College, Gudlavalleru.
- One week Faculty Development Programme on “Communications and Signal Processing” on 29th October - 3rd November 2018 at Gudlavalleru Engineering College,Gudlavalleru.
- One day workshop on “NPTEL Awareness Program” on 6th December 2018 at Gudlavalleru Engineering College,Gudlavalleru.
- One week Faculty Development Programme on “Real Time Embedded Systems and IoT, Its Applications” (RESIoT) on 17th – 22nd December 2018 at Vasavi College of Engineering, Hyderabad.
- NPTEL AICTE FDP on “Introduction to Internet of Things”.
- NPTEL AICTE FDP on “Introduction to Industry 4.0 and Industrial Internet of Things”.

- One week FDP on " Sensor Networks & IoT" on 26th - 31st August 2019 at VNR Vignan Jyothi Institute of Engineering and Technology (EICT NIT Warangal).
- One week online FDP on "Data science and Cloud Computing" on 18th - 22nd May 2020 by Chebrolu Engineering College/
- One week online FDP on "Artificial Intelligence" on 22nd May - 26th May organized by Gayathri Vidya Parishad College for Degree and PG Courses in collaboration with Brain O vision.
- One week International online Knowledge Development Program on " Challenges and advancements in the design of IoT , Embedded and VLSI systems: A researcher's view" on 8th - 13th June 2020 organized by Gudlavalleru Engineering College, Gudlavalleru.
- One week refresher course on " Embedded System Design" on 27th July - 1st August organized by Gudlavalleru Engineering College, Gudlavalleru.
- One week online FDP on "Cyber Security" on 22nd-26th July organized by Gudlavalleru Engineering College, Gudlavalleru.(Cyberpsy)
- Webinar on “Value of IEEE Content in Engineering Education and Research" on 14th July 2020 by EBSCO.
- A National Level Webinar On Intellectual Property Rights & Patents - A View”, during 10th - 12th August, 2020 by Gudlavalleru Engineering College, Gudlavalleru.

CERTIFICATIONS/Training Programs Attended No.:

1. Training program on “Training Teachers for Excellence”, organized by Gudlavalleru Engineering College 19.8.14-21.8.14 & 29.12.14-31.12.14.
2. 8 Infosys certifications (related to Product Engineering)
3. Faculty Industrial Training program on “ARM Controller Programming” during 11th June 2018 to 11th July 2018 at Efftronics Systems Pvt. Ltd. At Vijayawada.
4. NPTEL certification on “Real Time Operating Systems” with Elite.
5. NPTEL certification on “Introduction to Internet of Things” with Elite.
6. NPTEL certification on “Introduction to Industry 4.0 and Industrial Internet of Things” with Elite + Silver

7. AICTE sponsored online training program on "Blockchain Architecture Design and Use cases " Phase I on 10th - 15th August organized by Gudlavalleru Engineering College.

GUEST LECTURES DELIVERED: ----

MEMBERSHIP IN PROFESSIONAL BODIES: ----

S. No.	Name of the Professional body	Membership Number
1	The Institution of Engineers India (IE)	AM162862-6

CONSULTANCY ACTIVITIES: ----

GRANTS FETCHED: ----

Subjects Handled

Under Graduation

- Computer Architecture and Organization
- Real Time Operating Systems
- Computer Networks
- Embedded Real Time Operating Systems
- Microcontrollers and Embedded Systems
- Computer and Communication Networks

Post-Graduation

- Embedded System Design
- Embedded Real Time Operating Systems
- Embedded C
- Embedded Computing
- Advanced Embedded Systems
- Internet of Things

Labs Handled

Under Graduation

- Electronic Devices and Circuits Lab
- Microprocessors and Microcontrollers Lab
- Microprocessors and Interfacing Lab
- Electronic Devices Lab

Post-Graduation

- HDL Lab
- Embedded Systems Lab
- Advanced Digital Design Lab

Other Responsibilities

College Level

- Member in Budget team for GECFEST'15 and GECFEST'16
- Member in Registration and Reception team for GECFEST'17
- Member in Program Organizing Committee for CITAES'18

Department Level

- Online Feedback coordinator
- Online Anti-ragging Feedback coordinator

Projects Guided

Under Graduation

S. No	Name of the project	Year
1	A Web Application for Public Distribution System	2015
2	Synthesis and Simulation of a 32-bit Pipelined Processor	2016
3	Human less Boat For Coastal Area Surveillance	2017

4	IoT based Health monitoring system using Arduino	2018
5	IHMS: An IoT Based Application For Real Time Health Monitoring System	2019
6	Smart Health Care Monitoring System Using Raspberry Pi	2019
7	Intelligent Traffic Control System Using RFID and GSM	2020

Post-Graduation

S.No	Name of the project	Year
1.	ESDIT – An approach for improving performance in MANET by detecting and isolating selfish nodes	2016
2.	Design and analysis of 128 bit area efficient carry select adder	2017
3.	Identification and Isolation of various attacks using enhanced AODV routing protocol for mobile adhoc network	2017

Guiding Ph. D Students – NIL

Books Published No. :

1. Co-author for chapter 3 titled “ Microprocessors and Interfacing Laboratory” of Electronics and Communication Engineering laboratory Manual series 2 with ISBN – 978-1-4276-5564-6 in the year 2016
2. Co-author for chapter 3 titled “Digital System Design and DICA Laboratory” of Electronics and Communication Engineering laboratory manual series with ISBN – 978-1-4276-5563-9 in the year 2016
3. Co-author for chapter 7 titled “Computer Architecture and Organization” of Electronics and Communication Engineering Instructional manual with ISBN – 978-1-4276-5539-4 in the year 2015.

4. Co-author for chapter 26 titled “Structural Digital Design” of Electronics and Communication Engineering Instructional manual with ISBN – 978-1-4276-5539-4 in the year 2015.
5. Co-author for chapter 3 titled “Digital Communications Laboratory” of Electronics and Communication Engineering laboratory Manual with ISBN – 978-1-4276-5536-3 in the year 2015

R&D and Consultancy

S. No	Project Title	Source of Funding	Duration	Role	Status
1	IoT based e-health care system	In-house R & D	1 year	Investigator	Completed

Curriculum Design and Development

1. Embedded Computing
2. Internet of Things

Extra Curricular Activities: Department level activities

Declaration

I hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date: 18-8-2020

Place Gudlavalleru

Ramya P

Signature