FACULTY PROFILE

P. Anil Kumar

Department: Electronics and Communication Engineering

College: Gudlavalleru Engineering College

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Address for Communication

P. Anil Kumar

S/o Srinivasa Rao, D.No:20-700/85

Vishwabrahamana Colony

Machilipatnam

Krishna District-521001

Academic Qualifications:

S. No.	Name of the Degree	University	Percentage of Marks/Grade	Specialization	Year of Pass
1	Ph.D	Pondicherry University	-	Biomedical Signal Processing	**Pursuing**
2	M.Tech	J.N.T.U.K Kakinada	86.3	Digital Electronics and Communication Systems	2014
3	B.Tech	J.N.T.U.K Kakinada	73.5	Electronics and Communication Engineering	2009
4	Diploma	S.B.T.E.T. Hyderabad	87.2	Electronics and Communication Engineering	2008
5	S.S.C	B.S.E Hyderabad	80.2	---	2003

Professional Experience:

S. No.	Designation	Institution Name	Working Period	
			From	To
1	Assistant Professor	Gudlavalleru Engineering College, Gudlavalleru	20-05-2013	Till date
2	Assistant Professor	Gudlavalleru Engineering College, Gudlavalleru	15-06-2009	31-10-2011

Professional Body Membership:

S. No	Name of the Professional body	Membership Number
1	Institution of Engineers India	AM 162844-8

Papers Published in Journals No. : 06

1. P. Anil Kumar, Sunkara Rajeev, Veera Punnaiah Manda, *" MRI Image Reconstruction using Compressive Sensing"*, International Journal of Recent Technology and Engineering (IJRTE), ISSN: 2277-3878, Volume-8 Issue-2, July 2019, PP:5256-5260.
2. P. Anil Kumar, L. Hema Harshitha, K.Hemanth, G.Chandrika, L.Sai Spandana, *"Multi Face Detection and Recognition"*, Journal of Applied Science and Computations, Volume VI, Issue IV, April/2019, ISSN NO: 1076-5131, PP: 1594-1601.
3. P. Anil Kumar, P. Bhargavi, "LBP Based Sharpness Parameter Based Approach for Defocus Blur Acts on Robust Segmentation algorithm", International Journal of Professional Engineering Studies (IJPRES), Vol.8, Issue 4, May 2017.
4. P. Anil Kumar, P. Divya and M.Kamaraju, "Satellite Remote Sensing Image Based Aircraft Recognition Using Wavelet and Curvelet Transforms", International Advanced Research Journal in Science, Engineering and Technology (IARJSET), Vol. 3, Issue 7, July 2016, ISSN (Online) 2393-8021, ISSN (Print) 2394-1588.
5. P.Anil Kumar, A. Venkata Suresh and M.Kamaraju, "Reconfigurable Low Power FPGA Design with Low Density", International Journal of Innovative Research in Advanced Technology, Volume-2, Issue-12, ISSN: 2319-1467.
6. P.Anil Kumar, Akula Venkata Subba Rao, "Hardware Implementation of Modified Weighted Median Filtering on FPGA", International Journal of Computer Applications, Volume 105 – No. 4, November 2014, ISSN: 0975 – 8887.

Papers Published in Conferences No. : 04

1. P.Anil Kumar, M.Kamaraju, " Wireless Fingerprtint Attendance Management System", 2015 IEEE International Conference On Electrical, Computer And Communication Technologies, 05-07 March, 2015 at SVS College of Engineering, Coimbatore, Tamil Nadu, India. PP.1342-1347.
2. P.Anil Kumar, M.Kamaraju, "DSP Based Embedded Fingerprint Recognition System", IEEE Conference on Hybrid Intelligent Systems (HIS - 2013) during 04th -06th December 2013, ISBN: 978-1-4799-2439-4/13 at Yassmine Hammamet, Tunisia, pp.6-11.
3. P.Anil Kumar, M.Kamaraju, B. Ananda Krishna, B. Rajasekhar, "Embedded Fingerprint Recognition System", Recent Researches in Telecommunications, Informatics, Electronics and Signal Processing, ISBN: 978-960-474-330-8,Sept 17-19,2013 at Baltimore, MD, USA ,pp.394-401.
4. P.Anil Kumar, M.Kamaraju, B. Ananda Krishna, B. Rajasekhar, "A Novel Implementation of Fingerprint Recognition System using DSP Processor", AAMT-2013, AAMT-2013, pp. 3-6, ISSN:2278-0181, March-2013.

Workshops Organized No. : Nil

Guest Lectures Organized:

1. Organized a Guest Lecture on "Entrepreneurship Development" for III Year students on 15th December 2018.

Workshops Attended No. : 08

1. One Day Workshop on "Research in Energy Management Wireless Networks and Smart Transportation Systems" organized by the Institute of Engineers (IE), INDIA, Vijayawada local Center, Vijayawada on 29th March 2015.
2. One Day Workshop on "Ultra Low Power Communication Using CC430 Microcontroller – Hands on Experience", organized by the Department of Electronics and communication Engineering, Gudlavalleru Engineering College, Gudlavalleru on 09th February 2015.
3. Two-Week ISTE Workshop on "Control Systems", conducted by Indian Institute of Technology Kharagpur from 2nd to 12th December 2014 at Gudlavalleru Engineering College, Gudlavalleru.
4. One Day Workshop on "Speech Processing: Current Challenges and Hands-on Experience", organized by Signal Processing and Communication Research Group, Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru on 23rd August 2014.

5. Two-Week ISTE Workshop on "Signals & Systems", conducted by Indian Institute of Technology Kharagpur from 2nd to 12th January 2014 at Gudlavalleru Engineering College, Gudlavalleru.

6. A Two Day Workshop on "Issues in Integrated Circuits (ICs) Design" held during 13th to 14th December, 2013 organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru.

7. One-Week Workshop on "Training Teachers for Excellence" organized by Gudlavalleru Engineering College from 17th to 21th June 2013, conducted by Dr B.G.Barki, Former Director, NITTTR, Chennai.

8. A One Day Workshop on "Analog, Digital and Mixed Signal VLSI Design" on 15th , October 2011 organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru.

Webinars

1. A Webinar on "Virtual Classroom Management using Microsoft Teams", an online training program offered remotely on 11/05/2020, organized by Department of Computer Science & Engineering, Lakireddy Bali Reddy College of Engineering (Autonomous).

2. WEBINAR on "e-vehicles for sustainable transportation" organized by the Department of Mechanical Engineering, Geethanjali Institute Of Science & Technology Nellore on 14th May 2020.

3. A webinar on "Future of AI" on 20th May 2020 Hosted by IETE Mumbai in association with PANTECH.

4. A webinar on "Brain Computer Interface" on 5th June 2020 Hosted by NITK-STEP in association with PANTECH.

5. A webinar on "Brain Controlled Robot Design" on 6th June 2020 Hosted by NITK-STEP in association with PANTECH.

6. "Webinar on Advanced IoT Applications - A Practical Approach", an online training program offered remotely on 23rd May, 2020, organized by Department of Computer Science & Engineering, Lakireddy Bali Reddy College of Engineering (Autonomous).

7. A Technical Webinar on "Design Approaches for Frontend VLSI Design" organized by Department of Electronics and Communication Engineering, Vaagdevi College of Engineering (Autonomous).

8. A webinar on "Radio Frequency Measurements for Cellular and Wireless Communication Systems" organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College on 25th May, 2020.

9. IT Live webinar on "How to Prepare a Good Research Grant Proposal?" on May 20, 2020, organized by Department of Information Technology, PSNA College of Engineering and Technology.

10. Webinar on "Writing an Effective Research Paper-Means and Methods" organized by Department of Computer Science & Engineering, CMR Engineering College on 29th May 2020.

11. A webinar on "Development of Deep Learning Architecture" on 25th May 2020 Hosted by IETE Mumbai in association with PANTECH.

12. Webinar on "Intellectual Property Rights and their Importance" organized by K L University, Andhra Pradesh dated on 02 June 2020.

13. A Live Webinar on "Smart Manufacturing and Industry 4.0." held on 06th June 2020 Organized by the Department of Electronics and Communication Engineering, Mekapati Rajamohan Reddy Institute of Technology & Science.

14. National Level Webinar on "Physical Activity, Immunity and Covid-19" organized by A. V. Patel Commerce College on June 5th, 2020.

15. "Faculty Awareness Programme on Outcome Based Education (OBE) and NBA Accreditation" organized by Sinhgad Institute of Technology and Science, Narhe, Pune on 11-5-2020.

16. Faculty Program on NBA on 5/12/2020, organized by Bharati Vidyapeeth College of Engineering, Navi Mumbai-400614.

17. One Week Online Faculty Development Program on "Research Challenges and Opportunities Post COVID-19 (RECOP 2020)" organized by Research & Development Cell and Institution's Innovation Council of Sri Vasavi Engineering College (Autonomous) during 4-9 May 2020.

18. Quiz on "Matlab Skill Test" organized by Department of Electronics and Communication Engineering, Raghu Engineering College (Autonomous) from 15th May to 17th May 2020.

19. One Week Online Faculty Development Program on "Advanced Antenna Design Using HFSS" Organized by Department of ECE, Santhiram Engineering College, Nandyal in collaboration with Vardhaman College of Engineering, Hyderabad from 12-05-2020 to 17-05-2020.

20. Workshop on VLSI Circuits Analog & Digital Design Perspectives conducted by Department of Electronics and Communication Engineering, JNTA College of Engineering Anantapuramu (Autonomous) in association with AMS Semiconductors & HCL Technologies from 16-05-2020 to 17-05-2020.

21. Online Faculty Awareness Program on "Research Methodology" organized by Department of Mechanical Engineering, Rajgad Dnyanpeeth's Shri Chhatrapati Shivajiraje College of Engineering, Pune-412206 during the period of 20/05/2020 to 25/05/2020.

22. Two Day Online FDP on "Microstrip Antennas for Advanced Wireless Communication" organized by Department of Electronics and Communication Engineering, Kallam Haranadhareddy Institute of Technology, in association with IETE, Vijaywada Division from 15th May to 16th May.

23. National Level Quiz on NAAC on 27-5-2020, organized by Department of Computer Engineering, P.V.P.P. College of Engineering, Mumbai-22.

24. Five Day National Level Online FDP on "Modern Research Trends in Communication, Signal Processing and VLSI/MEMS", organized by the Department of Electronics, Andhra Loyola Institute of Engineering and Technology and Communication Engineering, from 19th to 23rd May 2020.

25. Faculty Skill Test on VLSI Design on 28/05/2020 organized by Department of Electronics and Communication Engineering, Dhanekula Institute of Engineering and Technology, Ganguru, Vijayawada-521139.

26. Virtual Summit on "COVID-19: Impact on Education, Technology, Environment & Mankind" Conducted on 30th May, 2020 organized by Andhra Loyola Institute of Engineering and Technology and Communication Engineering.

27. Faculty Development Program on the Topic "Multi Carrier Modulation in Broadband Communication and Technology Challenges in 5G" organized by Department of Electronics and Communication Engineering, Raghu Engineering College (Autonomous), Vishakapatnam from 29th May to 30th May 2020.

28. Faculty Development Program on "Artificial Intelligence and its Applications" Conducted by Department of Computer Science and Engineering, Ramachandra College of Engineering in collaboration with 360DigiTMG from 25th May to 30th May 2020.

Faculty Development/Faculty Training Programs/Seminars Attended No. : 17

1. One Week Faculty Development Program (FDP) on "Bio- Inspired Algorithms for Intelligent Signal Processing" organizes by E& ICT Academy, NIT Warangal at Vardhaman College of Engineering (Autonomous), Shamshabad, Hyderabad from 24th - 30th April, 2019.

2. One Week Faculty Development Program (FDP) on "Machine Learning Applications in Signal Processing" organizes by E& ICT Academy, NIT Warangal at Gudlavalleru Engineering College (Autonomous), Gudlavalleru from 04th -09th April, 2019.

3. Attended the one week faculty development program on "Communication and Signal Processing" organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 29th October to 3rd November 2018.

4. Completed the NPTEL-AICTE Faculty Development Programme(FDP) funded by the Ministry of HRD, Govt. of India in the course "Switching Circuits and Logic Design" during July to October 2018.

5. Participated in a Two-Week ISTE STTP on CMOS, Mixed Signal and Radio frequency VLSI Design conducted by Indian Institute of Technology Kharagpur from 30th January to 4th February, 2017. This workshop was held under the National Mission on education through ICT (MHRD). Completed the Two – Week ISTE STTP with **GRADE: VERY GOOD.**

6. Attended a one week National Level Faculty development Program (FDP) on "Signal and Image Processing LabVIEW" held during 13th -18th November 2017 at Gudlavalleru Engineering College, organized by signal processing research group of Department of Electronics and Communication engineering in Association with NI Systems Pvt. Ltd.

7. Attended the Two Day Faculty Training Program on "Advances in VLSI Design and Embedded System" organized by Department of Electronics and Communication Engineering , Gudlavalleru Engineering College, Gudlavalleru, during 6th - 7th November 2017.

8. One Week Faculty Development programme on "Hands on Experience in Signal Processing Applications" held during 07th - 12st November 2016 at Gudlavalleru Engineering College Gudlavalleru.

9. Two day Staff Training Program (STP) on " Simulation of Linear & Digital Integrated Circuits and Microcontroller based Applications" using NI Multisim organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru on 4th -6th November 2016.

10. Certification for participated in the One Week Faculty Development Programme (FDP) on " Development of Wireless Sensor Network using Network Simulator(NS2)-Hands on Experience", organized by Communication Research Group in Association with Vertex Soft, Hyderabad during 1st to 5th December 2015 at Gudlavalleru Engineering College, Gudlavalleru.

11. Two day training program on "Digital Image Processing using MATLAB" organized by Computer Centre, MIT Campus, Anna University, Chennai during 20th-21st June 2015.
12. One day Seminar on "Research in Signal and Image Processing on FPGAs and Embedded system Platforms" organized by the Institute of Engineers (IE), INDIA, Vijayawada local Center, Vijayawada on 22nd November 2014.
13. Attended the One –Week Refresher Course on "Electronic Devices and Circuits", during 23-28 September, 2013, organized by Department of Electronics and Communication Engineering , Gudlavalleru Engineering College, Gudlavalleru.
14. Attended the Complimentary Training/Orientation on "MATLAB, Simulink & Related Tool Boxes for Engineering Education" organized by Akademy of Design & Architecture from 3rd to 4th April 2013 in Gudlavalleru Engineering College, Gudlavalleru.
15. Attended the Five Day Training Program on "Mentor Graphics (Front end & Back end)and Xilinx Tools" organized by Department of Electronics and Communication Engineering , Gudlavalleru Engineering College, Gudlavalleru in association with IETE Vijayawada Center & IETE Students' Form(ISF-GEC) Coreel Technologies, Bangalore from 13th to17th of September, 2010.
16. Certification for participated in the AICTE Sponsored Staff Development Program on " Current Trends in Biometrics", organized by Department of Information Technology during 2nd to 14th November 2009.
17. Certification for presented the paper entitled "Interactive Multimedia e- Learning System by Logistics Engineering" in the ISTE Fourth Annual Convention during 21-22 March 2009 at Gudlavalleru Engineering College, Gudlavalleru.

NPTEL Certifications:

1. Successfully completed the 12 Week NPTEL Online certification "Principles of Signals and Systems" course funded by the Ministry of HRD, Govt. of India during January to April 2019 with a consolidated score of 77%.
2. Successfully completed the 12 Week NPTEL Online certification "Digital Circuits" course funded by the Ministry of HRD, Govt. of India during July to October 2018 with a consolidated score of 73%.
3. Successfully completed the 12 Week NPTEL Online certification "Switching Circuits and Logic Design" course funded by the Ministry of HRD, Govt. of India during July to October 2018 with a consolidated score of 77%.
4. Successfully completed the 8 Week NPTEL Online certification "Analog Circuits" course funded by the Ministry of HRD, Govt. of India during January to April 2018 with a consolidated score of 86%.

Guest Lectures Delivered No. : 01

1. Delivered a lecture on 'FINGERPRINT RECOGNITION SYSTEM' at Gudlavalleru Engineering College, Gudlavalleru in the academic year 2013-2014 for ECE Faculty.

Subjects Handled:

Under Graduation
- Electronics Devices and Circuits
- Basic Electronics
- Elements of Electronics Engineering
- Linear and Digital ICs
- Electronic System Design
- Pulse and Integrated Circuits
- Pulse and Digital Circuits
- Control Systems
- Analog Communication
- Linear IC Applications
- Digital Circuits
- Signals and Systems

Post Graduation   --   NIL

Labs Handled:

Under Graduation
- Pulse Digital Circuits
- Digital Communications
- IC Applications
- IC and PDC
- Electronic Devices and Circuits
- Digital Signal Processing

Post Graduation   --   NIL

Other Responsibilities:

College Level (in GEC) –
- College Level Coordinator for INNOVATION and START-UP Club during 2017-2018 and 2018-2019 academic years
- College Level Convener for MHRD's Innovation Cell.

Department Level (in GEC)

- Training and Placement Coordinator for the academic years 2014-2015, 2015-2016, 2016-2017, 2017-2018 and 2018-2019.
- Internal Examination Coordinator for the academic years 2010-2011 and 2011-2012.

Projects Guided:

Under Graduation

S. No.	Name of the project	Year
1	RF BASED AUTOMATIC SPPED LIMITER FOR VEHICLES	2010
2	RFID-BASED INTELLIGENT BOOKS SHELVING SYSTEM	2011
3	INTELLIGENT LIBRARY MANAGEMENT SYSTEM	2014
4	AUTOMATIC ELECRICITY BILLING SYSTEM THROUGH GPS	2015
5	REAL-TIME MULTIFRAME IMAGE SUPER-RESOLUTION	2016
6	BUS TRACKING AND MONITORING SYSTEM USING IOT	2017
7	SMART IRRIGATION MONITORING AND CONTROLLING SYSTEM USING IOT	2018
8	MULTI FACE DETECTION AND RECOGNITION	2019
9	A REAL-TIME BUS TRACKING SYSTEM USING G PS ON SMART PHONE	2019

Post Graduation

S.No	Name of the project	Year
1	HARDWARE IMPLEMENTATION OF MODIFIED WEIGHTED MEDIAN FILTERING ON FPGA	2015
2	RECONFIGURABLE LOW POWER FPGA DESIGN WITH LOW DENSITY	2016
3	SATELLITE REMOTE SENSING IMAGE BASED AIRCRAFT RECOGNITION USING WAVELET AND CURVELET	2016

	TRANSFORMS	
4	LBP BASED SHARPNESS PARAMETER BASED APPROACH FOR DEFOCUS BLUR ACTS ON A ROBUST SEGMENTATION ALGORITHM	2017

Guiding Ph. D Students   --   NIL

Books Published No. : 04

1. Co-author for chapter 1 titled "Electronic Devices and Circuits" of Electronics and Communication Engineering laboratory Manual series 2 with ISBN – 978-1-4276-5564-6 in the year 2016
2. Co-author for chapter 3.3 titled "Pulse and Digital Circuit Laboratory" of Electronics and Communication Engineering laboratory manual series with ISBN – 978-1-4276-5563-9 in the year 2016
3. Co-author for chapter 6 titled "Analog Communication" of Electronics and Communication Engineering Instructional manual with ISBN – 978-1-4276-5539-4 in the year 2015.
4. Co-author for chapter 2.1 titled "Electronic Circuit and Pulse and Digital Circuits Laboratory" of Electronics and Communication Engineering laboratory Manual with ISBN – 978-1-4276-5536-3 in the year 2015.

R&D and Consultancy

S. No.	Project Title	Source of Funding	Duration	Role	Status
1	A real Time Implementation of GEC Bus Tracking System using Google Maps and Android APP	Internal R&D	2 Years	Investigator	On-Going
2	Wireless Fingerprint Attendance Management System	Internal R&D	2 Years	Co-Investigator	Completed
3	Automatic Electricity Billing System using GSM	Internal R&D	2 Years	Investigator	Completed
4	Authorized and Enhanced Multifunctional Bus Tracking System	Consultancy	2 Years	Investigator	Completed

Curriculum Design and Development

- Analog Circuits syllabus preparation(EEE-Autonomous)
- Pulse Circuits Syllabus Preparation (Autonomous)
- Linear & Digital ICs Syllabus Preparation (Autonomous)
- IC Applications LAB syllabus Preparation(Autonomous)
- Pulse and Digital Circuits LAB syllabus preparation (Autonomous)
- IC Applications Course Module.
- IC Applications Lab Manual.

Academic Rewards / Achievements

- Got Academic Topper award in M-Tech course.
- Secured State First Rank in PGECET-2011 Entrance Examination.
- Got TWO GOLD MEDALS for COLLEGE FIRST and BRANCH FIRST in DIPLOMA.
- Got 100% Attendance Cash Award during the B-Tech II year.

Extra Curricular Activities

- Acted as Class Teacher for III Year –B Section during 2019-2020 academic year I Semester.

- Acted as the PRESS and MEDIA Coordinator for the First International Conference on "CIATES-2K18" during 30th November to 1st December 2108 held at Gudlavalleru Engineering College.

- Acted as coordinator for various JOB Awareness Programs- Guest Lectures given Various Company HRs and CEOs

- Acted as the coordinator for Various Placement Recruitment Drives.

- Acted as the Coordinator for Various INNOVATION and Start -Up Club Events

- Acted as the Project coordinator for IV Year Section –A during II semester 2017-2018 Academic year.

- Acted as Class Teacher for III Year –D Section during 2018-2019 academic year I Semester.

- Acted as Class Teacher for III Year –C Section during 2018-2019 academic year II Semester.

- Enacted as BoS member for the Autonomous curriculum design of Electronics and Communication Engineering at Gudlavalleru Engineering College, Gudlavalleru on 2nd August 2014 and 25th May 2015.

- Prepared the 2 Experiment Modules in the "Electronic Devices Laboratory Manual" for R17 regulation Syllabus.

- Prepared the 3 Experiment Modules in the "Digital Circuit Design" for R17 regulation Syllabus.

Declaration

I, hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date: 26-08-2020   Signature

Place: Gudlavalleru   (P.Anil Kumar)