

T.SUBHASHINI



Department: Electronics and Communication Engineering
College: Gudlavalleru Engineering College
Phone No: (0)9493811529
Mobile: (0)9493811529
E-mail: subhashinitata19@gmail.com

RESIDENTIAL ADDRESS:

T.SUBHASHINI,
20/69, Gandhi Nagar,
Opp Hyny School,
Machilipatnam.
Phone: 9493811529

ACADEMIC QUALIFICATIONS:

Examination	Discipline/ Specialization	School/college	Board/ University	Year of passing	%
Ph.D (Pursuing)	VLSI Design	JNTUK, Kakinada	JNTUK, Kakinada	Pursuing (Admitted Acc. Year 2015-16)	-----
M.Tech	Digital Electronics & Communication Systems	Gudlavalleru Engineering College, Gudlavalleru.	JNTUK University	2011	73
B.Tech	Electronics & Communication Engineering	V.R.Siddhartha Engineering College, Vijayawada.	Acharya Nagarjuna University	2005	72.6

ACADEMIC REWARDS / ACHIEVEMENTS: NIL

PROFESSIONAL EXPERIENCE:

S.No	Designation	Institution Name	Working Period
------	-------------	------------------	----------------

			From	To
1	Assistant Professor	Gudlavalleru Engineering College, Gudlavalleru	25 th October 2012	Till date
2	Assistant Professor	Sri Vasavi Institute of Engineering & Technology, Nandamuru, Pedana	June 2011	October 2012

PROJECTS GUIDED:

PhD	Post Graduation	Under Graduation
NIL	04	05

SPECIALIZATION: Digital Electronics & Communication Systems

RESEARCH PAPERS / BOOKS:

BOOKS PUBLISHED IN PUBLICATIONS:

1.EDC Lab Manual

Number of Publications : 26

A. Papers Published in Journals No. : 19

1. T. Subhashini, M. Kamaraju, K. Babulu, “ Power Optimized BCD adder Using Low Power Techniques”, Journal of Test Engineering and management, **January-February 2020, ISSN: 0193-4120, pp. 8789 – 8795**, Published by: The Mattingley Publishing Co., Inc.
2. T. Subhashini, M. Kamaraju, K. Babulu, “ Low-Power and Fast Adders Using New XOR and XNOR Gates,” International Journal of Engineering Research & Technology , ISSN: 0974 – 3154, Volume-12, Number-12, December 2019.
3. T. Subhashini, M. Kamaraju, K. Babulu, “Single Cycle Risc Micro Architecture Processor Using Clock Gating Technique,” International Journal of Scientific Technology Research , ISSN: 2277 – 8616, Volume-8, Issue-12, December 2019, pp 2326 – 2334.
4. T. Subhashini, M. Kamaraju, K. Babulu, “A New ALU Design using PNS-FCR: Static CMOS Logic for Microprocessors,” International Journal of Engineering and Advanced Technology (IJEAT) ISSN: 2249 – 8958, Volume-8, Issue-6S2, August 2019, pp 876-882
5. T.Subhashini, M.Kamaraju, K.Babulu, “A Novel Test Programs for Hybrid RISC Controller,” Springer [Lecture Notes in Electrical Engineering](#) book series (LNEE, volume 521), [Microelectronics, Electromagnetics and Telecommunications](#), November 2018, DOI: https://doi.org/10.1007/978-981-13-1906-8_57, Publisher Name: Springer, Singapore, pp 559-565.
6. M. Kamaraju, D. Ramya Krishna, T. Subhashini, “Implementation of A Wallace Tree Approach for Data Aggregation in Wireless Sensor Nodes on ZED board and Send that Aggregated Data to the Sink through WSN Master Development Board(IOT)” International Journal for Research in Engineering Application & Management (IJREAM) ISSN : 2454-9150, IMC18405, Jan 2018, pp 278-287.

7. T.Subhashini, M.Kamaraju, K.Babulu, "Programmable core processor using data driven clock gating", International Journal for Modern Trends in Science and Technology(IJMTST), Volume 2 ,Special Issue 1,ISSN:2455-3778,pp:196-203,October,2016 Impact Factor:2.175
8. T.Subhashini, K.Babulu, M.Kamaraju, "Low Power Programmable Processor Core(LPPPC) Architecture Using Clock Gating Technique", Annual Technical Volume of ETEB, Institute of Engineers (India), Theme: Design of Circuits and Systems for Signal, Image and Video Processing, vol. 1, pp. 59-61, 2016.
9. M.Kamaraju, T.Subhashini, "Power efficient convolution using modulo multipliers", International Journal of Applied Engineering and Research, Volume No: 10, Issue No: 21, ISSN: 0973-4562, pp. no: 42581-42587, November, 2015. Peer reviewed, scopus.
10. T.Subhashini, M.Kamaraju, "Power Optimized Datapath units of Hybrid Embedded Controller Architecture," International Journal of VLSI Design and Communication Systems (DOI : 10.5121/vlsic.2015.6604) , December 2015, vol.6, No.6, pp: 33-43.
11. S. Anitha, T. Subhashini, Dr. M. Kamaraju, " A Novel Multimodal Medical Image Fusion Approach based on Phase Congruency and Directive Contrast in NSCT Domain", International Journal of Computer Applications, ISBN: 0975-8887, Vol.129,Issue.10, November-2015, pp:30-35.
12. T.Subhashini, Y.Srinivasa Rao, K.Rambabu "Design and performance analysis of double precision floating point multiplier using urdhva triyagbhyam sutra" International Journal of VLSI and Embedded systems, vol.5,issue.10, ISSN:2249-6556,Oct,2014,pp.1313-1317.
13. B.Ruxanayasmin, B.Ananda Krishna, T.Subhashini "Minimization of Power Consumption in Mobile Ad Hoc Networks" International Journal of Computer Networks and Information Security, vol.6,No.2,ISSN(Print)2074-9090,ISSN(online):2074-9104,Jan 2014,pp.38-44.
14. B.Ruxanayasmin, B.Ananda Krishna, T.Subhashini "Implementation of Data Compression Techniques in Mobile Ad hoc Networks" at International Journal of Computer Applications, Volume.80, No.8, October 2013, pp.8-12.Impact factor:0.85
15. M.Madhurya, B.Ananda Krishna, T.Subhashini "Implementation of Enhanced Security Algorithms in Manets" International Journal of Computer Networks and Information Security, vol.6,No.2,ISSN(Print)2074-9090,ISSN(online):2074-9104,Jan 2014,pp.30-37 Impact factor:N/A
16. N.Madhuri, B.Ananda Krishna, T.Subhashini "Secured Routing Through Multi Stage Authentication in MANETs" at International Journal of Engineering Research & Technology (IJERT), Vol.2, Issue.11, November 2013, ISSN:2278-0181, pp:3509-3515.Impact factor:1.982.
17. A.Mallaiah, G.Harish, M.Kamaraju, T.Subhashini, " Implementation of AHB Bus Tracer with dynamic multiresolution for lossless Real time compression" International Journal Of Engineering Science & Advanced Technology (IJESAT) Volume-2, Issue-3, Page no. 445 – 448, ISSN: 2250–3676. Impact factor: 1.14.

18. A.Mallaiah, M.Ratna kiran, T.subhashini, and Dr. G. N. Swamy “ DTT Based Hybrid Embedded Coder For Image Compression Using SPHIT Algorithm” International Journal of Emerging trends in Engineering and development (IJETED), Volume .5, Issue 2, Page No. 740-747, ISSN 2249-6149. Impact factor: 2.87.
19. A.Mallaiah, SK.Shabbir, T.Subhashini, and Dr. G. N. Swamy “SPIHT Algorithm with Huffman Encoder for Image Compression and Quality Improvement Using Retinex Algorithm”, International journal of advanced scientific and technical research Volume .4, Issue 2, Page No. 163-170, ISSN 2249-9954. Impact factor: 2.94.

B.IEEE Xplorer Publications: 02

20. T.Subhashini, Dr.M.Kamaraju, Dr.K.Babulu, “Design and Analysis of Multiple Port Memory Architecture for Low Power Applications”, Proc. of IEEE 2018 Conference on Signal Processing and Communication Engineering Systems (SPACES), January 2018, pp. 210-214.
21. T.Subhashini, Dr.M.Kamaraju, Dr.K.Babulu, “A Novel Implementation of Mixed ISA on FPGA”, Proc. of IEEE 2017 International Conference on Communication and Signal Processing (ICCSP), December 2017, pp. 0404-0409.

C.Papers Published in Conferences No. : 05

22. T.Subhashini, M.Kamaraju, K.Babulu, " A Novel ALU design using PNS-FCR: Static CMOS Logic for Microprocessors", Proc. of First International Conference on Computational and Intelligent Techniques for Automation of Engineering Systems (CITAES-2018), Gudlavalleru Engineering College, Gudlavalleru during 30th November 2018 to 1st December 2018.
23. T.Subhashini, M.Kamaraju, “Programmable Hybrid Embedded Controller Architecture using Clock Gating Technique”, IFERP International Conference, Hyderabad, ISBN:9788192958040,pp:55-59,January,2016.
24. A.Mallaiah, G.Harish, T.Subhashini, “Design of an on-chip AHB bus tracer for SOC Applications”, National Conference on advances in Engineering & Technology SPANDAN 2012, Nagpur 28-29 February-2012.
25. A.Mallaiah, Sk.Shabbir, T.Subhashini, “An SPIHT Algorithm with Huffman Encoder for Image compression and quality improvement using Retinex Algorithm”, 1st international Conference on AMELIORATIONS in communication and power engineering (ICAP-12), 2-3 March 12.
26. A. Mallaiah, Sk. Shabbir, T.Subhashini, “A Novel Hybrid HVS based Embedded Image coding Algorithm using DIT and SPIHT”, 1st International Conference on AMELIORATIONS in communication and power engineering (ICAP-12), 2-3 March 2012.

LIST OF WORKSHOPS ORGANIZED: 05

1. Organized a workshop on “Image processing Applications using MATLAB”, organized by ISF in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held on 12th – 13th October, 2015.
2. Organized a workshop on “Emerging Technology : Internet of Things”, organized by IE in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held on 21st – 22nd September, 2015.
3. Organized a Guest Lecture on “ALIEN VISION : Exploring Electromagnetic Spectrum with Imaging Technology”, organized by IE in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held on 15th September, 2015.
4. Organized a Guest Lecture on “Future Development of Product/Solution”, organized by ECSAT in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held on 5th September, 2015.
5. Organized a Guest Lecture on “Detection and Estimation in SONAR Signal Processing”, organized by Department of ECE, Gudlavalleru Engineering College, at Gudlavalleru held on 1st August, 2015.

LIST OF WORKSHOPS /CONFERENCES /SEMINARS ATTENDED:43

1. Presented a paper titled “ Power optimized BCD adder Using Low Power Techniques” at International Conference on Computing , Communication, Electrical and Electronics Engineering (ICCCEEE -2020), G.Pulla Reddy Engineering College , Kurnool during 10th – 11th January 2020.
2. A one week Faculty Development Programme (FDP) on “Communication and Signal Processing”, organized Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru, during 29th October to 3rd November 2018.
3. A 33rd National Convention of Electronics and Telecommunication Engineers and National Seminar on “Recent Advances in IoT”, organized by The Institution of Engineers (India), Telangana State Centre in association with Department of Electronics and Communication Engineering, Spoorthy Engineering College, Hyderabad, during 29th November to 30th November 2017.
4. A Two day Faculty Training Programme on “Advances in VLSI Design and Embedded Systems”, organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, during 6th to 7th November 2017.
5. A one week National Level Faculty Development Programme (FDP) on “Signal and Image Processing using LabVIEW”, organized by Signal Processing Research Group of Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, in association with NI Systems Pvt. Ltd., during 13th November to 18th November 2017.

6. A Two day Faculty Training Programme on “Mentor Graphics Tools”, organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, during 31st August to 1st September 2017.
7. A Two Day National Level Workshop on “Advances in Electronics and Communication Engineering, JNTUK, Kakinada during 26th to 27th August 2017.
8. A 2-week Faculty Development Programme on Entrepreneur-ship organized by Department of MBA, Gudlavalleru Engineering College, Gudlavalleru from 6th February to 21st February 2017.
9. A Two week ISTE STTP on “CMOS, Mixed Signal and Radio frequency VLSI Design”, conducted by Indian Institute of Technology Kharagpur, under the National Mission on Education through ICT (MHRD) from 30th January to 4th February 2017.
10. A one week Faculty Development Program on “Hands on Experience in Signal Processing Applications”, organized by the Department of Electronics and Communication, Gudlavalleru Engineering College, Gudlavalleru from 7th to 12th November 2016.
11. Participated in National Convention and National Seminar on, “Intelligent System for more Connected World: Internet of Things”, organized by The Institution of Engineers- Andhra Pradesh State Center, on 24th to 25th October 2016.
12. A one week National Workshop on “Recent Advancements in VLSI technology and design using EDA tools(VLSITP-2016)”, organized by Department of Electronics and Communication, UCEK (A), JNTUK, Kakinada sponsored by TEQIP-II, Kakinada from 20th to 24th July 2016
13. A Two week Audit Course on “Research Methodologies”, organized by Department of Electronics and Communication, UCEK (A), JNTUK, Kakinada during 1st May to 15th May 2016.
14. A Two week Credit Course on “EDA Tools”, organized by Department of Electronics and Communication, UCEK (A), JNTUK, Kakinada during 1st May to 15th May 2016.
15. A Three Day workshop on “Swarm Intelligence Techniques- Applications to Signal Processing”, organized by the Department of Electronics and Communication, Gudlavalleru Engineering College, Gudlavalleru from 5th to 7th February 2016.
16. A Three Day workshop on “Overview on Latest Telecom Technologies”, organized by the BSNL Krishna SSA, from 11th to 13th January 2016.
17. A Three Day workshop on “Swarm Intelligence Techniques- Applications to Signal Processing”, organized by the Department of Electronics and Communication, Gudlavalleru Engineering College, Gudlavalleru from 5th to 7th February 2016.
18. A one week Faculty Development Program(FDP) on “Development of wireless Sensor Network Simulator(NS2) – Hands on Experience”, organized by the Communication Research Group, Department of Electronics and Communication, Gudlavalleru Engineering College, Gudlavalleru in association with Vertex Soft, Hyderabad from 1st to 5th December 2015.

19. A one week Refresher Course on “Microprocessors and Microcontrollers”, organized by the Department of Electronics and Communication, Gudlavalleru Engineering College, Gudlavalleru, from 17th to 21st November 2015.
20. A Two day workshop on “Image Processing Applications using MATLAB”, organized by the Department of Electronics and Communication, Gudlavalleru Engineering College, Gudlavalleru, from 12th to 13th October 2015.
21. A Two day All India workshop on “Emerging Techniques on VLSI Architecture Design for Real Time Environment”, organized by Department of Electronics and Communication Engineering, PACE Institute of Technology & Sciences, Ongole, held during 9th to 10th October 2015.
22. A Two day workshop on “Real Time Signal & Image Processing using Xilinx Vivado and Zynq-7000 SOC”, organized by Department of Electronics and Communication Engineering SRK Institute of Technology, Vijayawada in collaboration with CoreEL Technologies, Bangalore held on 11-12 September 2015.
23. A Two day workshop on “All India Seminar on Agricultural, Horticultural –unmanned air vehicles in micro farming”, organized by the IE(I), vijayawada Local centre in association with Sri Vishnu College of Engineering at Bhimavaram held on 26th to 27th March 2015.
24. A One day workshop on “Research and Development Facilitation” organized by the IE(I), vijayawada Local center, Vijayawada, held on 29th March 2015.
25. A One day workshop on “Ultra Low Power Communication using CC430 Microcontroller: Hands on Experiences” organized by the ECE Dept. of Gudlavalleru Engineering College, held on 7th February 2015.
26. A Four day Coursework on “Research Methodology” organized by the KL University, Vaddeswaram, during 3rd to 6th January 2015.
27. A one day Seminar on “Research in Signal and Image Processing on FPGAs and Embedded Platforms” organized by the IE(I), vijayawada Local center, Vijayawada, held on 22nd November 2014.
28. A one day workshop on “Speech Processing: Current Challenges and Hands on Experiences” organized by the ECE Dept. of Gudlavalleru Engineering College, held on 23rd August 2014.
29. A three-day National level workshop on “Theory & applications of Intelligent Signal Processing” organized by the ECE Dept. of Gudlavalleru Engineering College, Gudlavalleru held during 28th February- 02nd March 2014.
30. Two Day workshop on “Issues in Integrated Circuits (ICs) Design” during 13th – 14th December, 2013 at Gudlavalleru Engineering College.

31. Two-week Refresher course on “Electronics Devices and Circuits” during 23rd -28th September, 2013 at Gudlavalleru Engineering College.
32. One weeks workshop on “Training Teachers for Excellence” during 7th -21st June, 2013 conducted by Dr B. G. Barki, Former Director, NITTTR, Chennai, at Gudlavalleru Engineering College.
33. Two weeks workshop on “Analog Electronics” during 4th -14th June, 2013 conducted by Indian Institute of Technology Kharagpur at Gudlavalleru Engineering College.
34. Attended a one day workshop on “Research in Energy Management” at Vijayawada, conducted by Institution of Engineers (India) on 29th March’2015
35. A webinar on “Career Choices in Civil Services” by Dr.Kiranmayi Koppiseti, IRTS who cracked all India Rank of 573 in UPSC organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College on 30th June 2020. 185 students participated.
36. A one week International online Knowledge Development Programme on “Challenges and Advancements in the Design of IoT, Embedded and VLSI systems:A Researchers View (CADIEVR-2020)”, organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru from 8th to 13th June 2020 as a convenor .
37. A Webinar on “Introduction to Radio frequency Measurements for Cellular and Wireless Communication Systems” organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College on 25th May 2020. Dr.Ramakrishna Yellapantula, Senior Member of Technical Staff, Lite Point Corporation, USA is the resource person.A total of 85 participants attended.
38. Attended A National level on line Faculty Development Program on Research challenges and Emerging Trends in ECE organized by Dept.of ECE, Sri Ramachandra College of Engineering during 7th May 2020 to 1st June 2020.
39. Attended webinar on “ Impact of COVID in Engineering Education & Way forward “ organized by ISTE Chapter, GEC on 9th June 2020.
40. Attended 5 days on line certification Program on Industrial IoT held from 19th to 23rd of May 2020 organized by Apply Volt Company.
41. Attended AICTE STTP Phase I on “ Block Chain Architecture and Use cases” during 10-8-2020 to 15-8-2020 organized by Dept.of ECE , GEC.
42. Attended AICTE STTP Phase II on “ Block Chain Architecture and Use cases” during 17-8-2020 to 22-8-2020 organized by Dept.of ECE , GEC.

43. Attended AICTE STTP Phase III on “ Block Chain Architecture and Use cases” during 24-8-2020 to 29-8-2020 organized by Dept.of ECE , GEC.

Workshops /Conferences /Seminars Organized No. : 08

1. Organized a workshop on “Image processing Applications using MATLAB”, organized by ISF in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held on 12th – 13th October, 2015.
2. Organized a workshop on “Emerging Technology : Internet of Things”, organized by IE in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held on 21st – 22nd September, 2015.
3. Organized a Guest Lecture on “ALIEN VISION : Exploring Electromagnetic Spectrum with Imaging Technology”, organized by IE in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held on 15th September, 2015.
4. Organized a Guest Lecture on “Future Development of Product/Solution”, organized by ECSAt in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held on 5th September, 2015.
5. Organized a Guest Lecture on “Detection and Estimation in SONAR Signal Processing”, organized by Department of ECE, Gudlavalleru Engineering College, at Gudlavalleru held on 1st August, 2015.
6. Delivered a staff seminar on Metamaterials on 18th September’15 in the department.
7. Acted as co-coordinator in organizing a one day workshop on “Ultra Low Power Communication using CC430 microcontroller::Hands on experience” on 7th February’2015 in the department.
8. Acted as Coordinator for one week refresher course on “ Embedded System Design” during 27th July 2020 to 1st August 2020 organized by Dept.of ECE, Gudlavalleru Engineering College

CERTIFICATIONS: 02

1. Microprocessors & Microcontrollers Certification with Elite during October 2017
2. Digital Circuits Certification with Elite during April 2018

GUEST LECTURES DELIVERED:

1. Delivered a lecture on “ Introduction to Embedded System design” at a one week Refresher course on “ Embedded System Design” held during 27th July 2020 to

1st August 2020 on 27th July 2020

MEMBERSHIP IN PROFESSIONAL BODIES:

S.No	Name of the Professional body	Membership Designation
1	Institution of Engineers	AMIE

CONSULTANCY ACTIVITIES:

NIL

GRANTS FETCHED:

- As a Co-investigator, Internal R & D project titled “Design & Development of intelligence traffic control system and traffic guidance system using IOT”, sanctioned an amount of Rs 80,000/- in the Year 2019 by the GEC authorities, duration of the work : 1 Year