FACULTY PROFILE

Name of the faculty: DAMODHAR RAO.M

Department: Electronics and Communication Engineering.

College: Gudlavalleru Engineering College

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Address for Communication

M.DAMODHAR RAO,
BUDDALAPALEM,
MUNJULURU POST,
MACHILIPATNAM,
KRISHNA- 521369.
AP.

Academic Qualifications

S. No	Name of the Degree (Starting from Ph.D to 10th Class)	University	Percentage of Marks/Grade	Specialization	Year of Pass
1	M.Tech	JNTUK Kakinada	69.02%	Embedded Systems	2012
2	B.Tech	JNTU Hyderabad	66.07%	ECE	2008
3	Diploma	SBTET Hyderabad	71.55%	DECE	2005
4	SSC	Board of Secondary Education	68.55%	--	2002

Professional Experience

S. No	Designation	Institution Name	Working Period	
			From	**To**
1	Assistant Professor	Gudlavalleru Engineering College	18-10-2012	Till date
2	Assistant Professor	BIET Bhemavaram	14-10-2011	15-10-2012

Professional Body Membership

S. No	Name of the Professional body	Membership Number
1	**Associate member of IEI**	**AM 162846-4**

Papers Published in Journals No. : 04

1. Beram Eswar Charan Teja, M. Damodhar Rao, Dr. Y. V. Narayana, Dr. V. V. K. D. V. Prasad " Design and Implementation of Improved SRAM Cell "International Journal of Engineering Science and Computing, ISSN 2321 3361 © 2017 IJESC, volume 7, Issue no 6, June 2017

2. M.Vinay Babu, M.Damodhar Rao, Dr.V. V. K. D. V. Prasad "A Novel Architecture of 16-Bit Multiplier Using Modified Gate Diffusion Input Logic "IRACST-International Journal of Computer Science and Information Technology & Security(IJCSITS),ISSN : 2249-9555, Vol.6, No.4, July-August 2016, Pages: 69 - 74.

3. M.Damodhar Rao and R.Vijay " A Low Power 32-Bit Ripple Carry Adder Using Dynamic DML CMOS Logic Gates", International journal of Research-IJR, p-ISSN:2348-6848, e-ISSN: 2348-795x, vol-03,Issue 10, June 2016.

4. M.Damodhar Rao and G.Manasa "Design and analysis of fast addition mechanism for integers using quaternary signed digit number system", International journal of VLSI and Embedded systems-IJVES, vol-05, Article 09455, October 2014.

Papers Published in Conferences No. : 01

1. M.Damodhar Rao and V.Vittal Reddy, "Implementation of Improved Redundant binary Booth Encoding For Fast Multiplier", International Conference on Emerging Trends in Electronics, Communication and Computing Technology (ICECCT-11) on September 12-13 2011.

Workshops Organized No. : -- Nil

Workshops /Conferences /Seminars Attended No. : 13

1. One week faculty Development Programme (FDP) on " Advanced CMOS VLSI " organized by E & ICT Academy, NIT Warangal at, NIT Warangal during 3rd -8th December 2018.

2. One week faculty Development Programme (FDP) on " Communication and signal processing " organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 29th October to 3rd November 2018.

3. Five days faculty Development Programme (FDP) on " CMOS Integrated circuits for Instrumentation and IoT applications" organized by E & ICT Academy, NIT Warangal at, velagapudhi Ramakrishna Siddhartha Engineering College, vijayawada during 22nd – 28th January 2018

4. Two week Online workshop on " CMOS,Mixed signal and radio frequency VLSI Design" organized by IIT Kharagpur, during 26th December 2016 to 4th February 2017.

5. Two day staff training program on " Simulation of Linear & Digital Integrated Circuits and Microcontroller based Applications" using NI Multisim " organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru on 4-5 November 2016.

6. Five days faculty Development Programme (FDP) on " Development of Wireless Sensor Networks using Network Simulator(NS2)- Hands On Experience" organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 1st–5th December 2015.

7. One Week Refresher course on "Microprocessor & Microcontrollers" organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 17th-21st November 2015.

8. I have attended a workshop on "Ultra Low Power Communication using CC430 Microcontroller: Hands on Experiences" organized by the ECE Dept. of Gudlavalleru Engineering College, held on 7th February 2015.

9. I have attended a workshop on "Speech Processing: Current Challenges and Hands on Experiences" organized by the ECE Dept. of Gudlavalleru Engineering College, held on 23rd August 2014.

10. I have attended a three-day national level workshop on "Theory & applications of Intelligent Signal Processing" organized by the ECE Dept. of Gudlavalleru Engg. College, Gudlavalleru held during 28th February - 02nd march December 2014.

11. Attended two-week Refresher course on "Electronics Devices and Circuits" during 23rd - 28th September, 2013 at Gudlavalleru Engineering College.

12. Attended a Two Day workshop on "Issues in Integrated Circuits (Ics) Design" during 13th – 14th December, 2013 at Gudlavalleru Engineering College.

13. Attended a workshop on "Signals & Systems" during 2nd -12th January, 2014 conducted by Indian Institute of Technology Kharagpur at Gudlavalleru Engineering College.

Certifications/Training Programs Attended No. : 04

1. Certified in Four subjects through NPTEL program and got top 5% grade in CMOS digital VLSI design subject with Elite + silver Grade.

2. Training program on "Evaluating students' performance and designing question papers"-, organized by National Institute of Technical Teachers Training And Research, Kolkata during 25th February to 1st March 2019.

3. Training program on "Development of wireless sensor networks simulator (NS2)"-Hands on experience, organized by Gudlavalleru Engineering College, Gudlavalleru during 01st – 05th December 2015.

4. Training program on "Training Teachers for Excellence", organized by Gudlavalleru Engineering College, Gudlavalleru during 17th- 21st June 2013.

Online FD/Webinar/Training programs: 08

1. One day Webinar on " Successful steps towards high quality scientific publications" by Dr.B.T.P.Madhav, Professor and Associate Dean, Academic Research, K L Deemed to be University, organized by Chalapathi Institute of Technology, Mothadaka, Guntur on 18th May 2020.

2. A Three day FDP on " VLSI Digital Circuits and Testing Techniques" organized by Miracle Education Society Group of Institutions, held from 21st to 23rd may 2020.

3. One week faculty Development Program (FDP) on "Recent trends in VLSI and Embedded Auto Industry" organized by organized by the Centre for VLSI and Embedded Systems Design (CVESD) in association with Dept. of ECE, CMR Technical Campus, Hyderabad from 20th to 24th of May, 2020

4. Participated a webinar on "Radio Frequency Measurements for Cellular and Wireless Communication Systems" organized by Department of Electronics and Communication Engineering on 25th May, 2020.

5. One week International online knowledge development program on " Challenges and advancements in the design of IoT, Embedded and VLSI systems: A Researchers View"

organized by Gudlavalleru Engineering College, Gudlavalleru during $08^{th} – 13^{th}$ June 2020.

6. One week National level online knowledge development program on " Cyber Security" organized by Gudlavalleru Engineering College, Gudlavalleru with Cyberpsy, during $22^{nd} – 26^{th}$ July 2020.

7. One day Webinar on " loe Power VLSI Circuits & Energy Harvesting for IoT Applications" organized by Department of Electronics and computer Engineering, sreenidhi institute of Science and Technology, Hyderabad eld on 15th August 2020.

8. Participated in AICTE sponsored Online Short Term Training Programme on "Blockchain Architecture Design and Use Cases" Phase-I organized by Department of Electronics and Communication Engineering from 10-08-2020 to 15-08-2020.

Guest Lectures Delivered No. : Nil

Subjects Handled
Under Graduation
- Pulse and Digital Circuits.
- Microprocessors and Microcontrollers.
- Microprocessors and Multi-core systems.
- Radar Systems.
- Television Engineering.
- Real-Time Operating Systems.
- Elements of Electronics Engineering.
- Sensors and Instrumentation.

Post Graduation
- Embedded System Concepts.
- Principles of Digital System Design.

Labs Handled

Under Graduation
- Microprocessor and Microcontroller.
- IC Applications.
- Electronic Devices and Circuits.
- EC&PDC.
- Microwave and Optical communication.

Post Graduation

- Embedded system concepts.
- Digital system design.

Other Responsibilities
College Level
  1. UPS Coordinator.

Department Level
  1. Exam section coordinator.
  2. 4th year Academic Coordinator.
  3. Internship Coordinator.

Projects Guided

Under Graduation

S. No	Name of the project	Year
1	Child Distance Monitoring And Alerting System	2014
2	Vehicle Accident prevention using IR sensor.	2015
3	Spying ROBOT with night vision wireless camera by android application	2016
4	Automatic Aquaculture managing system	2019
5	Lossless data compression and decompression	2019
6	Drudgery Reduction Vehicle for Rural Women	2020
7	Smart Gadget for Women Safety Using IoT	2020

Post Graduation

S.No	Name of the project	Year
1	Design and analysis of fast addition mechanism for integers using quaternary signed digit number system	2015
2	A Novel Architecture of 16-Bit Multiplier Using Modified Gate Diffusion Input Logic	2016

Guiding Ph. D Students: Nil

Books Published No. : 04

1. Co-author for chapter 3.2 titled " Pulse And Digital Circuits Laboratory"   of Electronics and Communication Engineering laboratory manual series 2 with ISBN-978-1-4276-5564-6 in the year 2016.
2. Co-author for chapter 3.3 titled " Pulse And Digital Circuits Laboratory" of Electronics and Communication Engineering laboratory manual series 2 with ISBN-978-1-4276-5563-9 in the year 2016.
3. Co-author for chapter 21 titled " Radar Systems "of Electronics and Communication Engineering laboratory manual series 2 with ISBN-978-1-4276-5539-4 in the year 2016.

4. Co-author for chapter 2.1 titled " Electronic circuit and Pulse And Digital Circuits Laboratory " of Electronics and Communication Engineering laboratory manual series 2 with ISBN-978-1-4276-5536-3 in the year 2016.

R&D and Consultancy

S. No	Project Title	Source of Funding	Duration	Role	Status
1	Development of module to check the active or inactive status of GPS panic button.	Nyltech. Solutions Pvt.Ltd.	1 year	Co-Investigator	On going

Curriculum Design and Development: Nil

Academic Rewards / Achievements: Nil

Extra Curricular Activities: Nil

Declaration

I hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date: 27-08-2020.   Signature

Place: Gudlavalleru.   (M. Damodhar Rao)