

FACULTY PROFILE

Bolem Vamsy Krishna

Department: Electronics and Communication Engineering

College: Gudlavalleru Engineering College

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Address for Communication

1/115, Sulthanagaram

Gollapalem (P), Bandar (M)

Krishna (Dt.), Pin Code: 521001.

Academic Qualifications

S. No	Name of the Degree (Starting from Ph.D to 10th Class)	University	Percentage of Marks/Grade	Specialization	Year of Pass
1	PhD	KL Deemed to be University, Vijayawada	Pre PhD Completed	Nanotechnology	Pursuing
2	M.Tech	JNTU Kakinada	70.85	VLSISD	2009
3	B.Tech	JNTU Hyderabad	61.88	ECE	2007
4	Diploma	State Board of Technical Education	69.30	DECE	2004
5	SSC	State Board of Secondary Education	57.85	--	2001

Professional Experience

S. No	Designation	Institution Name	Working Period	
			From	To
1	Assistant Professor	Gudlavalleru Engineering College	07-06-2012	Till date
2	Assistant Professor	Usha Rama College of Engineering And Technology	12-05-2011	06-06-2012
3	Assistant Professor	Sri Sarathi Institute of Engineering And Technology	14-05-2007	06-05-2011

Professional Body Membership

S. No	Name of the Professional body	Membership Number
1	IEI	AM156034-7

Papers Published in Journals No. : 06

1. P.Bala Krishna and **B.Vamsi Krishna** “Designing of Embedded Web Server based on Energy Meter” in IJERA journal, Vol.3, Issue 5, September-October 2013
2. Ch. Sri Lakshmi and **B.Vamsi Krishna** “A Novel Method to Improve the Logic Test by Using Single Cycle Access Structure” in IJERA journal, Vol.3, Issue 6, November-December 2013
3. G.Vijayaraju and **B.Vamsi Krishna** “Automatic Screening of Missing Objects and Identification with Group Coding of RF Tags” in IJERA journal, Vol.3, Issue 6, November-December 2013
4. K.Jaswanth Singh, **B.Vamsi Krishna**, “Design and Implementation of Modified Booth Encoder Multiplier using Carry Select Adder”, International Journal of Recent Technology and Engineering, 2014.
5. **B.Vamsi Krishna**, M. A. Majeed, Dr.M. Kama Raju, “A Real-Time Remote Control And Monitoring System Using Zynq SoC FPGA Based Web Server” International Journal Of Engineering Sciences & Research, 2016.

6. **B.Vamsi Krishna** “Design and Development of Automated Public Transportation System and Traffic Guidance System” IOSR Journal of Electronics and Communication Engineering (IOSR-JECE) e-ISSN: 2278-2834, p-ISSN:2278-8735.Volume 12, Issue 1, Ver. II (Jan.-Feb. 2017), PP 37-44.
7. Y.Jaya Praksh Reddy, **B.Vamsy Krishna** “Wireless Notice –Board using Node MCU” International Research Journal of Engineering and Technology (IRJET) Journal Volume:07, issue: 08, August 2020.

Papers Published in Conferences No. : 1

B.Vamsi Krishna, Dr.Ravi Krishna Reddy, A.Ravi “Speckle Noise Reduction in SAR images using AMOSDS optimization with Multi Dimensional Transforms” Two day international conference held during 30th November 2018 to 1st December 2018 at Gudlavalleru Engineering College, 2018.

Workshops Organized No. : 21

1. Faculty Development Program on “VLSI and Embedded System Design” in 2012, organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru.
2. One day workshop on “Design and Development of sensor based applications using microcontrollers” for IV B.Tech. Students” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru in 2013.
3. Organized a workshop on “Blogging”, organized by IE in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held during 15th October, 2016.
4. Organized a Guest Lecture on “Multirate Signal Processing”, organized by IE in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held during 21st September, 2016.
5. Organized a Guest Lecture on “Skill Development for young Engineers to reform the core-sector “,organized by IE in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held during 15th September, 2016.
6. Organized a workshop on “Intelligent Systems for more Connected World: Internet of Things”, organized by IE in association with the ECE Dept. of Gudlavalleru

- Engineering College with the Technical Support of R & D Team of Efftronics Systems Pvt. Ltd., Vijayawada at Gudlavalleru held during 6th September, 2016.
7. Organized a Guest Lecture on “VLSI & Signal Processing -A View”, organized by IE in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held during 24th August, 2016.
 8. Organized a workshop on “Verilog Programming: Hands on Experience”, organized by IE in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held during 3rd – 7th August, 2016.
 9. Organized a Seminar on “Curriculum Design: Introduction to cognitive Internet of Things”, organized by ISF in association with IE and the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held during 3rd August, 2016.
 10. Organized a workshop on “Swarm Intelligence Techniques - Applications to Signal Processing, Communications and Instrumentation”, organized by IE in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held during 5th – 7th February, 2016.
 11. Organized a workshop on “Advanced Telecom Technologies”, organized by IE in association with the BSNL, Vijayawada and ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held on 11th – 13th January, 2016.
 12. Organized a workshop on “Image processing Applications using MATLAB”, organized by ISF in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held on 12th – 13th October, 2015.
 13. Organized a workshop on “Emerging Technology : Internet of Things”, organized by IE in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held on 21st – 22nd September, 2015.
 14. Organized a Guest Lecture on “ALIEN VISION : Exploring Electromagnetic Spectrum with Imaging Technology”, organized by IE in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held on 15th September, 2015.
 15. Organized a Guest Lecture on “Future Development of Product/Solution”, organized by ECSAt in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held on 5th September, 2015.

16. Organized a Guest Lecture on “Detection and Estimation in SONAR Signal Processing”, organized by Department of ECE, Gudlavalleru Engineering College, at Gudlavalleru held on 1st August, 2015.
17. Organized a two day workshop on ROBOTICS for III B.Tech ECE Students with Department of ECE, Gudlavalleru Engineering College, at Gudlavalleru held on 8 to 9th August, 2018.
18. Organized a two day workshop on PCB Design for II B.Tech ECE Students with Department of ECE, Gudlavalleru Engineering College, at Gudlavalleru held on 2nd to 3rd January, 2019.
19. Organized a two day workshop on IOT for III B.Tech ECE Students with Department of ECE, Gudlavalleru Engineering College, at Gudlavalleru held on 4th to 5th January, 2019.
20. Organized a two day workshop on DRONES for III B.Tech ECE Students with Department of ECE, Gudlavalleru Engineering College at Gudlavalleru held on 18th to 19th January, 2019.
21. Organized a one day workshop on Making of Electronics Circuits for I B.Tech ECE Students with Department of ECE, Gudlavalleru Engineering College, at Gudlavalleru held on 20th March, 2019.

Workshops /Conferences /Seminars Attended No. : 25

1. Two day 5th biannual technical Conference on “Signal Processing and Communication Engineering Systems” (SPACES) organized by Department of ECE, K L University held during 4th-5th January 2018 at K L University, Vaddeswaram.
2. Two day 6th International Conference on “Computing, Communication and Sensor Network” held during 30th-31st December 2017, organized by Applied Computer Technology in Association with International Association of Science, Technology and Management at Kolkata.
3. Two day FDP on “Advances in VLSI Design and Embedded systems” organized by department of ECE, Gudlavalleru engineering college, during 6th -7th November, 2017.

4. Attended in the “Popular lecture on applications of microwave energy” organized by IE (INDIA), A.P. state centre, Vijayawada on 4th September 2017.
5. Two day international workshop on “Advanced Modeling of Signals and System for Industrial Applications” (AMSSIA-2017) organized by ECE department, KL University during 9th-10th January 2017 at KL University, Vaddeswaram.
6. Three day workshop on “Research Methodology” organized by ECE department, KL University during 5th-7th January 2017 at KL University, Vaddeswaram.
7. One day workshop on “Embedded System Design for Wireless Sensor Networks Applications” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru on 7th January 2015.
8. One day workshop on “Speech Processing: Current Challenges and Hands-on Experience” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru on 23rd August 2014.
9. One week faculty refresher course on “Signals & Systems” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 9th – 13th June 2014.
10. Two day workshop on “Embedded System Development”, organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 30th - 31st December 2013.
11. Two day workshop on “Issues in Integrated Circuits (ICs) Design” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 13th – 14th December 2013.
12. One day workshop on “Ultra low Power Communication using CC430 Microcontroller: Hands on Experience” on 7th February 2015 organized by Signal Processing and Communication Research group, Department of ECE, Gudlavalleru Engineering College, Gudlavalleru.
13. One week faculty refresher course on “Electronic Devices and Circuits” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 23rd – 28th September, 2013.
14. Two day workshop on “Microwave Engineering Concepts and Lab Practice” on 9th to 10th June 2012 organized by Department of ECE, Gudlavalleru Engineering College, Gudlavalleru.

15. One day workshop on “Research in Energy Management Wireless Networks and Smart Transportation Systems” organized by Institution of Engineers, Vijayawada local center, Vijayawada on 29th March 15th 2014.
16. Two day Faculty development program on “VLSI and Embedded System Design” organized by Department of ECE in association with IETE Vijayawada local center, Vijayawada, during 7-8-July 2012.
17. A two day seminar 32nd National convention of Electronics and Telecommunication Engineering on “Intelligent system for more connected world Internet of Things” organized by Institution of Engineers India Vijayawada state center Vijayawada during 24-25th October 2016.
18. Five day workshop on “Signal Processing Applications” during 7th- 12th November 2016 organized by Department of ECE, Gudlavalleru Engineering College, Gudlavalleru.
19. A one week faculty development on Machine Learning Applications in Signal Processing organized by E & ICT academy, NIT Warangal and ECE department , Gudlavalleru Engineering College during 4th to 9th February 2019.
20. A Six day Faculty development programme on “Emerging Trends in VLSI Design and Technology with Hands on Training from 28th January to 2nd February 2019 under AICTE Margdarshan scheme organized by ECE department VR Siddhartha Engineering College.
21. One week Faculty development programme on “DNA of Mixed Signal IC Design for Portable Systems” organized by the E&ICT academy at department of Electronics and Communication Engineering, NIT Warangal from 20th -25th May 2019.
22. B.Vamsi Krishna, Dr.Ravi Krishna Reddy, A.Ravi “Speckle Noise Reduction in SAR images using AMOSDS optimization with Multi Dimensional Transforms” Two day international conference held during 30th November 2018 to 1st December 2018 at Gudlavalleru Engineering College, 2018.
23. Five day workshop organize by Apply Volt on “Industrial IOT” 19th to 23rd May 2020.
24. Two day FDP program conducted by Kallam Harinada Reddy Institute of Technology in association with IETE Vijayawada on “Microstrip Antennas for Advanced wireless Communications” during 15th to 16th May 2020.

25. Five day FDP program conducted by St. Martin's Engineering College on "RECENT COMMUNICATION TOOLS AND ITS APPLICATION" during 19th to 23rd May 2020.

Certifications/Training Programs Attended No. : 13

1. Two week ISTE STTP on "Introduction to Design of Algorithms" conducted by Indian Institute of Technology Kharagpur from 27th April to 30th May 2015 (This workshop was held under the national mission on education through ICT (MHRD)).
2. One week training program on "Training Teachers for Excellence", organized by Gudlavalleru Engineering College, Gudlavalleru on 17th to 21st June 2013.
3. Two week ISTE STTP on "Signal and Systems" conducted by Indian Institute of Technology Kharagpur from 2nd to 12th January 2014 (This workshop was held under the national mission on education through ICT (MHRD)).
4. Two week ISTE STTP on "Control Systems" conducted by Indian Institute of Technology Kharagpur from 2nd to 12th December 2014 (This workshop was held under the national mission on education through ICT (MHRD)).
5. One week refresher course on "Microprocessors and Microcontrollers" from 17/11/12 to 21/11/15 organized by research and development center of Gudlavalleru Engineering College.
6. One day training program on "MATLAB, Simulink and Related Tool Box for Engineering Education" organized by Academy of Design and Architecture in association with Department of ECE, Gudlavalleru Engineering College.
7. Two week sort term training program on "Transform Techniques in Signal Processing" from 8th June to 20th June 2009 organized by department of ECE Lakireddy Balireddy college of Engineering and sponsored by AICTE.
8. Two day staff training program on " Simulation of Linear & Digital Integrated Circuits and Microcontroller based Applications" using NI Multisim organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 4th to 5th November 2016.
9. One week national level FDP on "Signal and image processing using Lab View" held during 13th – 18th November, 2017 at Gudlavalleru engineering college, organized by Signal processing research group of department of ECE in association with NI systems Pvt. Ltd.

10. Two week faculty development program on “Digital VLSI Circuit Design” held during 3rd to 12th June 2017 jointly organized by NITW and ICT Academies at NIT Warangal.
11. One week faculty development program on “Communication s and Signal Processing” organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 29th October to 3rd November 2018.
12. Two day training program on “Design and Development of Digital Circuits using VLSI Mentor Graphic Tools” organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 6th to 7th November 2018.
13. AICTE sponsored Three week FDP on “Design Block Chain Technology” organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 10th to 29th August 2020.

ONLINE:

Webinars: 4

- 1) Attended an Online Webinar on “Radio Frequency Measurements for Cellular and Wireless Communication Systems” organized by Department of Electronics and Communication Engineering on 25th May, 2020.
- 2) Attended on Online Webinar on “Impact of COVID in Engineering Education & Way forward”, organized by ISTE Chapter, Gudlavalleru Engineering College, Gudlavalleru on 09-06-2020.
- 3) Attended an Online Webinar on “Crypto currencies and Block Chain”, organized by Chalapati Institute of Engineering and Technology on 19-05-2020.
- 4) Attended an Online Webinar on “Advanced IoT Applications - A Practical Approach” organized by Laki Reddy Bali Reddy College of Engineering 23rd May 2020.

Guest Lectures Delivered No. : Nil

Subjects Handled

Under Graduation

- Microprocessor and Microcontroller
- VLSI Design
- Electronic Measurements and Instrumentation
- Optical Communication
- Digital IC Applications
- Analog Communication

- Structured Digital Design
- Switching Theory and Logic Design
- Analog Electronics
- Elements of Electronic Engineering
- Digital design Through Verilog

Post Graduation: Nil

Other Responsibilities

College Level

1. GEC FEST '13 , GEC FEST '14, GEC FEST '15, GEC FEST '16,
2. One week space walk coordinator

Department Level

1. Time Table Coordinator
2. Autonomous Coordinator
3. ECSAt Faculty Advisor
4. NBA Criteria-2 Member
5. Class In-Charge
6. Industrial Tour Coordinator
7. SDP coordinator
8. NBA coordinator

Projects Guided

Under Graduation

S. No	Name of the project	Year
1	BABY MONITORING SYSTEM USING IOT	2020
2	Wireless Notice –Board using Node MCU	2020
3	MOTION DETECTION USING IR SENSOR AND COUNTERS	2016
4	Design and Development of Automated Public Transportation System	2015
5	RFID and GSM Based Intelligent Courier Mail Box with Automatic Delivery Notification	2014

Post Graduation

S.No	Name of the project	Year
1	A Novel Method To Improve The Logic Test By Using Single Cycle Access Structure	2013
2	Automatic Screening of Missing Objects and Identification with Group Coding of RF Tags	2013
3	Designing of Embedded Web Server based on Energy meter	2013
4	Design and Implementation of Modified Booth Encoder Multiplier using Carry Select Adder	2014
5	“A Real-Time Remote Control And Monitoring System Using Zynq SoC FPGA Based Web Server”	2016

Guiding Ph. D Students

University	Year of Registration	Area	Status
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Books Published No. : Nil

1. Co-author of book entitled “Electronics and Communication Engineering Instructional Material” with ISBN – 978-1- 4276-5539- 4.
2. Co-author of book entitled “Electronics and Communication Engineering laboratory Manual” with ISBN – 978-1- 4276-5536- 3.

R&D and Consultancy

S. No	Project Title	Source of Funding	Duration	Role	Status
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1	Design and Development of Automated Public Transportation System and Traffic Guidance System	UGC	2 years	Hardware and Software Development	Completed
2	Design of Wireless Intelligence Traffic Guidance System	In House R & D ECE, GEC	1 Year	Hardware and Software Development	On going

Patents:

1. A patent titled "PREVENTING VEHICLE ACCIDENTS: INTELLIGENT ACCIDENT AVOIDANCE USING SATELLITE-BASED POSITIONING SYSTEM" submitted to Intellectual Property India, Government of India under review process in July 2020.

Curriculum Design and Development

1. BOS member for autonomous course structure and syllabus preparation
2. Optical Communication Course module
3. Electronic Measurements and Instrumentation Course module
3. Microprocessor and Microcontroller Lab Manual

Academic Rewards / Achievements: Nil

Extra Curricular Activities: Nil

Declaration

I hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date: 14-09-2020
Place: Gudlavalleru

(B.Vamsy Krishna)
Signature