

FACULTY PROFILE

Name of the Faculty: **Vargil Vijay E**
Department: **ECE**
College: Gudlavalleru Engineering College
Mobile: **+91 9502392827**
E-Mail: **vargilvijay@gmail.com**



Address for Communication:

D.No.:1-3/4-226,
4 Poles Center,
Vidyadharapuram,
Vijayawada-520 012
Krishna Dist. (A.P.)

Academic Qualifications:

S. No	Name of the Degree (Starting from Ph.D to 10th Class)	University	Percentage of Marks/Grade	Specialization / Research Area	Duration
1	Ph.D.	JNTUA, Ananthapuram	--	Wireless Sensor Networks in Wireless Communications	Pursuing
2	M.TECH.	V. I. T. University, Vellore, T.N.	8.8 (CGPA)	Automotive Electronics (In Collaboration with TIFAC-CORE)	2007-2009
3	B.TECH.	JNTUH, Hyderabad	72.34%	Electronics & Communications Engineering (E.C.E.)	2003-2007
4	INTERMEDIATE	B.I.E.	88.4%	M.P.C.	2000-2002
5	S.S.C.	B.S.E.	79%	--	1999-2000

PROFESSIONAL EXPERIENCE:

Designation	Institution	Period
Assistant Professor	Gudlavalleru Engineering College	09-06-2009 to till date

MEMBERSHIP IN PROFESSIONAL BODIES:

S.No.	Name of Professional Body	Membership Number
1	IE(I)	AM162849-9

PAPERS PUBLISHED IN CONFERENCES/WORKSHOPS: 05

1. Vargil Vijay E, Ch.V. Rama Rao, G N Swamy, M. Kamaraju Published a paper titled **“Electronic Control Unit for an Adaptive Cruise Control System using EFI”** in International Conference on Next Generation Communication and Computing Systems (**ICNGC2S-10**), pp.191-193, held in Chandigarh during 25-26 December, 2010 [ISBN:978-93-81068-00-7].
2. Vargil Vijay E, Sunil Chakravarthy K published a paper titled **“New efficient Image Denoising Method for Digital Cameras with color filter array”** International Conference on Nano science Engineering & Advanced computing (ICNEAC-11) during July 8-10, 2011 Conducted in Swarnandhra College of Engineering & Technology, Andhra Pradesh.
3. Vargil Vijay E, Miranji K Published a paper titled **“Implementation of Pre-computational based content addressable Memory”** in International Conference on Nano science Engineering & Advanced computing (ICNEAC-11) during July 8-10, 2011 Conducted in Swarnandhra College of Engineering & Technology, Andhra Pradesh.
4. Vargil Vijay E, Dr. D. Prabhakar published paper titled **“Implementation of Rectangular Spiral Patch Antenna by using Coaxial Feed”** in International Conference on Computational and Intelligent Techniques for Automation of Engineering Systems” held in Gudlavalleru Engineering College during 30th November - 1st December, 2018.
5. Dr. D. Prabhakar, Vargil Vijay E published paper titled **“Implementation of Koch Snowflake Fractal Antenna for Multi-band Applications”** in International Conference on Computational and Intelligent Techniques for Automation of Engineering Systems” held in Gudlavalleru Engineering College during 30th November - 1st December, 2018.

PAPERS PUBLISHED IN JOURNALS: 08

1. G Rama Chandra Rao, E V Vijay, Ch. Sathish Kumar Published a paper titled **“Design of modified Radix 2² FFT algorithm in OFDM communication system”** in International Journal of VLSI & Signal Processing Applications, Vol. 2, Issue 5, ISSN 2231-3133, (392-396) during November 2012.

2. Baba S Fariddin and Vargil E Vijay “**Design of Efficient 16-Bit Parallel Prefix Ladner- Fischer Adder**” International Journal of Computer Applications volume 79- Number 16, 10-14, October 2013. Published by Foundation of Computer Science, New York, USA.
3. G.Subrahmanyam, T.JayaPrakash, E.VargilVijay,” **Data Acquisition and Processing Samples of an Explosive Material using Arm Microcontroller (ARM9263)**”, in International Journal of Computer Applications, (IJCA) Volume 88, Number 18, January-February 2014, published on line Online February 14th 2014, (www.ijcaonline.org) ISSN: 973-93-80880-48-3.
4. Ramesh Kakollu, Vargil Vijay E “**A Novel Pattern Classification using Granular Reflex Fuzzy Min-Max Neural Network**” in International Journal of Computer Applications(IJCA) ,pp. 29-33 Vol. 107, No. 19, December 2014.
5. Soma Sekhar, Vargil Vijay E “**All Digital Self Interference Cancellation technique for MIMO-Full Duplex System**” in International Journal of Computer Applications (IJCA) , pp.13-18, Vol. 148-No. 10 ,August, 2016.
6. P. Srinivasa Rao, Vargil Vijay E, “ **A frame by frame Digital video de-noising by Non-Local Means Algorithm and Bilateral Filter**” in International Journal of Advanced Research in Computer Science and Software Engineering”,pp. 266-270, Vol. 6, Issue 9, ISSN:2277 128X,September 2016.
7. A. Loka Pavani, Vargil Vijay E, “**A Low PAPR OFDM System with PTS Technique using SUI 5 Channel**” in International Journal of Innovative Research in Electrical, Electronics, Instrumentation and Control Engineering, ISO 3297:2007 Certified Vol. 5, Issue 9, ISSN (Online) 2321 – 2004 ,September 2017.
8. S.Shanmukhi Devi , Mr. Vargil Vijay. E, “**Change Detection Using Spatial Fuzzy C-Means Clustering For VHR Remote Sensing Images**” in International Journal of Creative Research Thoughts (IJCRT), **Volume 6, Issue 2, ISSN: 2320-2882, April 2018.**
9. Vijay E V, K. Siva Anjaneyulu, G. Jagadeesh, K. Poornima c, K. Mohan K published a paper titled “**Crop Pestilence Detection using Image Segmentation**” in International Journal of Research in Advent Technology, Vol. 7, No. 3, E-ISSN:2321-9637, March 2019.

LIST OF WORKSHOPS ORGANIZED:

- Coordinator for Three day workshop on “**Overview on Latest Telecom Technologies**” by BSNL Krishna District during 18-20 December, 2014.
- Coordinator for National Level Technical Project Expo- **YODHA ’14** conducted by Dept. of ECE Gudlavalleru Engineering College on 4th February, 2015.
- Coordinator for Workshop on “**Microwave Devices: Hands-on Experience in association**” in Association with Vajenthura Microwave Solutions Ltd., Chennai.

LIST OF WORKSHOPS /CONFERENCES /SEMINARS ATTENDED:

- Attended AICTE Sponsored one day National Seminar on “**Wavelets and its Applications**” at V. R. Siddhartha Engineering College, Gudlavalleru on 9th August, 2009
- Attended AICTE sponsored staff development program on “**Current trends in Biometrics**” at Gudlavalleru Engineering College, Gudlavalleru during 2-14 November, 2009.
- Attended A two day workshop on “**Embedded Design using Xilinx FPGA**” organized by TIFAC-CORE in Telematics and ECE Department at V.R. Siddhartha Engineering College, Vijayawada during 8-9 July, 2011.
- Attended one-day workshop on “**Analog and Mixed Signal VLSI Design**” at Gudlavalleru Engineering College, Gudlavalleru on 15th October 2011.
- Attended workshop on “**RF system Design & Testing**” at V. R. Siddhartha Engineering College, Vijayawada in collaboration with ROHDE & SCHWARZ on 18th December, 2011.
- Attended Technical seminar on “**Advanced Design Concepts in Electronics & Telecommunication**” at vijayawada organized by FALCON Electro-Tek Pvt. Ltd., Mumbai on 22nd June, 2012.
- Attended Six day Faculty Development program on “ **Analog System design using Texas instruments Analog system Lab Kit (ASLK)**” at PVP Siddhartha Institute of TECHNOLOGY, VIJAYAWADA DURING 4-9 JUNE, 2012.
- Attended Faculty Development Program on **VLSI & Embedded System Design** at Gudlavalleru Engineering College, Gudlavalleru during 7-8 July, 2012.
- Attended workshop on “**SPEECH SIGNAL PROCESSING**” organized by Department of Electronics & Communication Engineering of National Institute of Technology, Warangal sponsored by TEQIP Phase-II during 10-12 May, 2013.
- Attended A two day workshops on “**Issues in Integrated Circuits (ICs) Design**” organized by Gudlavalleru Engineering College, Gudlavalleru during 13-14 December, 2013.
- Attended a Three day National Workshop on “**Theory & Applications of Intelligent Signal Processing**” organized by ECE Department of Gudlavalleru Engineering College, Gudlavalleru during 28 February – 2 May 2014.
- Attended one week Workshop on “**Training Teachers for Excellence**” at Department of Electronics & communication Engineering of Gudlavalleru Engineering College, Gudlavalleru during 29th April to 4th May 2013.
- Participated in a Two- day ISTE Workshop on “**Aakash for Education**” conducted by Indian Institute of Technology, Mumbai held under the National Mission on Education Through ICT (MHRD) during 10-11 November, 2012.
- Attended the Complimentary Training / Orientation on “**MATLAB, Simulink & Related tool boxes for Engineering Education**” conducted by Gudlavalleru Engineering College & Mathworks Matlab Academy of Design & Architecture during 3-4 April,2013.
- Participated in a Two-week ISTE Workshop on “**Analog Electronics**” conducted by Indian Institute of Technology, Kharagpur under National Mission on Education through ICT (MHRD) from 4-14 June, 2013

- Participated in National Level Workshop on **“Advanced Trends in Embedded systems design (practical Approach)”** conducted by R.V.R. & J.C. College of Engineering, Guntur during 14-15 December, 2012.
- Attended one-week refresher course on **“Electronic Devices & Circuits”** conducted by Gudlavalleru Engineering College during 23-28 September, 2013.
- Attended Two-day workshop on **“Development of Embedded System- A Practical Approach”** conducted by Department of ECE of Gudlavalleru Engineering College, Gudlavalleru during 20-21 August, 2014.
- Attended one day workshop on **“Ultra Low Power Communication using CC430 Microcontroller: Hands on Experience”** conducted Department of ECE, Gudlavalleru Engineering College on 7th February, 2015.
- Participated in a Two-week ISTE workshop on **“Signals & Systems”** conducted by Indian Institute of Technology, Kharagpur under National Mission on Education through ICT (MHRD) during 2-12 January, 2014.
- Participated in one day workshop on **“Speech Processing:: Current Challenges and Hands-on Experience”** conducted by Department of ECE of Gudlavalleru Engineering College on 23rd August, 2014.
- Attended Two Day National Seminar on **“Recent Advances in Microwaves & Antennas” sponsored by Department of Science & Technology (DST), New Delhi** Conducted by Sri Vasavi Engineering College, Tadepalligudem during 6-7 Septmeber, 2014.
- Attended **“Overview on Latest Telecom Technologies”** conducted by BSNL, Krishna during 18-20 December, 2014.
- Attended **“One day Research & Development Facilitation workshop”** conducted by The Institution of Engineers (I) ,Vijayawada Local center on 29th March, 2015.
- Attended a two day staff training program on **“Simulation of Linear & Digital Integrated Circuits and Microcontroller based Applications”** using NI Multisim organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru.
- Attended a two day national seminar on **“Intelligent System For More Connected World: Internet of Things”** organized by Institution of Engineers (India) on behalf of 32nd National convention of Electronics & Telecommunication Engineers held at Vijayawada during 24-25 October, 2016
- Participated in two AICTE Sponsored Seminar on **“Advanced Digital Image processing Techniques (ADIPT)** at NBKR Institute of Science & Technology, Nellore, A.P. during 15-16 Decmeber, 2017.

Certification/ Training Programs Attended:

- Attended Five-day training program on **“Mentor Graphics (Front End & Back End) and Xilinx Tools”** at Gudlavalleru Engineering College, Gudlavalleru during 13-17 September, 2010.

- Attended 2 Day Training on “**Digital Image Processing using MATLAB** ” organized by Computer centre, MIT Campus, Anna University, Chennai during 20 – 21 June, 2015.
- Attended one Week faculty Development programme on “**Development of Wireless Sensor Networks Using Simulator NS-2- Hands on Experience**” Organized by Communication Research Group in Association with Vertex soft Hyderabad at Gulavalleru Engineering College Campus during 1 -5 December, 2015.
- Participated in one week National Level Faculty Development Program on “Signals and Image Processing using LabVIEW” at Gudlavalleru Engineering college campus during 13-18 November,2017.
- Attended one Week Faculty Development Programme on “Communications and Signal Processing” organized by Dept. of ECE, Gudlavalleru during 29th October – 3rd November, 2018.
- Attended one Week Faculty Development Programme on “Machine Learning Applications in Signal Processing” organized by Dept. of ECE, Gudlavalleru during 4th February – 9th February, 2019.
- Attended a Five day online FDP on “ IPR Awareness and patent prosecution”organized by CMR Engineering College, Hyderabad during 6-10 May, 2020.
- Attended one week online faculty development program on “ Research challenges and Opportunities post covid-19 (RECOP-2020) organized by Sri Vasavi Engineering college, Tadepalligudem during 4-9 May, 2020.
- Attended Five day National level online faculty development program on “ Modern Trends in Communication , Signal Processing and VLSI/MEMS” organized by Andhra Loyola Institute of Engineering and Technology, Vijayawada during 19-23 May, 2020.
- Completed one week online Faculty awareness program on “Research Methodology” organized by Department of Mechanical Engineering Shri Chhatrapati Shivajiraje College of Engineering, Pune during the period of 20-25 May, 2020 with a score of 89%.
- Participated in Five days online FDP on Computing & Signal Processing in IoT applications organized by QIS College of Engineering and Technology, Ongole and co-sponsored by IEEE SSIT Society Madras section during 23-27 May,2020 .
- Participated in National Level online Faculty Development Program on “Cyber Security” Organized by Gudlavalleru Engineering College during 22-26, July 2020.
- Participated in AICTE Sponsored Online Short Term Training Program on “ Blockchain Architecture design and use cases” Phase-I organized by Department of ECE, Gudlavalleru Engineering College, during 10-15 August,2020.

NPTEL Online Certification Courses Completed

- Principles of Communication Systems-I with a Score of 82% (In Top 5% of Course)

Subjects Handled: Under Graduation

- Digital Communications
- Digital Signal Processing
- Analog Communications
- Signals & Systems
- Optical Communications
- Electromagnetic field Theory
- Microwave & Optical Communications

Post Graduation

- DSP Processors & Architecture
- Advanced Digital signal Processing
- Digital Data Communications

Labs Handled

Under Graduation

- Digital Communications Lab
- Analog Communications Lab
- Digital Signal Processing Lab
- Electronic Devices & Circuits Lab
- Electronic Computer Aided Design Lab
- Digital Circuit Design Lab

Other Responsibilities

College Level:

- EPABX Incharge(2012-2014)
- GECFEST '09, '15,'18 '16 –In-Charge.
- Central Lectures- Incharge (2016 – till date)

Department Level:

- Time Table Coodintor for A.Y.:2010-11 , 2015-16 & 2016-till date.
- ECSAt Faculty Advisor in A.Y.: 2012-13

RESEARCH PAPERS / BOOKS:

BOOKS PUBLISHED:

1. Co-author for chapter 3.1 titled “**Digital Communications LAB(Autonomous)**” of **Electronics and Communication Engineering laboratory Manual** series 2 with **ISBN – 978-1-4276-5564-6** in the year 2016
2. Co-author for chapter 4.1 titled “**Digital Communications LAB(R13)**” of **Electronics and Communication Engineering laboratory manual** series with **ISBN – 978-1-4276-5563-9** in the year 2016
3. Co-author for chapter 12 titled “**Digital Communications**” of **Electronics and Communication Engineering Instructional manual** with **ISBN – 978-1-4276-5539-4** in the year 2015.

4. Co-author for chapter 3.1 titled “**Digital Communications(R10)**” of **Electronics and Communication Engineering laboratory Manual** with ISBN – **978-1-4276-5536-3** in the year 2015

Curriculum Design & Development:

Enacted as Board of Studies member for the Autonomous curriculum design of Electronics and Communication Engineering at Gudlavalleru Engineering College, Gudlavalleru.

Curriculum developed for following subjects:-

1. Digital Communications
2. Digital Communications Lab
3. Digital Signal Processors & Architecture

Other Achievements: Got campus placement in Cognizant Technology Solutions (CTS) Pvt. Ltd. in 2008.

Declaration:

I hereby declare that all the above furnished information is correct to the best of my knowledge.

Date:

Place: Gudlavalleru

(Signature)