

SRI CHAKRAPANI YELLAMRAJU

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RESIDENTIAL ADDRESS:

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ACADEMIC QUALIFICATIONS:

PhD (Pursing) in VLSI & Embedded systems.

Master of Technology in VLSI & Embedded systems.

Bachelor of Engineering in Electronics and Communication Engineering

ACADEMIC REWARDS / ACHIEVEMENTS: NIL

PROFESSIONAL EXPERIENCE:

Designation	Institution	Period
Assistant Professor	Gudlavalleru Engineering College	30-10-2012 to till date
Assistant Professor	Bapatla Engineering College	07-11-2007 to 29-10-2012
Project Intern	VSBU, Wipro Technologies, Bangalore	01-01-2005 to 30-04-2005

PROJECTS GUIDED:

PhD	Post Graduation	Under Graduation
NIL	09	23

SPECIALIZATION: VLSI & Embedded systems

RESEARCH PAPERS / BOOKS: NIL

BOOKS PUBLISHED IN PUBLICATIONS: NIL

PAPERS PUBLISHED IN JOURNALS: 11

1. Surapaneni Aparna, T. Venkata Lakshmi, M. Kamaraju, **Y.Sri Chakrapani**," Implementation of Deep Learning Accelerator Unit", International Journal of Engineering Research & Technology (IJERT), vol. 8, issue 07, July-2019, ISSN: 2278-0181.
2. Surapaneni Aparna, **Y. Sri Chakrapani**, T. Venkata Lakshmi, M. Kamaraju, "A Survey of VLSI Architectures for Hamming Code Algorithm", International Journal of Advanced Scientific Research and Management, Special issue February 2019, pp. 1-10, CITAES-18, ISSN 2455-6378.
3. Y. Mamillu, **Y. Sri Chakrapani**, M. Kamaraju, "FPGA Implementation of Integral Image generator in SURF detector", International Journal of Electronics Engineering, Volume 10, Issue 2, pp. 233-240, June 2018.
4. J.Naga Surekha, **Y. Sri Chakrapani**, M. Kamaraju, "Implementation of High Performance Speeded Up Robust features Detection", International Journal of Scientific & Engineering Research Volume 8, Issue 5, May 2017.
5. J.Naga Surekha, **Y. Sri Chakrapani**, N.Venkatewsara Rao, "A Survey of Trends in Local Invariant Feature Detectors", International Journal of Electrical and Electronic Engineering & Telecommunications, Volume 2, Issue 5, April 2017.
6. K. Sai Kumar, **Y.Sri Chakrapani**, M. Kamaraju, "Implementation of Low Power Ultrasonic Phased Array Using ZYNQ SoC", International Journal of Computer Science and Information Technology & Security, Volume 6, Issue 4, July August 2016.
7. Y. Sasi Rekha, **Y.Sri Chakrapani**, M. Kamaraju, "Design and Implementation of Low Power, Light Weight Cryptographic Core using Hummingbird2 Algorithm", International Journal of Scientific & Engineering Research, Volume 4, Issue 35, August-2015.
8. K.Sindhu, **Y.Sri Chakrapani**, M. Kamaraju, "FPGA Implementation of Irrigation Control System", International Journal of Scientific & Engineering Research, Volume 5, Issue 12, December-2014.
9. M.Aparna, **Y.Sri Chakrapani**, M. Kamaraju, " FPGA Implementation of an Agricultural Management System using Wireless Networks", International Journal of Computer Applications, Volume 105 – No. 18, November 2014.
10. S.Venkata Kishore, **Y.Sri Chakrapani**, M. Kamaraju, " Design and Implementation of High Speed Data Transmission over Dual Independent Aurora Channels on One GTX dual tile using Virtex-5 FPGA", International Journal of Scientific & Engineering Research, Volume 4, Issue 12, December-2013.

11. V. Krishna Kumari, **Y.Sri Chakrapani**, M. Kamaraju, “Design and Characterization of Koggestone, Sparse Koggestone, Spanning tree and Brentkung Adders”, International Journal of Scientific & Engineering Research, Volume 4, Issue 10, October-2013

PAPERS PUBLISHED IN CONFERENCES/WORKSHOPS: 07

1. Y.Mamillu, Y. Sri Chakrapani, N.Venkateswara Rao, “FPGA Implementation of Integral Image generator in SURF detector”, Online National conference on Communications and Computing (NC3-2020), organized by Department of Electronics and Communication Engineering, Bapatla Engineering College on 28th & 29th July 2020.
2. G.Poojitha, K.Uday Bhaskar, K.Vinay, P.Vijay kumar, **Y. Sri Chakrapani**, “Implementation of Sobel Edge Detection using FPGA”, Online National conference on Communications and Computing (NC3-2020), organized by Department of Electronics and Communication Engineering, Bapatla Engineering College on 28th & 29th July 2020.
3. Surapaneni Aparna, **Y. Sri Chakrapani**, T. Venkata Lakshmi, M. Kamaraju, “A Survey of VLSI Architectures for Hamming Code Algorithm”, CITAES-18, December 2018, Gudlavalleru Engineering College, Gudlavalleru.
4. J.Naga Surekha, **Y. Sri Chakrapani**, N.Venkateswara Rao, “A Survey of Trends in Local Invariant Feature Detectors”, National Conference on Emerging Trends in Electronics and Communications Engineering, Department of Electronics and Communications Engineering, Bapatla Engineering College on 20th & 21st January 2017.
5. **Y.Sri Chakrapani**, M. Kamaraju, N.Venkateswara Rao, “FPGA Implementation low power UART”, National Conference on RF Wireless Communication & Signal Processing (NCRFWCSP) 2014, Department of Electronics and Communications Engineering, Bapatla Engineering College.
6. M.Aparna, **Y.Sri Chakrapani**, M. Kamaraju, “ FPGA Implementation of an Agricultural Management System using Wireless Networks”, National Conference On RF Wireless Communication & Signal Processing (NCRFWCSP) 2014, Department of Electronics and Communications Engineering, Bapatla Engineering College.
7. **Y.Sri Chakrapani**, A.V.Sri Harsha, G. Nitin, “Implementation of Elliptic Curve Digital Signature Algorithm For Security Applications”, National Conference on Social Networking, Aurora’s Engineering College, Bhongir on 16th & 17th March 2012.

LIST OF WORKSHOPS ORGANIZED: 12

- 1.A Two Day Faculty Development Programme on “Design and development of digital circuits using VLSI Tools”, 6th - 7th December 2018 at Gudlavalleru Engineering College, Gudlavalleru.

- 2.A Two Day Faculty Training Programme on “Advances in VLSI Design and Embedded systems”, 6th - 7th November 2017 at Gudlavalleru Engineering College, Gudlavalleru.
- 3.A Two Day Training Programme on “Mentor Graphics Tools”, 31st August – 1st September, 2017 at Gudlavalleru Engineering College, Gudlavalleru.
- 4.A One day seminar on “Memory Design Techniques”, 08th January 2016 at Gudlavalleru Engineering College, Gudlavalleru.
- 5.A One day seminar on “Advanced VLSI Design: special topics”, 08th June 2015 at Gudlavalleru Engineering College, Gudlavalleru.
- 6.A Two day workshop on “Embedded System Design and Programming”, 19th to 20th December 2014 at Gudlavalleru Engineering College, Gudlavalleru.
- 7.A Workshop on “Low Power VLSI Design”, 9th August 2014 at Gudlavalleru Engineering College, Gudlavalleru.
- 8.A Refresher Course on “Digital System Design & Digital IC Applications”, 16th to 20th June 2014 at Gudlavalleru Engineering College, Gudlavalleru.
- 9.A Five Day Training Program on “Design Of Integrated Circuits Using Mentor Graphics Tools”, 21st to 25th January 2014 at Gudlavalleru Engineering College, Gudlavalleru.
10. A Three Day Workshop on “VLSI Design”, 8th to 10th February 2013 at Gudlavalleru Engineering College, Gudlavalleru.
11. A Two Day Workshop on “ROBOTICS”, 12th to 13th March 2011 at Bapatla Engineering College, Bapatla.
12. A Two Day Workshop on “VLSI Design Flow”, 2nd to 3rd March 2008 at Bapatla Engineering College, Bapatla

LIST OF WORKSHOPS /CONFERENCES /SEMINARS ATTENDED: 50

1. AICTE sponsored Online Short Term Training Programme on "Blockchain Architecture Design and Use Cases" Phase-I, II, III organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College from 10th to 29th August 2020.
2. A National Level Webinar on “Intellectual Property Rights & Patents - A View” organized by IQAC, Gudlavalleru Engineering College, Gudlavalleru from 10th to 12th August 2020.
3. A One week STTP on “Recent Trends In Wireless Sensor Networks & Applications”, organized by Malla Reddy Engineering College for Women, from 3rd to 8th August, 2020.

4. A Refresher Course on “Embedded System Design”, organized by Department of ECE, Gudlavalleru Engineering College, Gudlavalleru, from 27th July to 1st August 2020.
5. A National Level Faculty Development Program on “Cyber Security” organized by Department of CSE, Gudlavalleru Engineering College, from 22nd to 18th July 2020.
6. A One Week FDP on “Recent Advances in Biomedical Applications and Communications Networks”, organized by Department of ECE, GMRIT, Rajam, from 13th to 18th July 2020.
7. A One Week FDP on “Exposure & Rejuvenation of Technologies in changed Era of the World”, organized by Department of ECE, Raj Kumar Goel Institute of Technology, Ghaziabad, from 6th to 10th July 2020.
8. A National Level Faculty Development Program on “PCB Design” organized by Department of ECE, VVIET, Mysuru in association with Sienna ECAD Technologies and ESSCI from 29th June 2020 to 03rd July 2020.
9. A One Week FDP on "Role of Machine Learning & Data Sciences in AI", organized by Department of CSE and M&H, Mahatma Gandhi Institute of Technology, Hyderabad, from 28th June to 2nd July 2020.
10. A National Level Five Day Online Faculty Development Programme on “Scilab and NgSpice” organized by the Department of Electronics & Communication Engineering in association with Spoken Tutorial, developed by IIT Bombay at Universal College of Engineering and Technology, Guntur, from 22nd to 26th June 2020.
11. A Five day online FDP on “Opportunities and Challenges in Next-Generation Semiconductor Devices" organized by Department of Electronics and communication Engineering, Anil Neerukonda Institute of Technology and Sciences, Visakhapatnam from 16th to 20th June, 2020.
12. A One Week International Online Knowledge Development Program on "Challenges and Advancements in the Design of IoT, Embedded and VLSI Systems: Researchers View" organized by Department of Electronics & Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru, from 8th to 13th June , 2020.
13. A webinar on "Impact of COVID in Engineering Education & Way forward" organized by Indian Society for Technical Education (ISTE) Chapter, Gudlavalleru Engineering College, Gudlavalleru on June 9th 2020.
14. A National Level Online FDP on "Recent Trends in Communication and Wireless Networks" organized by KKR & KSR Institute of Technology & Sciences, Guntur, from June 5th to 9th, 2020.
15. A One Week Online E-certification FDP on “Industrial IOT using LoRa Technology” organized by Vemu Institute of Technology, College and Apply volt from June 1st to 5th, 2020.
16. A One Week National Level Online Faculty Development Program on " Research Challenges and Emerging Trends in Electronics and Communication Engineering (RCEECE2020) " organized by Ramachandra College of Engineering, Eluru from May 27th to June 1st , 2020.
17. A Two day workshop on "Introduction to Robotics”, conducted by IIT, Bombay at Saint Pious X Degree & PG college For Women, Hyderabad on 23rd – 24th July, 2019

18. A Short Term Training Programme through ICT mode on “Evaluating Student’s Performance and Designing Questions Papers”, conducted by NITTR, Kolkata at Gudlavalleru Engineering College, Gudlavalleru on 25th February 2019 to 1st March 2019,
19. A Two Day Training Programme on “Design and Development of Digital Circuits using VLSI Tools”, at Gudlavalleru Engineering College, Gudlavalleru on 6th – 7th December 2018..
20. A One Week FDP on “Trends in Reconfigurable (FPGA) SoC Design” at NIT Warangal on 25th – 30th November 2018.
21. A one week Faculty Development Program on “Communications and Signal processing”, at Gudlavalleru Engineering College, Gudlavalleru from 29th October to 3rd November 2018.
22. A Two day workshop on "Custom IC Design using Mentor EDA Tools” at Bapatla Engineering College, Bapatla on 16th - 17th April, 2018.
23. A one week National level Faculty Development Program on “Signal and Image processing using LabView”, at Gudlavalleru Engineering College, Gudlavalleru on 13th – 18th November 2017.
24. A Two Day Faculty Training Programme on “Advances in VLSI Design and Embedded systems”, at Gudlavalleru Engineering College, Gudlavalleru on 6th - 7th November 2017.
25. A Two Day Training Programme on “Mentor Graphics Tools”, at Gudlavalleru Engineering College, Gudlavalleru 31st August – 1st September, 2017.
26. A Two day National Workshop on “Advances in Electronics and Communication Engineering”, at JNTUK, Kakinada on 26th – 27th August, 2017.
27. A Two week STTP on “CMOS Mixed Signal and Radio Frequency VLSI Design” at Gudlavalleru Engineering College, Gudlavalleru, conducted by IIT Kharagpur on 30th January– 4th February 2017.
28. A one week National level Faculty Development Program on “Hands on experience in Signal Processing Applications”, at Gudlavalleru Engineering College, Gudlavalleru on 7th – 12th November 2016.
29. A Two Day Training program on "Simulation of Linear & Digital Integrated Circuits and Microcontroller based Applications" using NI Multisim at Gudlavalleru Engineering College, Gudlavalleru on 4th -5th November 2016.
30. A Two Day National Convention of Electronics and Telecommunication Engineers and National Seminar on “Intelligent System for more Connected World: Internet of Things” at Vijayawada on 24th - 25th October 2016.

31. A one day workshop on “Brainstorming on innovation in incubation centres” at JNTUK, Kakinada on 1st September 2016.
32. A Five Day Refresher course on “Microprocessors and Microcontrollers” at Gudlavalleru Engineering college, Gudlavalleru on 17th - 21st November 2015.
33. A Two Day All India Workshop on Emerging Techniques on “VLSI Architecture Design for Real Time Environment” at PACE Institute of Technology & Sciences, Ongole on 9th - 10th October 2015.
34. A Five Day Workshop on “Low power System on Chip Design using Xilinx FPGA Devices” at NIT Warangal on 3rd - 7th October 2015.
35. All India Seminar on “Research in Energy management, wireless networks & Smart Transportation systems” at Institute of Engineers, Vijayawada Local Centre on 29th March 2015.
36. A Two Day All India Seminar on “Agricultural And Horticultural Applications Of Unmanned Vehicles For Micro – Farming” at Shri Vishnu Engineering College for Women on 26th to 27th March 2015.
37. A Workshop on “Ultra Low Power Communication using CC430 Microcontroller” at Gudlavalleru Engineering College, Gudlavalleru on 7th February 2015.
38. A One day seminar on “Research in Signal and Image Processing on FPGA’s and Embedded platforms” at Institute of Engineers, Vijayawada Local Centre on 22nd November 2014.
39. A Two day National Conference on RF Wireless Communication & Signal Processing (NCRFWCSP) November 17th – 18th 2014, Department of Electronics and Communications Engineering, Bapatla Engineering College.
40. A One day workshop on “Speech Processing: Current Challenges and Hands on experience” at Gudlavalleru Engineering College, Gudlavalleru on 23rd August 2014.
41. A Two week ISTE Workshop on “Signals & Systems” at Gudlavalleru Engineering College, Gudlavalleru, conducted by IIT Kharagpur on 2nd – 12th January 2014.
42. A Two Day Workshop on “Issues in Integrated Circuits (IC) Design” at Gudlavalleru Engineering College, Gudlavalleru on 13th – 14th December 2013.
43. A Two week ISTE Workshop on “Analog Electronics” at Gudlavalleru Engineering College, Gudlavalleru, conducted by IIT Kharagpur on 4th – 14th June 2013.
44. A Training program on “MATLAB, Simulink & Related Tool Boxes for Engineering Education” at Gudlavalleru Engineering College, Gudlavalleru, conducted by Academy of Design & Architecture on 3rd – 4th April 2013.

45. Participated in “Advanced Embedded Systems” workshop at CDAC, Hyderabad from 18th to 29th June 2012.
46. A Two day National Conference on Social Networking, Aurora’s Engineering College, Bhongir on 16th & 17th March 2012.
47. Participated in “VLSI Design & Testing” workshop at PBR VITS, Kavali on 2nd and 3rd December 2011.
48. Participated in “FPGA Based DSP Implementation” using XILINX Workshop at GMRIT, Rajam on 26th and 27th November 2010.
49. Participated in “VLSI Design Software” Workshop at JNTU, Kakinada on 27th and 28th September 2008.
50. Participated in “Low Power VLSI Design” short term course at SNIST, Hyderabad from 3rd to 8th December 2007.

CERTIFICATIONS:

1. Audit Course “Research Methodologies” during May 1st – 15th, 2015 at JNTUK, Kakinada.
2. Audit Course “Intellectual Property Rights during May 16th – 30th, 2015 at JNTUK, Kakinada.
3. Completed Course “Nanoelectronics & Its Industrial Applications” at Nano Science and Technology Consortium, Noida.

GUEST LECTURES DELIVERED: 5

1. A Refresher Course on “Embedded System Design :: State Machine and Concurrent Process Models”, 30th July 2020 at Gudlavalleru Engineering College, Gudlavalleru.
2. Faculty Seminar on “Carbon Nanotubes”, 25th September 2014 at Gudlavalleru Engineering College, Gudlavalleru.
3. A Refresher Course on “Digital System Design & Digital IC Applications”, 19th June 2014 at Gudlavalleru Engineering College, Gudlavalleru.
4. A Refresher Course on “Electronic Devices and Circuits”, 24th September 2013 at Gudlavalleru Engineering College, Gudlavalleru.
5. Faculty Seminar on “System on Chip”, 20th February 2013 at Gudlavalleru Engineering College, Gudlavalleru.

MEMBERSHIP IN PROFESSIONAL BODIES:

S. No	Name of the Professional body	Membership Number
1	IEI	M-151044-9

CONSULTANCY ACTIVITIES:

NIL

GRANTS FETCHED:

NIL