

CURRICULUM VITAE

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RAJASEKHAR BUTTA

Objective:

Intend to build a career with a leading corporate of hi-tech environment with committed and dedicated people, which will help me to exploit my skills in the best possible way. Willing to work as a key player in challenging and creative environment.

Academic Qualifications

S. No.	Name of the Degree	University	Percentage of Marks	Specialization	Year of Pass
1.	Ph.D.	JNTUA, Anantapuramu	-	Signal Processing & VLSI	Submitted Thesis in Sep 2019
2.	M.Tech.	JNTU, Hyderabad	82%	Digital Systems & Computer Electronics	2007
3.	B.E.	Dr. BAMU, Aurangabad	62%	Electronics	1998
4.	Intermediate	BHE, Hyderabad	73%	MPC	1993
5.	S.S.C	BSE, Hyderabad	70%	-	1991

Professional Experience: 20 Years 7 Month (Till July 2020)

S. No.	Designation	Institution Name	Working Period	
			From	To
1.	Associate Professor	Gudlavalleru Engineering College, Gudlavalleru	June 2011	Till Date
2.	Associate Professor	LakiReddy Balireddy College of Engineering, Mylavaram	Aug 2009	May 2011
3.	Associate Professor	Aurora's Technological & Research Institute, Hyderabad	July 2008	July 2009
4.	Associate Professor	Vignan Institute of Technology & Science, Hyderabad	June 2006	June 2008
5.	Assistant Professor	Karshak Engineering College, Hyderabad	June 2003	June 2006
6.	Assistant Professor	Ramappa Engineering College, Mahabubabad	Dec 2000	May 2003
7.	Teaching Assistant	Rajeev Gandhi Memorial College of Engineering and Technology, Nandyal	Dec 1999	Nov 2000

Academic Identity:

ORCID Id

0000-0003-0860-7117 (<https://orcid.org/0000-0003-0860-7117>)

Scopus Id

57200205857 (<https://www.scopus.com/authid/detail.uri?authorId=57200205857>)

Web of Science ResearcherID

AAM-4703-2020

Patent Published (1)

1. B. Rajasekhar, M. Kamaraju, V. Sumalatha, “Intelligent Customer Care Center”, The Patent Office Journal No. 02/2020, dated 10/01/2020, page no. 1897.

Papers Published in Journals (17)

1. B, Rajasekhar, M, Kamaraju, V, Sumalatha, “A novel speech emotion recognition model using mean update of particle swarm and whale optimization-based deep belief network”, Data Technologies and Applications, **Emerald Insight**, ISSN 2514-9288, 17 April 2020, DOI 10.1108/DTA-07-2019-0120.

Impact Factor: 0.556(2016), H-Index:1, SSCI (Social Science Citation Indexed)

2. B. Rajasekhar, M. Kamaraju, V. Sumalatha, “Glowworm Swarm based Fuzzy classifier with Dual Features for Speech Emotion Recognition”, Evolutionary Intelligence, **Springer Journal**, ISSN 1864-5909, 16 July 2019, DOI 10.1007/s12065-019-00262-1, **Impact Factor: 0.94, SJR: 0.222, H-Index: 19, SCOPUS INDEXED.**
3. **B. Rajasekhar**, M. Kamaraju, V. Sumalath, “Speaker Independent Emotion Recognition From Speech Signals”, ARPN Journal of Engineering and Applied Sciences, ISSN: 1819-6608, Vol. 13, No. 9, May 2018, pp: 3188-3191. **SJR: 0.19, H-Index: 17, SCOPUS INDEXED.**
4. D. Naga Prasad, B. Rajasekhar, “FPGA-Based Low-power Emotion Recognition with Recurrent Neural Networks”, International Journal of Electronics Engineering (IJEE), ISSN: 0973-7383, Vol 10, Issue 2, pp: 241-247, June 2018-Dec 2018. **Impact factor: 5.1 UGC Approved Journal.**
5. B. Rajasekhar, M. Kamaraju, V. Sumalatha, “FPGA Based Recognition of Emotions from Speech Signals”, 2017 Third International Conference on Biosignals, Images and Instrumentation (ICBSII), **IEEE Xplore**: 26 Oct 2017, 10.1109/ICBSII.2017.8082290.
6. Ramya Krishna, **B. Rajasekhar**, “Biometrics Mechanism over Wireless Networks using Steganographic Method”, International Journal of Advanced Technology and Innovative Research (IJATIR), vol. 9, issue 9, ISSN 2348 – 2370, August 2017, pp: 1512-1520. **Impact factor: 4.1**
7. K. Sarat Chandra, B. Rajasekhar, M. Kamaraju, “Enhancing the Accuracy of Emotion Recognition Using Emotion-Specific Features”, International Journal of Electronics, Electrical and Computational System (IJEECS), ISSN 2348-117X, Volume 6, Issue 5 May 2017, pp: 344-346. **Impact factor: 2.52**
8. N. Sowjanya, **B. Rajasekhar**, M. Kamaraju, “FPGA Implementation of Emotion Recognition from Speech Signals”, International Journal on Electronics, Electrical and Computational Systems (IJEECS), vol.5, issue 7, ISSN: 2348-117X, July 2016, pp: 10-14. **Impact factor: 2.52**

9. B.Kranthi, Dr.B.Ananda Krishna, Dr.M.Kamaraju, **B.Rajasekhar** "Enhanced Image Compression Algorithm for Image Processing Applications" International Journal of Computer Science and Information Technologies(IJCSIT), Vol.5(2),ISSN:0975-9646, 2014, pp:1509-1515. **Impact factor: 2.94**
10. G.Nitin, **B.Rajasekhar** "Design of Low Band Frequency Amplifier using different RF Substrates" International Journal of Engg. Research & Technology (IJERT), vol.2, issue.11, ISSN: 2278-0181, Nov 2013, pp.4025-4029 **Impact factor: 1.78**
11. U.Siva Reddy, **B.Rajasekhar**, M.Kamaraju, B.Ananda Krishna "Novel Implementation of Employee Fingerprint Database Management System" at International Journal of Engineering & Science Research (IJESR), Vol.3, Issue.10, ISSN:2277-2685, October 2013, pp:4799-4808. **Impact factor:1.4. UGC Approved Journal.**
12. M.Kamaraju, P.Anil Kumar, B. Ananda Krishna, **B. Rajasekhar**, "Embedded Fingerprint Recognition System", Recent Researches in Telecommunications, Informatics, Electronics and Signal Processing, ISBN: 978-960-474-330-8, Sept 17-19, 2013 at Baltimore, MD, USA, pp.394-401
13. M.Kamaraju, B.Ananda Krishna, **B.Rajasekhar**, P.Anil Kumar, "A Novel Implementation of Fingerprint Recognition System using DSP Processor", International Journal of Engineering Research and Technology (IJERT), AAMT-2013, pp. 3-6, ISSN:2278-0181, March-2013. **Impact Factor:1.76**
14. B.Ananda Krishna, M.Kamaraju, **B.Rajasekhar**, B.Kranthi, "Enhanced SPIHT Algorithm for Image Compression", International Journal of Engineering Research and Technology (IJERT), AAMT-2013, pp. 15-18, ISSN:2278-0181, March-2013. **Impact Factor:1.76**
15. **B.Rajasekhar**, U.Siva Reddy, M.Kamaraju, B.Ananda Krishna, "Fingerprint Database Management System", International Journal of Engineering Research and Technology (IJERT), AAMT-2013, pp. 21-23, ISSN: 2278-0181, March-2013. **Impact Factor:1.76**
16. **B.Rajasekhar**, Md.Nayeemuddin, "Image Compression based on Lifting based DWT Technique using FPGA", International Journal of VLSI and Embedded Systems-IJVES, ISSN: 2249-6556, vol. 4, issue 01, pp.202-206, January-February 2013. **Impact Factor:1.97**
17. N.Ajeeshvali, **B.Rajasekhar**, "Steganography Based on Integer Wavelet Transform and Bicubic Interpolation", I.J. Image, Graphics and Signal Processing, 2012, Vol. 4, Issue 12, pp: 26-33, DOI: 10.5815/ijigsp.2012.12.04. **Impact Factor: 0.722**

Papers Published in Conferences (8)

1. **B. Rajasekhar**, M. Kamaraju, V. Sumalath, "FPGA Based Emotions Recognition from Speech Signals", IEEE sponsored 3rd International Conference on Biosignals, Images and Instrumentation organized by S.S.N. College of Engineering, Chennai during Mar 16-18, 2017.
2. N. Sowjanya, **B. Rajasekhar**, M. Kamaraju, "FPGA Implementation of Emotion Recognition from Speech Signals", International Conference on Signal, Image, Video and Audio Processing (ICSIVA-16), organized by Conference Info and Academic Science at Acharya Nagarjuna University, Guntur on July 30, 2016.
3. **B. Rajasekhar**, M. Kamaraju, V. Sumalath, "Gender Driven Emotion Recognition System for Speech Signals Using Neural Networks", 6th International Advanced

- Computing Conference (IACC 2016) organized by IEEE Computer Society – Indian Council at S.R.K.R. Engineering College, Bhimavaram during Feb 27-28, 2016.
4. G.Sreelatha, **B.Rajasekhar**, M.Kamaraju, “A Robust and Reliable Gender Driven Emotion Recognition System”, International Conference on Emerging Trends in Engineering and Technology, IFERP, Visakhapatnam, pp. 7-10, 30th August 2015.
 5. M.Kamaraju, B.Ananda Krishna, **B.Rajasekhar**, P.Anil Kumar, “A Novel Implementation of Fingerprint Recognition System using DSP Processor”, National Conference on Advance Assistive and Main Stream Technologies for Persons with Special Needs at Sri Vishnu Engineering College for Women, Bhimavaram, during 5-7, pp.1-4, March 2013.
 6. B.Ananda Krishna, M.Kamaraju, **B.Rajasekhar**, B.Kranthi, “Enhanced SPIHT Algorithm for Image Compression”, National Conference on Advance Assistive and Main Stream Technologies for Persons with Special Needs at Sri Vishnu Engineering College for Women, Bhimavaram, during 5-7, pp.1-4, March 2013.
 7. **B.Rajasekhar**, M.Kamaraju, B.Ananda Krishna, U.Siva Reddy, “Fingerprint Database Management System”, National Conference on Advance Assistive and Main Stream Technologies for Persons with Special Needs at Sri Vishnu Engineering College for Women, Bhimavaram, during 5-7, pp.1-4, March 2013.
 8. **B.Rajasekhar**, V.Sumalatha, “Stand alone motor control card for a missile”, National Conference on Emerging Trends in Electronics and Communication Engineering at JNTU College of Engineering, Anantapur, 23 June 2007.

Online FDP/Webinar/training Programme (10)

1. Online Workshop on “Intellectual Property Rights” organized by Pandit Deendayal Petroleum University (PDPU) and Intellectual Property Protection Organization (IPPO) on 10th & 11th April 2020.
2. Online Faculty Development Program on “Team Building and Managing” (Online Live FDP) conducted by ICT Academy on 30th Apr 2020 to 5th May 2020.
3. One week Online Faculty Development Programme (FDP) on "Trends in Advanced Communications by Department of Electronics and Communication Engineering, Lakireddy Bali Reddy College of Engineering Krishna (Dt.) during 4th – 9th May 2020.
4. One Week Online Faculty Development Program on “Research Challenges and Opportunities Post COVID-19 (RECOP 2020)” organized by Research & Development Cell and Institution’s Innovation Council of Sri Vasavi Engineering College (Autonomous) during 4th – 9th May 2020.
5. Five Day FDP on “IPR Awareness and Patent Prosecution” Organized by the Department of Information Technology, CMR Engineering College, Hyderabad from 6th-10th May 20.
6. One Week Online AICTE MARGDARSHAN FDP on “Art of Writing Papers and Research Methodologies” during 7th – 13th May 2020.
7. One Week Online Faculty Development Program on “Advanced Antenna Design Using HFSS” Organized by Department of ECE, Santhiram Engineering College, Nandyal in collaboration with Vardhaman College of Engineering, Hyderabad during 12th-17th May 20.
8. A National Level Five Day Faculty Development Programme on “OBE-NBA Process” organized by Internal Quality Assurance (IQAC), Narasaraopeta Engineering College (Autonomous), Narasaraopet, during 15th-19th May 20.

9. One Week Online Faculty Development Programme on “Patent Filing Procedure” conducted by the IPR Cell & IIC of Sri Ramakrishna College of Arts & Science, Coimbatore during 18th – 23rd May 20.
10. One Week Online Faculty Development Program on “Speech Processing Applications & Challenges” Organized by Department of ECE, Santhiram Engineering College, Nandyal in collaboration with International Institute of Information Technology, Hyderabad during 20th-25th May 20.

Workshops /Conferences /Seminars Attended (20)

1. One day National Convention on “Research Trends in Wireless Communication Technologies”, organized by PVPSIT Vijayawada in association with National Instruments India on 29th July 2017.
2. Two week ISTE STTP on “CMOS, Mixed Signal and Radio Frequency VLSI Design”, conducted by IIT Kharagpur in association with department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 30th January - 4th February 2017.
3. A Two day workshop on “Advances in Mathematical Models for Image Processing” organized by Prasad V Potluri Siddhartha Institute of Technology, Vijayawada during 4th – 5th March 2016.
4. A Two Day All India Workshop on “Emerging Techniques on VLSI Architecture Design for Real Time Environment” conducted by PACE Institute of Technology & Science, Ongole during 9th – 10th October 2015.
5. One day workshop on “Research in Energy Management Wireless Networks & Smart Transportation Systems” organized by The Institute of Engineers (India), Vijayawada Local Centre, Vijayawada on 29th March 2015.
6. One day workshop on “Research & Development Facilitation” organized by The Institute of Engineers (India), Vijayawada Local Centre, Vijayawada on 29th March 2015.
7. One day workshop on “Speech Processing: Current Challenges and Hands-on Experience” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru on 23rd August 2014.
8. Three day National Workshop on “Theory & Applications of Intelligent Signal Processing”, organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 28th February to 2nd March 2014.
9. Two week ISTE workshop on “Signals & Systems” conducted by IIT Kharagpur in association with department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 2nd -12th January 2014.
10. One Week ISTE workshop for Coordinators on “Analog Electronics” organized by IIT Kharagpur during 1st - 5th April 2013.
11. Complimentary Seminar on “MATLAB & Simulink for Engineering Education” organized by Math Works India Pvt. Ltd. at Vijayawada on 13th September 2012.
12. Two day National Workshop on “Simulation of Wireless & Mobile Networks using NetSim” organized by department of ECE, JNTUH College of Engineering, Hyderabad during 2nd – 3rd July 2012.
13. One week workshop on “Mission 10X” organized by Lakireddy Balireddy College of Engineering, Mylavaram during 18th – 22nd April 2011.

14. One day workshop on “Research Opportunities for Women & Young Scientists in Engineering, Science & Technology” organized by Lakireddy Balireddy College of Engineering, Mylavaram on 18th December 2010.
15. One day seminar on “Computing Freedom in Engineering Education – A Free Software Perspective” organized by department of CSE, Gudlavalleru Engineering College, Gudlavalleru on 11th December 2010.
16. AICTE sponsored 6-day workshop on “Digital Image Analysis & Processing” organized by department of ECE, GNITS, Hyderabad during 11th – 16th February 2008.
17. Two day National Seminar on “Semi-Custom VLSI Design – An FPGA Based Approach” organized by Vardhaman College of Engineering, Hyderabad during 5th – 6th November 2007.
18. Two day workshop on “Design of Sensor Based Embedded System Using 8051” organized by department of ECE, M.V.S.R. Engineering College, Hyderabad during 4th – 5th December 2006.
19. One week Curriculum Development Workshop on “Advances in Microprocessors” jointly organized by Intel, JNTU, Hyderabad and CVR college of Engineering, Hyderabad during 9th – 13th December 2002.
20. The 3-day statewide workshop on “Microcontrollers the Technology of Tomorrow” organized by department of Instrumentation Engineering, M.J. College of Engineering & Technology, Hyderabad during 15th – 17th November 2001.

Certifications/Training Programs Attended (21)

1. Two days training programme on "IPR & Technology Transfer" conducted as part of IIC Innovation Ambassador Training Series, Organized by Institution's Innovation Council of MHRD's Innovation Cell, AICTE held at MLR Institute of Technology, Hyderabad, Telangana during 6th -7th February 2020.
2. One week faculty development programme on “Data Acquisition and RT using LabVIEW”, organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru in association with National Instruments, Bangalore, during 25th - 30th November 2019.
3. Three day training programme on “Advanced Development Tools Based IoT”, organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 11th - 13th November 2019.
4. One week faculty development programme on “Recent Trends in Image & Video Processing”, organized by E&ICT Academy, NIT Warangal at V.R. Siddhartha Engineering College, Vijayawada, during 11th – 16th February 2019.
5. One week faculty development programme on “Machine Learning Applications in Signal Processing”, organized by E&ICT Academy, NIT Warangal at Gudlavalleru Engineering College, Gudlavalleru, during 4th – 9th February 2019.
6. Two days training programme on “Design and development of Digital Circuits using VLSI Tools”, organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 6th - 7th December 2018.
7. One week faculty development programme on “New Algorithms of Signal Processing Hands On”, organized by LBRCE Mylavaram during 20-27 April 2018.

8. Two day faculty training programme on “Advances in VLSI Design and Embedded Systems”, organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 6th - 7th November 2017.
9. One week Faculty Development Programme (FDP) on “Development of Wireless Sensor Networks Simulator (NS2) – Hands on Experience”, organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 1st - 5th December 2015.
10. One week refresher course on “Microprocessor & Microcontroller” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 17th – 21st November 2015.
11. Two day Faculty Development Programme (FDP) on “Advanced Digital Signal Processing & Applications (ADSP-A)”, organized by V.R. Siddhartha Engineering College, Vijayawada, under TEQUIP-II during 12th -13th September 2015.
12. One week orientation program on “Research Methodology”, organized by JNTUA, Ananthapuramu during 15th - 20th June 2015.
13. “Faculty Interaction Programme & Industrial Visit” organized by Efftronics Systems Pvt. Ltd., Vijayawada, on 28th August 2012.
14. Faculty Development Program on “VLSI and Embedded System Design” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 7th – 8th July 2012.
15. One week refresher course on “Pulse & Digital Circuits (Theory & Lab Practice)” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 21st – 26th November 2011.
16. One week training programme on “LABVIEW CORE I & II” organized by NI Systems India Pvt. Ltd., Bangalore during 25th – 29th April 2011.
17. One week course on “DSP Using LabView”, as part of Indo-US Engineering Faculty Leadership Institute at Shri Vishnu Engineering College for Women, Bhimavaram during 5th – 9th July 2010.
18. Intensive course on “Advances in Wireless Communication” organized by department of ECE, University College of Engineering, Osmania University, Hyderabad on 29th December 2007.
19. Two week intensive course on “Cell-Based ASIC Design Using Cadence Tools(Theory & Practice)” organized by department of ECE, CVR College of Engineering, Hyderabad during 26th November – 7th December 2007.
20. A 4-day course on “Digital Signal Processing and Applications” organized by IETE, Hyderabad Centre during 26th – 29th July 2007.
21. Short term course on “Embedded Systems” jointly organized by University College of Engineering, Hyderabad and Sreenidhi Institute of Science & Technology, Hyderabad during 21st – 25th November 2006.

Guest Lectures Delivered (3)

1. “Introduction to Wireless Sensor Networks” for faculty at Gudlavalleru Engineering College, Gudlavalleru on 9th March 2012.
2. “Simulation of Electronic Circuits using LabView” for IV B.Tech Students at Gudlavalleru Engineering College, Gudlavalleru on 3rd Feb 2012.

3. Delivered a lecture on the topic “Time Base Generators” as part of One week refresher course on “Pulse & Digital Circuits (Theory & Lab Practice)” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 21st – 26th November 2011.

Workshops Organized (14)

1. One week faculty development programme on “Data Acquisition and RT using LabVIEW”, organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru in association with National Instruments, Bangalore, during 25th - 30th November 2019.
2. Three days workshop on “Embedded System Design using Advanced TI Tools”, organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru under Texas Instruments University Program in association with EdGate Technologies Pvt. Ltd. Bangalore, during 14th – 16th Oct 2019.
3. One Week FDP on “Communications and Signal Processing”, organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 29th Oct – 3rd Nov 2018.
4. One Day Training Programme on “DSP Kits”, organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru on 12th July 2018.
5. Two day staff training programme on “MATLAB Tools for Engineers – Hands on Experience”, organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 10th - 11th January 2017.
6. One day workshop on “Embedded System Design for Wireless Sensor Networks Applications” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru on 7th January 2015.
7. One day workshop on “Speech Processing: Current Challenges and Hands-on Experience” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru on 23rd August 2014.
8. One week faculty refresher course on “Signals & Systems” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 9th – 13th June 2014.
9. Two day workshop on “Embedded System Development”, organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 30th - 31st December 2013.
10. Two day workshop on “Issues in Integrated Circuits (ICs) Design” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 13th – 14th December 2013.
11. Two weeks ISTE workshop on “Analog Electronics” organized by IIT Kharagpur in association with department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 4th -14th June 2013.
12. “YODHA Technical Project Expo & Innovative Models Expo” for B.Tech students organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru on 1st February 2013.
13. “Applications of Electronics.... a Technical Expo” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru on 24th September 2012.
14. A Two day workshop on “Digital Image Processing & Lab Practice for IV B.Tech. Students” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 27th – 28th July 2012.

R&D and Consultancy

S. No	Project Title	Source of Funding	Duration	Role	Status
1.	Wireless Attendance Monitoring System	Internal R&D	One Year	Co-Investigator	Completed

Professional Body Membership

S. No.	Name of the Professional body	Membership Type & Number
1.	Institute of Electrical and Electronics Engineers	Member - 92166361
2.	The Institute of Engineers (India)	M-150432-5
3.	The Institute of Electronics & Telecommunication Engineers	AM-198489

Subjects Handled

Under Graduate Level

- Digital Signal Processing
- Digital Image Processing
- Signals & Systems
- Digital Circuit Design
- Linear IC Applications
- Computer Networks
- Electronic Devices & Circuits
- Pulse & Digital Circuits
- Microprocessor & Microcontrollers
- Data Communications
- Control Systems
- Computer Organization

Post Graduate Level

- Advanced Digital Signal Processing
- DSP Processors and Architectures
- Wireless Communications
- Probability & Random Variables

Laboratories Handled

Under Graduate Level

- Digital Circuit Design Lab
- Digital Signal Processing Lab
- Linear IC Applications Lab
- Electronic Devices & Circuits Lab
- Pulse & Digital Circuits Lab
- Interfacing through Microprocessor Lab

Post Graduate Level

- Advanced Digital Signal Processing Lab

Interaction with outside world

1. Executive Committee member, IETE Vijayawada local centre, Vijayawada during 2018 – 2020 and 2020 - 2022.
2. Executive Committee member, Institute of Engineers (India), Andhra Pradesh State Centre, Vijayawada during 2016 - 2018.
3. Executive Committee member, Institute of Engineers (India), IE Vijayawada local centre during 2014 – 2016.
4. Executive Committee member, IETE Vijayawada local centre, Vijayawada during 2014 – 2016.
5. Acted as BoS member for department of ECE, Gudlavalleru Engineering College, Gudlavalleru.
6. Acted as Judge for “A Student Technical Paper Contest – 2014” conducted by IETE Vijayawada Centre on 2nd February 2014.
7. Co-opted member, IETE Vijayawada Centre, Vijayawada during 2012 - 2014.

Value Offered:

With the teaching experience gained, I promise you Sir, that I will work with dedication, commitment and fulfill the responsibilities given to me by your expectations.

Date:**Place: Vijayawada****RAJASEKHAR BUTTA**