

# ELECTRONICS & ICT ACADEMY



## NATIONAL INSTITUTE OF TECHNOLOGY

WARANGAL – 506 004 (Telangana State)

DeitY, HRD division, Ministry of ICT, Govt. of India SPONSORED  
Faculty Development Programme (FDP)

On

Advanced Digital Signal Processing and Applications

Jointly Organized by

Electronics & ICT Academy, NIT Warangal  
and Department of ECE, Gudlavalleru Engineering College, Gudlavalleru  
(27<sup>th</sup> July 2016– 5<sup>th</sup> Aug 2016)



### Preamble:

“Electronics & ICT Academy” is set up at NIT Warangal with financial assistance from DeitY, MCIT, Gol. The role of academy is to offer faculty development programs in emerging areas of Electronics, Information & Communication Technologies; training & consultancy services for Industry; Curriculum development for Industry; CEP for working professionals; Advice and support for technical incubation and entrepreneurial activities. The jurisdiction of this academy is Telangana, Andhra Pradesh, Karnataka, Pondicherry and Goa and Andaman & Nicobar islands. Under the umbrella of this academy, a standardized course namely ‘Advanced Digital signal processing and Applications’ is proposed to be held at GEC, Gudlavalleru.

Advanced digital signal processing is the discipline where no. of algorithms are developed by emulating certain characteristics of biological systems and being fueled by recent advancements in computing technology and is emerging as a technology that may open avenues for significant technological advances. The characteristics include adaptation and learning, planning under large uncertainty and coping with large amounts of data. Today, the area of ADSP tends to encompass everything that is not characterized as in conventional methods. ADSP techniques are a key enabling technology for Medical and Industrial applications. This course focuses on recent developments and research outcomes addressing the related theoretical and practical aspects on ADSP in medical and industrial applications of engineering.

### Major Course Contents:

- Signal processing and Applications
- Transform Techniques
- Spectral Estimation and HOSA
- Adaptive Signal Processing
- Multirate Signal processing
- Speech Signal processing
- Image processing
- Radar signal processing
- Dsp Architectures for communications
- Hands on Sessions on above topics
- Lab Sessions using MATLAB Tools for the above content.

### Faculty conducting this programme:

The programme will be conducted by the faculty members of **NIT Warangal**. Academicians in the concerned field from **IITs/NITs** are invited to deliver lectures in the program. Speakers from **industries** are also expected as part of the course.

**Eligibility:** The programme is open to the **teachers of engineering colleges from Electronics and communication Engineering, Electrical Engineering, and other allied disciplines** in Telangana, Andhra Pradesh, Karnataka states and Goa. Industry personnel working in the concerned/allied discipline can also attend.

### Registration Fee Particulars:

Faculty members: Rs 2000/-

Industry participants: Rs 6000/-

The participants need to send a crossed demand draft (DD) for an amount of Rs. 2,000/- (Rs.6,000/-) drawn in favor of “Director, NIT Warangal” and payable at SBH, NIT Warangal branch.

### Accommodation:

All the selected participants will be provided FREE boarding & lodging in the college hostels at Gudlavalleru. No TA will be paid for the participants.

### How to apply:

A filled in form of application in the prescribed format duly signed and sponsored by appropriate authorities (along with demand draft) should reach the coordinator by speed-post. It is also mandatory to send scanned application form and demand draft through e-mail.

### Selection Criterion:

Selection will be done based on **first-cum-first-serve basis** and the confirmed candidates will be notified immediately. The maximum number of participants will be **50 (fifty)**. Additionally 10 participants from industry are allowed to participate. The list of selected participants will be notified in the institute web site [www.nitw.ac.in](http://www.nitw.ac.in) / [www.gecgudlavalleru.ac.in](http://www.gecgudlavalleru.ac.in). In case a candidate is not selected, the demand draft will be returned. **A test will be conducted at the end of the course. Candidates will be issued certificates on successful completion of the course along with grade awarded for the test.** Reservations are followed for selecting candidates as per GOI norms.

**Venue: Gudlavalleru Engineering, College, Gudlavalleru.**

### Important dates:

Last date for submission of application: 20.07.2016

Selection intimation by email: 23.07.2016

Duration of Program: 27<sup>th</sup> July 2016– 5<sup>th</sup> Aug 2016

### About NITW:

National Institute of Technology (formerly Regional Engineering College), Warangal is the first among 17 RECs setup as joint venture of the Government of India and the state government. Over the years the college has established itself as a premier Institution imparting technical education of a very high standard leading to the B.Tech degrees in various branches of engineering and M.Tech. and Ph.D. programs in various specializations.

### About GEC :

The Gudlavalleru Engineering College was established in 1998 by A.A.N.M. & V.V.R.S.R. Educational Society, Gudlavalleru. The college is autonomous under JNTUK and offers B.Tech. programme in six branches of Engineering, viz., Civil, EEE, ME, ECE, CSE & IT., and PG courses in MBA, M.Tech.(DECS), M.Tech. (ES), M.Tech (CSE), M.Tech (CS), M.Tech (PEED), M.Tech (SE) and M.Tech (MD). All the eligible UG engineering programmes have been accredited by National Board of Accreditation (NBA) of AICTE, New Delhi and by NAAC. The college is also accredited by TCS.

**Department of ECE:** The Department of ECE is recognized as Research Center by JNTUK-Kakinada in the year 2014. The department has well qualified staff specialized in microelectronics, embedded systems, signal processing and VLSI Design.

## FORMAT OF APPLICATION

### Electronics and ICT Academy Faculty Development Programme

on

Advanced Digital signal processing and Applications

(27<sup>th</sup> July 2016– 5<sup>th</sup> Aug 2016 )

1. Name:
2. Designation:
3. Institution:
4. E-mail:
5. DD No:                      Bank:                      Date:
6. Address for Correspondence
7. Educational Qualifications with specialization:
8. Subjects taught so far:
9. No. of refresher courses/workshops attended:
10. Experience (in years)  
Teaching:  
Research:  
Industry:
11. Accommodation required: YES / NO
12. Do you belong to SC/ST: YES/NO

#### Declaration

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

Place:

Date:

Signature of the applicant

#### **SPONSORSHIP CERTIFICATE**

Dr./Mr./Ms. ....

..... is an employee of our Institute/Organization and is hereby sponsored to participate in the FDP on signal processing for communications sponsored by **Electronics & ICT Academy** during 27<sup>th</sup> July 2016– 5<sup>th</sup> Aug 2016 at National Institute of Technology, Warangal

Place:

Date:

Signature of Head of Institution  
(with seal)

#### **Address for correspondence**

*Post your application form with DD to*

Dr.T.Kishore Kumar  
Dept. of Electronics and Communication Engg,  
National Institute of Technology Warangal,  
WARANGAL - 506 004, Telangana State, India.

*Mail the scanned copies of filled-in and duly signed application form with DD to*

[kishorefr@gmail.com](mailto:kishorefr@gmail.com)

OR

M.V.Srikanth, Asst.Professor,Dept. of ECE,  
Gudlavalleru Engineering College, Gudlavalleru-521356  
Mail id: [sree.02476@gmail.com](mailto:sree.02476@gmail.com), [ece\\_gec@yahoo.co.in](mailto:ece_gec@yahoo.co.in)  
For more information visit: <http://nitw.ac.in/eict/>

For any enquiry contact:

Mobile: 8332969353 or 7702191922

**Co-Coordinator**  
**Mr. M.V.Srikanth**

**Coordinator**  
**Dr.T.Kishore Kumar,**

**Jointly Organized by**  
**Electronics & ICT Academy, NIT Warangal &**  
**Department of ECE,Gudlavalleru Engineering College, Gudlavalleru-521356.**