

THOTA. VENKATA LAKSHMI

Department: Electronics and Communication Engineering

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ACADEMIC QUALIFICATIONS:

Master of Technology in Instrumentation and Control Systems.

Bachelor of Technology in Electronics and Communication Engineering.

ACADEMIC REWARDS / ACHIEVEMENTS:

NIL

PROFESSIONAL EXPERIENCE

Designation	Institution	Working Period	
		From	To
Associate Professor	Gudlavalleru Engineering College, Gudlavalleru	Feb 2009	Till date
Assistant Professor	Gudlavalleru Engineering College, Gudlavalleru	August 2003	January 2009

PROJECTS GUIDED:

PhD	Post Graduation	Under Graduation
NIL	10	19

SPECIALIZATION: LOW POWER VLSI DESIGN

RESEARCH PAPERS / BOOKS: NIL

BOOKS PUBLISHED IN PUBLICATIONS:

1. Co-author of book entitled “Electronics and Communication Engineering laboratory Manual” with ISBN – 978-1-4276-5536-3.
2. Co-author of book entitled “Electronics and Communication Engineering Instructional Material” with ISBN – 978-1-4276-5539-4.

Papers Published in Journals: 09

1. Pallavi Sesham, Venkatalakshmi Thota, Kamaraju M, “Design Of Kogge-Stone Adder Using Adiabatic Logic”, International Journal of Advances in Electronics and Computer Science, ISSN: 2393-2835 Volume-5, Issue-6, Jun.-2018.
2. S.Pallavi, T.Venkata Lakshmi, M. Kamaraju, “Implementation and comparative analysis of Carry Select Adder using Kogge-Stone Adder in Adiabatic logic” Journal of Engineering Research and Application, UGC approved Journal Research article, ISSN : 2248-9622, Vol. 8, Issue 6 (Part - III) June 2018, pp 22-26.
3. G.Rajesh,T.Venkata Lakshmi, M.Kamaraju,“Low Power VLSI implementation of error detection and correction codes using OLS codes” International Journal of Computer Science and Information Technology & Security (IJCSITS), ISSN: 2249-9555 Vol.6, No.4, July-August 2016.Impact Factor:3.765
4. V. Sreelakshmi, **T. Venkata Lakshmi**, M. Kamaraju “A Low Power delay buffer using Ring counter and Gated driver” International Journal of Engineering & Science Research (IJESR), Vol-5, Issue-12, Dec. 2015 ISSN 2277-2685, pp: 1447-1455.
5. A. Haritha, Y. Bhavani Prasad, M. Kamaraju, **T. Venkata Lakshmi** “Analysis of Memristors with and without Temperature Effects” International Journal of Applied Engineering Research (IJAER), Volume-10, number-20, ISSN: 0973-4562, 2015, pp: 41464-41470.
6. I. Srivalli, **T. Venkata Lakshmi**, M. Kamaraju “ Design of an Efficient Router in Networks” International Journal of Engineering in Advanced Research Science and Technology (IJEARST), Volume-01, issue-01, October 2015.
7. Sravanthi, **T. Venkata Lakshmi** “VLSI Implimentation of ALU using Reversible Logic with Vedic Mathematics” International Journal of Scientific Engineering and Technology Research (IJSETR), Volume. 04, Issue. 01, ISSN: 2319-8885, January-2015, pp: 0142-0146.

8. P.V. SubbaRao, **T. Venkata Lakshmi**, M. Kamaraju, “A Novel Voting System Using SMS” International Journal of Computer Science & Information Technology (CS & IT) (DOI: 10.5121/csit.2013.3634), August 2013, PP: 297-303. Impact factor: 2.93
9. J. Tulasi, **T. Venkata Lakshmi**, M. Kamaraju, “FPGA Implementation of Convolution Encoder and Hard Decision Viterbi Decoder” International Journal of Computer Communication Technology (IJCCT), Volume-3, Issue-4, ISSN (online): 2231-0371, 0975-7449, 2012.

Papers Published in Conferences: 08

1. Ch.S.Daya Adarsh, **T.Venkata Lakshmi**, M.Kamaraju “Implementation and Comparative Analysis of Double Gate Low Power Multiplexers Using Dynamic Logic Styles”, International Conference on Electronics, Communication and Aerospace Technology, ICECA 2017, IEEE Xplorer April 2018, DOI: 10.1109/ICECA.2017.8212825
2. Pallavi Sesham, **Venkatalakshmi Thota**, Kamaraju M, “Design Of Kogge-Stone Adder Using Adiabatic Logic”, National Conference on Advances in Electrical, Electronics & Computer Engineering (NCAEECE) in association with IRAJ Research Forum and Institute of Research and journals (IRAJ), April 2018.
3. **T. Venkata Lakshmi**, R. Trinadh, “A novel design of Area and Power Efficient high speed data path logic system”, International conference on Systemics, Cybernetics and Informatics (ICSCI-2014), under the aegis of Pentagon Research Centre, India in association with IEEE Hyderabad section during 3rd-6th February 2014, pp:75-78.
4. **T. Venkata Lakshmi**, M. Kamaraju, P.V. Subba Rao “A Novel Implementation of Voting System using SMS”, International Conference on Signal, Image Processing and Pattern Recognition (SIPP-2013) at Bangalore during February 18-19, 2013.
5. M. V. Phani Kumar, **T. Venkata Lakshmi**, “Impulse noise removal from color image sequences using Fuzzy Filter”, of International conference on Computer Science and Information Technology (CSIT-2012) at Bhopal on 3rd February 2012, pp-41-47.
6. Jayaprakash.P, A.Mallaiah, **T. Venkata Lakshmi**, “VLSI Implementation for Basic ARPS Algorithm for Video Compression”, International conference on Technology and Innovation (ICTI-2011), Chennai on 26th July 2011.

7. R. Trinadh, **T. Venkata Lakshmi**, “Enhanced Feature sets for face recognition under difficult lighting conditions”, International conference on Nanoscience, Engineering & Advanced Computing (ICNEAC-2011), pp: 64-69 at Narsaraopet during July 8-10, 2011.
8. Ravi Kumar K, **T. Venkata Lakshmi**, “Implementation of Image fusion algorithm based on wavelet transform and curvelet transform for improved quality factors”, International Conference on Nanoscience Engineering & Advanced computing (ICNEAC-2011) at Narsaraopet during July 8-10, 2011.

Workshops Organized: 02

1. One week ISTE STTP on “CMOS, Mixed Signal and Radio Frequency VLSI Design”, organized by IIT Kharagpur under the MHRD from 30th January-4th February, 2017.
2. Two day training programme on “**Advances in VLSI Design and Embedded Systems**” organized by VLSI & Embedded Systems Research group, department of ECE,

Workshops /Training Programs/Seminars Attended: 30

1. Two day training programme on “**Advances in VLSI Design and Embedded Systems**” organized by VLSI & Embedded Systems Research group, department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 31st August and 1st September, 2017.
2. Short course on “**Modeling and Simulation of Nano-Transistors**” organized by Department of Electrical Engineering, IIT Kanpur during 13th -17th February, 2017.
3. One week National Level FDP on “**Signal and Image Processing using LabVIEW**” during 13th -18th November, 2017 organized by Signal Processing Research group of department of ECE, Gudlavalleru Engineering College, Gudlavalleru in association with NI Systems Pvt. Ltd.
4. A one week faculty development programme on “**Hands on experience in Signal Processing Applications**” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru from 7th -12th November, 2016.
5. A one week faculty development programme on “**Development of Wireless Sensor Networks using Network Simulator (NS2)- Hands on experience**” organized by Communications Research group, department of ECE, Gudlavalleru Engineering College, Gudlavalleru in association with Vertex Soft, Hyderabad from 1st-5th December, 2015.
6. One week ISTE STTP for co-ordinators on “**CMOS, Mixed Signal and Radio Frequency VLSI Design**” conducted by IIT Kharagpur under the MHRD from 19th-23rd September, 2016.

7. Thirty second National Convention of Electronics and Telecommunication Engineers and National seminar on **“Intelligent System for More Connected World: Internet of Things”** organized by The Institution of Engineers (India), Andhra Pradesh State Centre during 24th -25th October, 2016.
8. A one week faculty development programme on **“Development of Wireless Sensor Networks using Network Simulator (NS2)- Hands on experience”** organized by Communications Research group, department of ECE, Gudlavalleru Engineering College, Gudlavalleru in association with Vertex Soft, Hyderabad from 1st-5th December, 2015.
9. One week refresher course on **“Microprocessors & Microcontrollers”** organized by Research & Development center, department of ECE, Gudlavalleru Engineering College, Gudlavalleru, during 17-21 November, 2015.
10. A five day continuing education and training programme on **“Low Power System on Chip Design using XILINX FPGA Devices”** organized by the department of ECE, NIT Warangal during 3rd -7th October, 2015.
11. A two day All India workshop on **“Emerging Techniques on VLSI Architecture Design for real time environment”** conducted by department of ECE, PACE institute of technology & sciences, Ongole organized by The Institution of Engineers (India), Vijayawada Local Centre during 9th -10th October, 2015.
12. One day workshop on **“Research & Development Facilitation”** organized by The Institute of Engineers (India), Vijayawada Local Centre, Vijayawada on 29th March, 2015.
13. A two day All India Seminar on **“Agricultural and Horticultural Applications of Unmanned Air Vehicles for Micro-Farming”** organized by The Institute of Engineers (India)-Vijayawada Local Centre along with Shri Vishnu Engineering College for women, Bhimavaram and National Design and Research Forum, Bangalore during 26th - 27th March, 2015.
14. A three day National workshop on **“Theory & Applications of Intelligent Signal Processing”** organized by Signal Processing & Communications group, department of ECE, Gudlavalleru Engineering College, Gudlavalleru on 28th February-2nd March, 2015.
15. A five day Short term course on **“Advanced DSP Design Techniques”** organized by IIT Kharagpur under Continuing Education Programme during July 6th -10th, 2015.
16. Two week audit course on **“Research Methodology”**, organized by JNTUK, Kakinada during 1st - 15th May 2015.

17. Two week audit course on “**Intellectual Property Rights and Patents**”, organized by JNTUK, Kakinada during 16th - 30th May 2015.
18. A one day workshop on “**Ultra Low Power Communication using CC430 Microcontroller::Hands on Experience**” organized by Signal Processing & Communications group, department of ECE, Gudlavalleru Engineering College, Gudlavalleru on 7th February, 2015.
19. A one day seminar on “**Research in signal and image processing on FPGA & Embedded platforms**” organized by The Institute of Engineers (India), Vijayawada Local Centre, Vijayawada on 22nd November 2014.
20. The value added programme on “**Digital IC Design using CADENCE Tools**” organized by the school of Electronics Engineering, VIT Chennai from 21st -23rd August, 2014.
21. One day workshop on “**Speech Processing: Current Challenges and Hands-on Experience**” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru on 23rd August 2014.
22. Two week ISTE workshop on “**Signals & Systems**” conducted by IIT Kharagpur in association with department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 2nd -12th January 2014.
23. A two day workshop on “**Issues In Integrated Circuits (ICs) Design**” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 13th -14th December, 2013.
24. A two day ISTE workshop on “**Aakash for Education**” conducted by IIT Bombay under the National Mission on Education through ICT (MHRD) on 10th &11th November, 2012.
25. Faculty Development Program on “**VLSI and Embedded System Design**” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 7th – 8th July 2012.
26. One week refresher course on “**Pulse & Digital Circuits (Theory & Lab Practice)**” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 21st – 26th November 2011.
27. A five day Training Programme on “**Mentor Graphics (Frontend & Backend) and XILINX Tools**” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru in association with IETE Vijayawada Centre & IETE Students’ Forum (ISF-GEC) and CoreEL Technologies, Bangalore from 13th – 17th September 2010.

28. A two week AICTE sponsored Staff Development Program on “**Current Trends in Biometrics**” organized by the department of IT, Gudlavalleru Engineering College, Gudlavalleru during 2nd -14th November, 2009.
29. A two day workshop on “**Verilog Programming & Implementation**” organized by department of ECE and IEEE Student branch of Gudlavalleru Engineering College, Gudlavalleru during 27th -28th October, 2007.
30. A two day workshop on “**VLSI Design**” organized by department of ECE and IEEE Student branch of Gudlavalleru Engineering College, Gudlavalleru during 9th -10th February, 2007.

CERTIFICATIONS:

1. Successfully completed the course “**Digital Electronic Circuits**” during January-April 2019.

GUEST LECTURES DELIVERED: 3

1. Delivered a talk on “**Over View on Blooms Taxonomy**” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru under Academic Strengthening and Advancement on 15th March, 2018.
2. Delivered CLT classes on “**VLSI Design**” for III B.Tech II sem ECE students during the academic year 2016-17.
3. Delivered a lecture on “**Multivibrators**” as part of One week refresher course on “Pulse & Digital Circuits (Theory & Lab Practice)” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 21st – 26th November 2011.

MEMBERSHIP IN PROFESSIONAL BODIES: MIE, LM- ISTE

CONSULTANCY ACTIVITIES: NIL

GRANTS FETCHED: NIL